

PATENT ASSIGNMENT

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Oki Electric Industry Co., Ltd.	05/13/2008

RECEIVING PARTY DATA

Name:	Ducci Enterprises IT, L.L.C.
Street Address:	1209 Orange Street
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19801

PROPERTY NUMBERS Total: 15

Property Type	Number
Application Number:	07297668
Application Number:	07382316
Application Number:	07697701
Application Number:	07808606
Application Number:	07872322
Application Number:	08494932
Application Number:	08508045
Application Number:	08564657
Application Number:	08665462
Application Number:	08978482
Application Number:	08958402
Application Number:	09113297
Application Number:	09247934
Application Number:	09316116
Application Number:	10026820

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PATENT

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REEL: 021105 FRAME: 0594

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DEIT-GEN

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Total Attachments: 5

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Oki Electric Industry Co., Ltd., a Japanese company having offices at 1-7-12 Toranomon, Minato-ku, Tokyo, Japan ("**Assignor**"), does hereby sell, assign, transfer, and convey unto Ducci Enterprises IT, L.L.C., a Delaware limited liability company, with an address at 1209 Orange Street, Wilmington, DE 19801 ("**Assignee**"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "**Patent Rights**"):

- (a) the patents listed in the table below (the "**Patents**");
- (b) all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, and/or (ii) for which any of the Patents directly or indirectly forms a basis for priority;
- (c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);
- (d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;
- (e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;
- (f) all inventions, invention disclosures, and discoveries described in any item in any of the Patents and/or any of the foregoing categories (b) through (e);
- (g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;
- (h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (i) damages,
- (ii) injunctive relief, and
- (iii) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h);

<u>Patent or Application</u> <u>No.</u>	<u>Country</u>	<u>Issue Date/</u> <u>(Filing Date)</u>	<u>Title of Patent and Inventors</u>
4,897,832 (07/297668)	US	1/30/1990 (1/17/1989)	Digital speech interpolation system and speech detector Suzuki, Takao; Shiraki, Yuichi; Noguchi, Osamu; Shoji, Yasuo
5,068,784 (07/382316)	US	11/26/1991 (7/20/1989)	Method and system for monitoring the number of available buffers Kishino, Kuniaki; Nakamura, Tamiya
5,164,962 (07/697701)	US	11/17/1992 (5/9/1991)	Adaptive equalizer with midburst correction capability Nakai, Toshihisa; Kondoh, Noriaki
5,214,671 (07/808,606)	US	5/25/1993 (12/17/1991)	Adaptive equalizer Nakai, Toshihisa
5,247,541 (07/872,322)	US	9/21/1993 (4/23/1992)	Automatic equalizer for a data transmission channel Nakai, Toshihisa
5,570,382 (08/494932)	US	10/29/1996 (6/26/1995)	Clock signal fault detector Miyamoto, Katsuhiko
GB2292055 (GB9515086.8)	GB	11/24/1998 (7/21/1995)	Clock recovery circuit employing delay-and-difference circuit and trajectory classification NAKAMURA SEIZO

<u>Patent or Application No.</u>	<u>Country</u>	<u>Issue Date/ (Filing Date)</u>	<u>Title of Patent and Inventors</u>
5,703,914 (08/508045)	US	12/30/1997 (7/27/1995)	Clock recovery circuit employing delay-and-difference circuit and pulse-sequence detection Nakamura, Seizo
JP3233801 (JP06-0306007)	JP	9/21/2001 (12/9/1994)	Bit-phase aligning circuit Taya, Takashi; Yoshida, Akira; Yamaoka, Shinsuke; Matsumoto, Shuichi
5,778,214 (08/564657)	US	7/7/1998 (11/29/1995)	Bit-phase aligning circuit Taya, Takashi; Yoshida, Akira; Yamaoka, Shinsuke; Matsumoto, Shuichi
5,822,327 (08/665462)	US	10/13/1998 (6/18/1996)	Device and method for converting a transmission rate by adding dummy data Satou, Atsuhiko
5,865,630 (08/978482)	US	2/2/1999 (11/25/1997)	Connection pin Nakano, Azuma
JP3437045 (JP08-0289703)	JP	6/6/2003 (10/31/1996)	Content addressable memory device Takahashi, Noriaki; Odagiri, Hideaki; Takeshita, Koji; Uyama, Yuji
5,905,668 (08/958402)	US	5/18/1999 (10/29/1997)	Content addressable memory device Takahashi, Noriaki; Odagiri, Hideaki; Takeshita, Koji; Uyama, Yuji
6,272,397 (09/113297)	US	8/7/2001 (7/10/1998)	Orthogonal type three-axis robot and a control method thereof Yamada, Masashi; Abe, Takashi; Amano, Taro

<u>Patent or Application No.</u>	<u>Country</u>	<u>Issue Date/ (Filing Date)</u>	<u>Title of Patent and Inventors</u>
ZL99101789.7 (CN99101789.7)	CN	8/27/2003 (2/10/1999)	Switch board for executing connections among multi terminals via multi communication channels MASAYASU NAKAMURA
6,295,349 (09/247934)	US	9/25/2001 (2/11/1999)	Exchange Nakamura, Masayasu
6,639,984 (09/316116)	US	10/28/2003 (5/20/1999)	Feeder circuit Taya, Takashi; Sekiguchi, Masaru; Kamio, Masao; Abe, Shigeo
JP13-0073337	JP	(3/15/2001)	CACHE MEMORY ENDO OSAMU
6,763,422 (10/026820)	US	7/13/2004 (12/27/2001)	Cache memory capable of reducing area occupied by data memory macro units Endo, Osamu

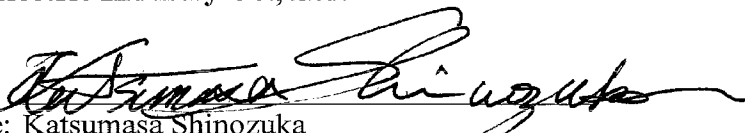
Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Tokyo, Japan on May 13, 2008.

ASSIGNOR:

Oki Electric Industry Co., Ltd.

By: 
Name: Katsumasa Shinozuka
Title: President and Chief Executive Officer
(Signature *MUST* be attested)

ATTESTATION

The undersigned witnessed the signature of Katsumasa Shinozuka to the above Assignment of Patent Rights on behalf of Assignor and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Katsumasa Shinozuka is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on May 13, 2008, to execute the above Assignment of Patent Rights on behalf of Assignor.
3. Katsumasa Shinozuka subscribed to the above Assignment of Patent Rights on behalf of Assignor.

I declare under penalty of perjury under the laws of the United States of America that the foregoing is true and correct.

EXECUTED on May 13, 2008


Print Name: Kenichi Tamura