

**PATENT ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
Invarium, Inc.	06/04/2008
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	Cadence Design Systems, Inc.
<b>Street Address:</b>	2655 Seely Avenue
<b>City:</b>	San Jose
<b>State/Country:</b>	CALIFORNIA
<b>Postal Code:</b>	95134-1931
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	10955067
<b>CORRESPONDENCE DATA</b>	
<b>Fax Number:</b>	(858)509-3691
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
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<b>Correspondent Name:</b>	SHEPPARD MULLIN RICHTER & HAMPTON LLP
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<b>Address Line 4:</b>	Los Angeles, CALIFORNIA 90071-1448
<b>ATTORNEY DOCKET NUMBER:</b>	041Y-127088
<b>NAME OF SUBMITTER:</b>	Linda M. de la Garza

CH \$40.00 10955067

**Total Attachments: 6**  
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**PATENT**



## ASSIGNMENT

WHEREAS, Invarium, Inc., (Assignor) is the owner of the patents, patent applications, and design registrations and other properties listed in Exhibit A attached hereto, and any and all Letters Patent or Patents in the United States of America and all foreign countries that may be granted therefor and thereon, and in and to any and all divisions, continuations and continuations-in-part of said applications, or reissues or extensions of said Letters Patent or Patents, and all rights under the International Convention for the Protection of Industrial Property, (the "Properties"); and

WHEREAS, Cadence Design Systems, Inc. (Assignee), desires to acquire by formal, recordable assignment the entire right, title and interest in and to the Properties;

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, said Assignor has sold, assigned, transferred and set over, and hereby does sell, assign, transfer and set over, unto said Assignee, its successors, legal representatives and assigns, the entire right, title and interest in and to the Properties, the same to be held and enjoyed by said Assignee, for its own use and the use of its successors, legal representatives and assigns, to the full end of the term or terms for which Letters Patent or Patents may be granted, as fully and entirely as the same would have been held and enjoyed by the Assignor, had this sale and assignment not been made.

AND for the same consideration, said Assignor hereby covenants and agrees to and with said Assignee its successors, legal representatives and assigns, that said Assignor will, whenever counsel of said Assignee, or the counsel of its successors, legal representatives and assigns, shall advise that any proceeding in connection with said inventions, or said application

for Letters Patent, or any proceeding in connection with Letters Patent for said inventions in any country, including interference proceedings, is lawful and desirable, or that any division, continuation or continuation-in-part of any application for Letters Patent or any reissue or extension of any Letters Patent, to be obtained thereon, is lawful and desirable, sign all papers and documents, take all lawful oaths, and do all acts necessary or required to be done for the procurement, maintenance, enforcement and defense of Letters Patent for said inventions, without charge to said Assignee, its successors, legal representatives and assigns, but at the cost and expense of said Assignee, its successors, legal representatives and assigns.

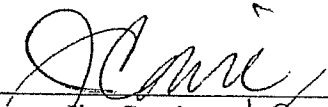
AND for the same consideration, said Assignor hereby covenants and agrees to and with said Assignee its successors, legal representatives and assigns, that, at the time of execution and delivery of these presents, said Assignor is the sole and lawful owner of the entire right, title and interest in and to the Properties, and that the same are unencumbered and that said Assignor has good and full right and lawful authority to sell and convey the same in the manner herein set forth. Assignor further represents and warrants that it has not granted and will not grant to others any rights inconsistent with the rights granted by this Assignment.

Assignor authorizes and requests the Commissioner of Patents and Trademarks of the United States and of all foreign countries to issue any Letters Patent granted for the

Properties, whether on applications or on any subsequently filed division, continuation, continuation-in-part or reissue applications, to Assignee, its successors and assigns, as the assignee of the entire interest in the invention.

Assignor:

INVARIUM, INC.

  
By: Jim Cowie  
Its: Vice President & Assistant Secretary

Date: \_\_\_\_\_

**EXHIBIT A**

**ATTACHMENT TO STATEMENT UNDER 37 CFR § 3.73(b)**

**Attorney Docket No. 041Y-127088**

**Assignor: Invarium, Inc.**

**Assignee: Cadence Design Systems, Inc.**

<b>Application Serial No. / Filing Date</b>	<b>Patent No. / Issue Date</b>	<b>Title</b>	<b>Assignment Reel/Frame Recorded Date</b>
10/955,527 Sept. 30, 2004	7,337,421 Feb. 26, 2008	METHOD AND SYSTEM FOR MANAGING DESIGN CORRECTIONS FOR OPTICAL AND PROCESS EFFECTS BASED ON FEATURE TOLERANCES	015859/0771  09/30/2004
10/933,192 Sept. 1, 2004	7,246,343 July 17, 2007	METHOD FOR CORRECTING POSITION-DEPENDENT DISTORTIONS IN PATTERNING OF INTEGRATED CIRCUITS	015769/0343  09/01/2004
10/955,067 Sept. 30, 2004	--	METHOD AND SYSTEM FOR SEMICONDUCTOR DESIGN HIERARCHY ANALYSIS AND TRANSFORMATION	015867/0400  09/30/2004
11/021,783 Dec. 23, 2004	--	METHOD AND SYSTEM FOR RETICLE- WIDE HIERARCHY MANAGEMENT FOR REPRESENTATIONAL AND COMPUTATIONAL REUSE IN INTEGRATED CIRCUIT LAYOUT DESIGN	016125/0776  12/23/2004
11/203,505 Aug. 13, 2005	--	METHOD AND APPARATUS OF MODEL-BASED PHOTOMASK SYNTHESIS	018396/0437  10/16/2006
11/203,329 Aug. 13, 2005	--	APPARATUS AND METHOD FOR COMPENSATING A LITHOGRAPHY PROJECTION TOOL	019491/0483  06/27/2007
11/203,330 Aug. 13, 2005	--	APPARATUS AND METHOD FOR PHOTOMASK DESIGN	017064/0504  11/25/2005
11/203,498 Aug. 13, 2005	--	APPARATUS AND METHOD FOR SEGMENTING EDGES FOR OPTICAL PROXIMITY CORRECTION	017256/0368  11/25/2005
11/141,803 May 31, 2005	7,224,437 May 29, 2007	METHOD FOR MEASURING AND VERIFYING STEPPER ILLUMINATION	016637/0458  08/13/2005

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**Attorney Docket No. 041Y-127088**

**Assignor: Invarium, Inc.**

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<b>Application Serial No. / Filing Date</b>	<b>Patent No. / Issue Date</b>	<b>Title</b>	<b>Assignment Reel/Frame Recorded Date</b>
11/141,805 May 31, 2005	--	METHOD FOR VERIFYING AND CHOOSING LITHOGRAPHY MODEL	017010/0689  11/13/2005
11/203,522 Aug. 13, 2005	--	APPARATUS AND METHOD FOR BREAKING UP AND MERGING POLYGONS	018396/0430  10/16/2006
11/102,206 April 9, 2005	--	OPTICAL LITHOGRAPHY VERIFICATION PROCESS	016652/0916  08/19/2005
11/101,872 April 9, 2005	--	OPTICAL LITHOGRAPHY CORRECTION PROCESS	016637/0295  08/12/2005
11/203,331 Aug. 13, 2005	7,379,170 May 27, 2008	APPARATUS AND METHOD FOR CHARACTERIZING AN IMAGE SYSTEM IN LITHOGRAPHY PROJECTION TOOL	017010/0696  11/13/2005
10/860,852 June 4, 2004	7,275,225 Sept. 25, 2007	CORRECTING DESIGN DATA FOR MANUFACTURE	014769/0440  06/23/2004
10/933,090 Sept. 1, 2004	7,189,481 March 13, 2007	CHARACTERIZING FLARE OF A PROJECTION LENS	015146/0938  09/17/2004
10/960,357 Oct. 6, 2004	--	METHOD FOR CHARACTERIZATION OF THE ILLUMINATOR IN A LITHOGRAPHIC SYSTEM	015273/0379  10/19/2004
10/860,853 June 4, 2004	7,277,165 Oct. 2, 2007	METHOD OF CHARACTERIZING FLARE	014769/0443  06/23/2004
10/971,350 Oct. 22, 2004	--	METHOD AND SYSTEM FOR REDUCING THE IMPACT OF ACROSS- WAFER VARIATIONS ON CRITICAL DIMENSION MEASUREMENTS	015858/0885  03/08/2005
11/169,616 June 30, 2005	--	METHOD FOR REAL TIME MONITORING AND VERIFYING OPTICAL PROXIMITY CORRECTION MODEL AND METHOD	017001/0853  09/20/2005

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10/861,170 June 4, 2004	7,266,800 Sept. 4, 2007	METHOD AND SYSTEM FOR DESIGNING MANUFACTURABLE PATTERNS THAT ACCOUNT FOR THE PATTERN- AND POSITION- DEPENDENT NATURE OF PATTERNING PROCESSES	015440/0623  06/04/2004
11/781,108 July 20, 2007	--	METHOD OF VERIFYING PHOTOMASK DATA BASED ON MODELS OF ETCH AND LITHOGRAPHY PROCESSES	019939/0641  10/10/2007
10/841,079 May 7, 2004	7,318,214 Jan. 8, 2008	SYSTEM AND METHOD FOR REDUCING PATTERNING VARIABILITY IN INTEGRATED CIRCUIT MANUFACTURING THROUGH MASK LAYOUT CORRECTIONS	015319/0510  05/07/2004