PATENT ASSIGNMENT

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SUBMISSION TYPE: **NEW ASSIGNMENT** NATURE OF CONVEYANCE: **ASSIGNMENT**

CONVEYING PARTY DATA

Name	Execution Date
Toshiba America Electronic Components, Inc.	04/28/2008

RECEIVING PARTY DATA

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PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	11105113

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Total Attachments: 1

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PATENT REEL: 021152 FRAME: 0357

500577247

ATTORNEY DOCKET NO.: SUZU03100 TOSHIBA DOCKET NO.: APG038960-USA-A STI-1150

ASSIGNMENT

We, Toshiba America Electronic Components, Inc., a corporation organized under the laws of California, located at 19900 MacArthur Boulevard, Suite 400, Irvine, CA 92612, U.S.A., the assignee of the following U.S. Patent Application:

SERIAL NO.

DATE FILED

INVENTORS

11/105,113

4/13/2005

Eiichi Hosomi, Paul M. Harvey

Date:_ 4/28/08

TITLE OF INVENTION

Systems and Methods for Reducing Simultaneous Switching Noise in an Integrated Circuit

hereby assign the entirety of our right, title and interest in said U.S. Patent Application, including all divisions, reissues, continuations, substitutions and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of industrial property, including rights of priority, resulting from the filing of any said application to KABUSHIKI KAISHA TOSHIBA, a Japanese corporation, located at 1-1 Shibaura 1-chome, Minato-ku, Tokyo, Japan, their successors, assigns, or other legal representatives.

The undersigned is authorized to sign this document on behalf of the corporation.

TOSHIBA AMERICA ELECTRONIC COMPONENTS, INC.

Name:

RECORDED: 06/25/2008

Mr. Hideya Sakaida

Title:

By:

President and CEO

Approved
TAEC Legal Department

PATENT REEL: 021152 FRAME: 0358