

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Riverstone Networks, Inc.	04/27/2006
RECEIVING PARTY DATA	
Name:	Lucent Technologies Inc.
Street Address:	600-700 Mountain Avenue
City:	Murray Hill
State/Country:	NEW JERSEY
Postal Code:	07974-0636
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	10623784
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ATTORNEY DOCKET NUMBER:	RIVERSTONE 69
NAME OF SUBMITTER:	Gregory J. Murgia
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ASSIGNMENT OF PATENT

Effective April 27, 2006, RIVERSTONE NETWORKS, INC., a Delaware corporation ("Parent"), located at 5200 Great America Parkway, Santa Clara, California 95054, and its undersigned direct and indirect subsidiaries (which, including the Parent, are each individually referred to herein as a "Seller" and are collectively with the Parent referred to herein as the "Sellers"), hereby convey, transfer, assign and deliver to LUCENT TECHNOLOGIES INC., a Delaware corporation ("Buyer"), located at 600 Mountain Avenue, Murray Hill, New Jersey 07974, the entire right, title and interest for the United States of America and its territorial possessions, and all foreign countries, jurisdictions and political entities of the world, including all rights of priority, in and to inventions disclosed in the patents, patent applications and inventive submissions identified on Schedule A, and in and to all Letters Patents of the United States and all foreign countries, jurisdictions and political entities of the world, which may or shall be granted on said inventions, or any parts thereof, or any divisional, continuing, reissue or other applications based in whole or in part thereon, and the right to recover for past infringement of the patents. Buyer hereby accepts said Assignment.

Sellers agree to execute all applications, amended specifications, deeds or other instruments, and to do all acts necessary or proper to secure the grant of Letters Patent in the United States and in all other countries, jurisdictions and political entities of the world, to Buyer, and to vest and confirm in Buyer, its successors, assigns, legal representatives or nominees, the legal title to all such patents.

Sellers do hereby authorize and request the Commissioner of Patents and Trademarks of the United States Patent and Trademark Office and any other similarly competent official in any other countries, jurisdictions or political entities of the world, to issue such Letters Patent as shall be granted upon said inventions or applications based thereon to said Buyer, its successors, assigns legal representatives or nominees.

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Witness my hand and seal this 27th day of April, 2006.

SELLER:

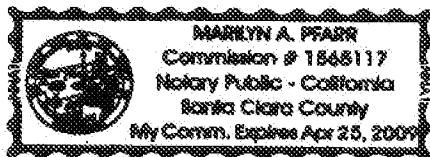
RIVERSTONE NETWORKS, INC.,
a Delaware corporation

By: [Signature]
Name: Juan Oscar Rodriguez
Title: Chief Executive Officer

State of California)
County of Santa Clara)

On this 27th day of April, 2006, before me, the undersigned notary public, personally appeared Juan Oscar Rodriguez, proved to me through satisfactory evidence of identification, which was Florida Drivers License, to be the person whose name is signed on the preceding document, and acknowledged to me that he signed it voluntarily for its stated purpose.

[affix seal]



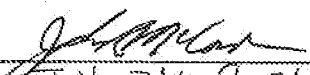
[Signature]
Notary Public
My commission expires: April 25, 2009

Assignment of Patent

Witness my hand and seal this 21st day of April, 2006.

BUYER:

LUCENT TECHNOLOGIES INC.,
a Delaware corporation

By: 
Name: John R. McCord
Title: Asst. Treasurer

State of) New Jersey
County of) Union

On this 21st day of April, 2006, before me, the undersigned notary public, personally appeared John R. McCord, proved to me through satisfactory evidence of identification, which was N.J. Driver License, to be the person whose name is signed on the preceding document, and acknowledged to me that he signed it voluntarily for its stated purpose.

[affix seal]


Notary Public

My commission expires: 8/07/2010

Assignment of Patent

Schedule A
Assignment of Patent

<u>Case No.</u>	<u>Title</u>	<u>Country</u>	<u>Inventors</u>	<u>Application Number</u>	<u>Filing Date</u>	<u>Patent Number</u>	<u>Date Issued</u>	<u>Expiration Date</u>	<u>Status</u>
CBLT-001	Method and apparatus for fair and efficient scheduling of variable-size data packets in input-buffered multiport switch	US	Aybay, G. Ferolito, P.	09/037,218	3/10/1998	6,044,061	3/28/2000	3/10/2018	Issued
CBLT-001-EPO	Method and apparatus for fair and efficient scheduling of variable-size data packets in ...	EU		99911067.9	3/3/1999				Pending
CBLT-001-AU	Fair and efficient cell scheduling in input-buffered point switch	AU		29800/99	3/3/1999	746166	2/26/2002	3/3/2019	Issued
CBLT-001-CA	Method and apparatus for fair and efficient scheduling of variable-size data packets in ...	CA		2,291,049	3/3/1999	2,291,049	12/21/2004	3/3/2019	Issued
CBLT-002	Method and apparatus for supplying requests to a scheduler in an input buffered multiport switch	US	Bauman, J. Anderson, E.	09/072,147	5/4/1998	6,160,812	12/12/2000	5/4/2018	Issued
CBLT-002-EPO	Method and apparatus for supplying requests to a scheduler in an input buffered multiport ...	EU		99920239.3	4/29/1999				Pending
CBLT-002-AU	Method and apparatus for supplying requests to a scheduler in an input buffered multiport ...	AU		37784/99	4/29/1999	746246	2/26/2002	4/29/2019	Issued
CBLT-002-CA	Method and apparatus for supplying requests to a scheduler in an input buffered multiport ...	CA		2,328,988	4/29/1999	2,328,988	10/19/2004	4/29/2019	Issued
CBLT-003	Method and system for identifying ports and forwarding packets in a multiport switch	US	Ferolito, P. Pfle, R.	09/062,377	4/17/1998	5,999,531	12/7/1999	4/17/2018	Issued
CBLT-003-EPO	Method and system for identifying ports and forwarding packets in a multiport switch	EU		99917573.0	4/16/1999				Pending
CBLT-003-AU	Method and system for identifying ports and forwarding packets in a multiport switch	AU		35661/99	4/16/1999	746360	2/25/2002	4/16/2019	Issued
CBLT-003-CA	Method and system for identifying ports and forwarding packets in a multiport switch	CA		2,329,015	4/16/1999	2,329,015	4/1/2003	4/16/2019	Issued

CBLT-004	ports and forwarding packets in a multipoint switch	US	Bauman, J	09/072,148	5/4/1998	6,046,979	4/4/2000	5/4/2018	Issued
CBLT-004-EPO	Method and apparatus for controlling the flow of variable-length packets through a multipoint switch	EU		99920163.5	4/29/1999				Pending
CBLT-004-AU	Method and apparatus for controlling the flow of variable-length packets through ...	AU		37726/99	4/29/1999	746446	8/15/2002	4/29/2019	Issued
CBLT-004-CA	Method and apparatus for controlling the flow of variable-length packets through ...	CA		2,329,020	4/29/1999	2,329,020	4/1/2003	4/20/2019	Issued
CBLT-005	Method and apparatus for forwarding variable-length packets between channel-specific packet processors and a crossbar of a multipoint switch	US	Aybay, G	09/084,081	5/22/1998	6,052,368	4/18/2000	5/22/2018	Issued
CBLT-005-EPO	Method and apparatus for forwarding variable-length packets between channel-specific...	EU		99923032.9	5/13/1999				Pending
CBLT-005-AU	Method and apparatus for forwarding variable-length packets between channel-specific...	AU		39895/99	5/13/1999	746227	2/25/2002	5/13/2019	Issued
CBLT-005-CA	Method and apparatus for forwarding variable-length packets between channel-specific...	CA		2,328,987	5/13/1999	2,328,987	9/23/2003	5/13/2019	Issued
CBLT-006	Method and apparatus for forwarding packets from a plurality of contending queues to an output	US	Aalresh, D	09/087,064	5/29/1998	6,067,301	5/23/2000	5/29/2018	Issued
CBLT-006-EPO	Method and apparatus for forwarding packets from a plurality of contending queues ...	EU		99923022.0	5/13/1999				Pending
CBLT-006-AU	Method and apparatus for forwarding packets from a plurality of contending queues ...	AU		39889/99	5/13/1999	746167	2/26/2002	5/13/2019	Issued
CBLT-006-CA	Method and apparatus for forwarding packets from a plurality of contending queues ...	CA		2,329,019	5/13/1999	2,329,019	9/23/2003	5/13/2019	Issued
CBLT-007	Method and apparatus for fair and efficient scheduling of	US	Aybay, G	09/188,431	11/9/1998	6,185,221	2/6/01	11/9/2018	Issued

CBLT-007-EPO	variable-size data packets in an input-buffered multiport switch Method and apparatus for fair and efficient scheduling of variable-size data packets in ...	EU	99967093.8	11/5/1999			Pending
CBLT-007-AU	Method and apparatus for fair and efficient scheduling of variable-size data packets in ...	AU	22554/00	11/5/1999	1/30/2003	11/5/2019	Issued
CBLT-007-CA	Method and apparatus for fair and efficient scheduling of variable-size data packets in ...	CA	2,348,089	11/5/1999	10/19/2004	11/5/2019	Issued
CBLT-008	Method and system for managing forwarding tables	US	09/364,502	7/30/1999	1/13/2004	7/30/2019	Issued
CBLT-008-EPO	Method and system for managing forwarding tables	EU	00948971.7	1/7/2002			Pending
CBLT-008-AU	Method and system for managing forwarding tables	AU	62387/00	1/23/2002	8/12/2004	7/26/2020	Issued
CBLT-008-CA	Method and system for managing forwarding tables	CA	2,380,792	1/30/2002			Pending
CBLT-009	A method and system for controlling data flow through a multiport switch	US	09/392,209	9/8/1999	10/21/2003	9/8/2018	Issued
CBLT-009-EPO	A method and system for controlling data flow through a multiport switch	EU	00950731.0	2/12/2002	11/2/2005	9/8/2019	Issued
CBLT-009-AU	A method and system for controlling data flow through a multiport switch	AU	63792/00	2/28/2002	10/30/2003	7/26/2020	Issued
CBLT-009-CA	A method and system for controlling data flow through a multiport switch	CA	2,382,568	2/22/2002			Pending
GRAY-1	Dynamic allocation of port bandwidth in high speed packet-switched digital switch	US	08/590,970	1/24/1996	11/17/1998	1/24/2016	Issued
GRAY-2	High speed packet-switched digital switch and method	US	08/606,163	2/23/1996	3/3/1998	2/23/2016	Issued
THAP-CS-0001	Queue management with support for multicasts in an asynchronous transfer mode switch	US	08/977,661	11/24/1997	4/17/2001	11/28/2017	Issued
THAP-CS-0002	Flexible scheduler in an asynchronous transfer mode (ATM) switch	US	08/976,686	11/24/1997	5/28/2002	11/24/2017	Issued

THAP-CS-0003	Method for integrated traffic shaping in a packet-switched network	US	Bonomi, F	08/825,990	4/4/1997	5,864,540	1/26/1999	4/4/2017	Issued
THAP-CS-0004	Efficient method and apparatus for allocating memory space used for buffering cells received on several connections in an asynchronous transfer mode (ATM) switch	US	Bonomi, F VU, D Shetty, S	09/081,969	5/20/1998	6,292,492	9/18/2001	5/20/2018	Issued
THAP-CS-0005	Explicit rate congestion control system and method	US	Bonomi, F Wong, M	08/975,331	11/20/1997	6,069,872	5/30/2000	11/20/2017	Issued
THAP-CS-0006	A flexible scheduler in an asynchronous transfer mode (ATM) switch	US	Bonomi, F Devarajan, K	09/593,313	6/13/2000	6,349,089	2/19/2002	11/24/2017	Issued
THAP-CS-0007	Flexible scheduler in an asynchronous transfer mode (ATM) switch	US	Bonomi, F Devarajan, K	10/103,912	3/25/2002				Pending*
TELEIP-001P TELE-001-US	(Router) Adaptive Packet Routing	AU	Martin, A	PR5803	6/20/2001				Pending
	(Router) Adaptive Packet Routing	US	Martin, A	10/479,897	12/3/2003				Pending
TELEIP-002P	(MASQ) Means for Interfacing Devices under SNMP	AU	Martin, A Paterson, B	PQ9631	8/24/2000				Pending
TELEIP-002-US	Means for Interfacing Devices Under SNMP	US	Martin, A Paterson, B	10/362,493	2/21/2003				Pending
TELEIP-003-AU	(GPS) Event Recording and Analysis in Telecommunications Networks	AU	Martin, A	42911/97	9/27/1996	716041	6/1/2000		Issued
RSTN-001	Method and system for connecting virtual circuits across an Ethernet switch	US	Lasserre, M	09/861,138	5/18/2001				Pending
RSTN-001-EPO	Method and system for connecting virtual circuits across an Ethernet switch	EU	Lasserre, M	02736962.8	12/9/2003				Pending
RSTN-002	Method and system for preventing transmission loops in a label switching domain	US	Behzadi, B	09/865,035	5/24/2001	6,728,220	4/27/2004	8/1/2021	Issued
RSTN-002-EPO	Method and system for preventing transmission loops in a label switching domain	EU	Behzadi, B	02727154.1	12/9/2003				Pending

RSTN-003	Method and system for updating a CAM that prioritizes CAM entries according to prefix length	US	Balakrishnan, R	09/876,499	6/7/2001	6,615,311	9/2/2003	4/3/2022	Issued
RSTN-004	Method, system, and computer program for managing a re-usable resource	US	Balakrishnan, R	09/888,209	6/22/2001	6,629,114	9/30/2003	1/8/2022	Issued
RSTN-005	Method and system for managing packets in a shared memory buffer that serves multiple output links	US	Schaub, M.	09/930,597	8/15/2001	7,031,331	4/18/2006	5/21/2024	Issued
RSTN-007	Flexible application of mapping algorithms within a packet distributor	US	Schaub et al.	09/967,563	9/28/2001				Pending
RSTN-009	Method and system for providing failure protection in a ring network that utilizes label switching	US	Behzadi, B	10/021,754	12/12/2001				Pending
RSTN-009-CN	Method and system for providing failure protection in a ring network that utilizes label switching	China	Behzadi, B	02827962.X	8/9/2004				Pending
RSTN-009-EPO	Method and system for providing failure protection in a ring network that utilizes label switching	EU	Behzadi, B	02795840.4	6/15/2004				Pending
RSTN-009-JP	Method and system for providing failure protection in a ring network that utilizes label switching	Japan	Behzadi, B	2003-551554	6/11/2004				Pending
RSTN-009-KR	Method and system for providing failure protection in a ring network that utilizes label switching	Korea	Behzadi, B	2004-7009113	6/11/2004				Pending
RSTN-010	Method, system, and computer program for managing a re-usable resource w/doubly linked...	US	Balakrishnan, R	09/909,549	7/19/2003	6,976,021	12/13/2005	12/24/2023	Issued
RSTN-011	Method and system for rate shaping in packet-based computer networks	US	Lohda et al.	10/007,409	12/5/2003	6,798,741	9/28/2004	12/4/2022	Issued
RSTN-011-CN	Method and system for rate shaping in packet-based computer networks	China	Lohda et al.	02827774.0	8/8/2004				Pending
RSTN-011-EPO	Method and system for rate shaping in packet-based computer networks	EU	Lohda et al.	02797188.6	6/17/2004				Pending
RSTN-011-JP	Method and system for rate shaping in packet-based computer networks	Japan	Lohda et al.	2003-550030	6/4/2004				Pending

RSTN-011-KR	networks Method and system for rate shaping in packet-based computer networks	Korea	Lohda et al.	2004-7008736	6/5/2004		Pending
RSTN-012	Method, system, and computer program product for providing failure protection in a network node	US	Balakrishnan, et al.	10/094,393	3/7/2002	6,941,487	8/15/2023 Issued
RSTN-013	Adaptive Classification of Network Traffic	US	MacFaden, et al.	10/321,924	12/17/2002		Pending
RSTN-013-CN	Adaptive Classification of Network Traffic	China	MacFaden, et al.	200380106500.3	12/3/2003		Pending
RSTN-013-EPO	Adaptive Classification of Network Traffic	EU	MacFaden, et al.	03796592.8	ibid		Pending
RSTN-013-JP	Adaptive Classification of Network Traffic	Japan	MacFaden, et al.	ibid	ibid		Pending
RSTN-014	Stateful failover protection among routers that provide load sharing using network address translation (LSNAT)	US	Madhav, et al.	10/233,002	8/30/2002		Pending
RSTN-015	Method and system for providing differentiated service on a per virtual circuit basis within a packet-based switch/router	US	Washabugh et al.	10/056,159	1/24/2002*	7,023,856	4/4/2006 9/30/2024 Issued
RSTN-016	Method, system, and computer program product for providing a software upgrade in a network node	US	Balakrishnan, et al.	10/140,395	5/7/2002		Pending
RSTN-017	Virtual local area network identifier translation in a packet-based network	US	Hawthorne, et al.	10/179,733	6/24/2002*		Pending
RSTN-019	Distributed weighted fair arbitration and forwarding	US	Ferolito	10/449,627	5/30/2003*		Pending
RSTN-020	Hardware-based rate control for bursty traffic	US	Jain, Aatresh Hegglin	10/446,419	5/28/2003*		Pending
RSTN-021	Classifying & distributing traffic at a network node	US	Yu, et al.	10/187,239	6/28/2002*		Pending
RSTN-023	Classifying traffic in a network node Using multiple on-chip memory arrays	US	Yu, et al.	10/197,747	7/17/2002*		Pending
RSTN-027	High-Speed Chip-to-Chip	US	Kleveland et al.	10/439,571	5/16/2003*		Pending

	Communications Interface					
RSTN-028	High-Speed Chip-to-Chip Communications Interface with signal trace routing and ended parallel interconnect and phase offset detection	US	Kleveland	10/439,566	5/16/2003*	Pending
RSTN-029	Method of flow control in a distributed scalable shared memory switching fabric system	US	Perolito, Anderson Mathews	10/453,975	6/4/2003*	Pending
RSTN-030	Dynamically controlling power consumption within a network node	US	John	10/384,272	3/7/2003*	Pending
RSTN-031	Arbitration logic for assigning an input packet to an available thread of a multi-threaded multi-engine network processor	US	John et al.	10/425,695	4/28/2003*	Pending
RSTN-031-1	Network processor with multiple multi-threaded packet-type specific engines	US	Morrison	10/425,693	4/28/2003*	Pending
RSTN-033	Flexible multilevel output traffic control	US	Bauman, et al.	10/439,681	5/16/2003*	Pending
RSTN-036	Random early drop with per hop behavior biasing	US	Mathews, et al.	10/370,670	2/18/2003*	Pending
RSTN-038	Traffic control at a network node	US	Anderson	10/186,880	6/28/2002*	Pending
RSTN-039	Multi-cast packet queuing	US	Anderson et al.	10/443,505	5/22/2003*	Pending
RSTN-042	Pointer allocation by prime numbers	US	Mathews, et al.	10/443,586	5/22/2003*	Pending
RSTN-044	Priority-based exit queue arbitration with fairness	US	Mathews	10/359,878	2/7/2003*	Pending
RSTN-045	Loop elimination in a Communications network	US	Srinivasan, et al.	10/314,680	12/9/2002*	Pending
RSTN-064	Distribution of forwarding information in a network node	US	Shenoy Metha	10/449,629	5/30/2003*	Pending
RSTN-065	Synchronizing multiple instances of a forwarding information base (FIB)	US	Shenoy Mehta	10/446,762	5/28/2003*	Pending

RSTN-067	using sequence numbers Managing VLAN traffic in a multiport network node using customer-specific identifiers	US	Ishwar, et al.	10/439,684	5/16/2003*	Pending
RSTN-072	Network node with layer 2 interfaces configurable by interface class	US	Ishwar Mehta, Shenoy	10/434,283	6/4/2003*	Pending
RSTN-073	Managing Traffic in a multiport network node using logical ports	US	Ishwar, Gionkar Mehta, Subbiah	10/455,510	6/4/2003*	Pending
RSTN-074	Managing configuration state within a network node	US	MacFaden Shenoy	10/454,102	6/4/2003*	Pending
RSTN-078	Using reassembly queue sets for packet reassembly	US	Mathews, Ferolito Anderson, Morrison	10/454,298	6/4/2003*	Pending
RSTN-079	Controlling the flow of packets within a network node utilizing random early detection	US	Lohda, et al.	10/199,238	7/19/2002*	Pending
RSTN-080	Method and Apparatus for multipath processing	US	Anderson, et al.	10/425,702	4/28/2003*	Pending
RSTN-081	Hitless restart of access control module	US	Krishnan	10/454,103	6/4/2003*	Pending*
RSTN-082	Forwarding Traffic in a network using a single forwarding table that includes forwarding information related to a plurality of logical networks	US	Krishnan	10/340,113	1/10/2003*	Pending
RSTN-084	Packet-based traffic shaping	US	Lohda	10/352,829	1/27/2003*	Pending
RSTN-085	Optimal load balancing across multiple switching fabrics	US	Mathews, Ferolito Anderson, Morrison	10/454,167	6/4/2003*	Pending
RSTN-086	Distributing unused allocated bandwidth using a borrow vector	US	Lohda, et al.	10/370,669	2/18/2003*	Pending
RSTN-087	Testing and error recovery across multiple switching fabrics	US	Mathews, Ferolito Anderson, Morrison	10/453,976	6/4/2003*	Pending
RSTN-088	Multiprotocol label switching (MPLS) edge service extraction	US	Footo, Anquid Ramakrishnan Manickavasagam Mehta	10/623,784	7/21/2003*	Pending

RSTN-089	Atomic lookup rule set transition	US	Rich, Lodha Krishnan, Pfile	10/449,628	5/30/2003*	Pending
RSTN-090	Efficient mechanism for wire-tapping network traffic	US	Ishwar, Mehta Sanchez, Saxena Shenoy	10/455,512	6/4/2003*	Pending
RSTN-091	Efficient cascaded lookups in US a network node	US	Saxena, et al.	10/546,407	1/16/2003*	Pending
RSTN-093	Protection switching at a network node	US	Rigby, et al.	10/540,083	1/10/2003*	Pending
RSTN-095	Efficient rendezvous point tree to shortest path tree switch-over process	US	Sanchez, Mehta	10/453,974	6/4/2003*	Pending
RSTN-097	Efficient reverse path forwarding check mechanism	US	Sanchez, Mehta	10/454,217	6/4/2003*	Pending
RSTN-098	Concurrent execution of kernel work and non-kernel work in operating systems with single-threaded kernel	US	Ishwar, et al.	10/431,556	5/7/2003*	Pending
RSTN-099	Managing a position-dependent data set that is stored in a content addressable memory array at a network node	US	Krishnan, et al.	10/321,871	12/17/2002*	Issued
RSTN-101	Efficient redirection of logging and tracing information in network node with distributed architecture	US	Josysula, et al.	10/443,576	5/22/2003*	Pending
RSTN-102	Shell specific filtering and display of log messages	US	Josysula, et al.	10/443,577	5/22/2003*	Pending
RSTN-105	Hierarchical Rate-limiting at a network node that utilizes an infinity rate-limit check	US	Tundlam, et al.	10/369,432	2/19/2003*	Pending
RSTN-108	Dynamic load balancing within a network	US	Tundlam, et al.	10/361,984	2/11/2003*	Pending
RSTN-109	Managing routes in a router utilizing threshold-specific discard algorithms	US	Sankaran et al.	10/222,125	8/16/2002*	Pending
RSTN-111	Obtaining path information related to a bridged network	US	Hongal Rao	10/925,624	8/25/2004*	Pending

RSTN-114	Processing Packet Info using an array of processing elements	US	Schultz Morrison	10/809,164	3/25/2004*	Pending
RSTN-116	Optical fiber management in a chassis-based network system	US	Fricker	10/844,021	5/12/2004*	Pending
RSTN-117	Managing loops between network devices by monitoring MAC moves	US	Anquid, Antich	10/809,028	3/25/2004*	Pending
RSTN-119	Customer-specific traffic shaping	US	Lodha	10/620,668	7/16/2003*	Pending
RSTN-122	Implicit interprocess communications (IPC) versioning support	US	Hegde Doshi	10/934,679	9/3/2004*	Pending
RSTN-124	Managing Processing Utilization in a Network Node	US	Lodha et al.	11/036,909	1/14/2005*	Pending
RSTN-124-PCT	Managing Processing Utilization in a Network Node	PCT	Lodha et al.	PCT/US05/01284	1/14/2005	Pending
RSTN-125	Managing process state information in an operating system environment	US	Balakrishnan Belevich	11/081,297	3/16/2005*	Pending
RSTN-125-PCT	Managing process state information in an operating system environment	PCT	Balakrishnan Belevich	PCT/US05/08808	3/16/2005	Pending
RSTN-126	Device-level address translation within a programmable non-volatile memory device	US	Belevich Balakrishnan	11/090,594	3/25/2005*	Pending
RSTN-126-PCT	Device-level address translation within a programmable non-volatile memory device	PCT	Belevich Balakrishnan	PCT/US05/10114	3/25/2005*	Pending
RSTN-127	Obtaining path information related to a virtual private LAN services (VPLS) based network	US	Hongal, Ishwar Kasraikar, Rigby Sankaran, Ramakrishnan	10/891,546	7/15/2004	Pending
RSTN-127-PCT	Obtaining path information related to a virtual private LAN services (VPLS) based network	US	Hongal, Ishwar Kasraikar, Rigby Sankaran, Ramakrishnan	PCT/US05/23312	6/30/2005	Pending
RSTN-130P	Loop detection in Ethernet bridged networks using source address filters	US	Jain, Vipin Srinivasan, Shrinam	60/748,135	12/7/2005	Pending
RSTN-131P	Caching message digests to scale layer 2 control protocols in Ethernet bridged networks	US	Jain, Vipin Ljacuna, Mark	60/748,188	12/7/2005	Pending
RSTN-132P	Algorithm to accurately adjust	US	Jain, Vipin	60/748,133	12/7/2005	Pending

RSTN-128	MAC move rate when detecting loops based on MAC moves in Ethernet bridged networks	Srinivasan, Shriram	Pending
	Proportional Rate Limiter for Hierarchical Network Traffic	Matthews, Gregory	

Not Yet Filed

* denotes that there is a claim to an earlier priority date