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PATENT REEL: 021400 FRAME: 0960

CONFIRMATORY INSTRUMENT

APPLICATION FOR: SYSTEM TO REDUCE LATENCY BY RUNNING A MEMORY CHANNEL FREQUENCY FULLY ASYNCHRONOUS FROM A MEMORY DEVICE FREQUENCY

INVENTOR(S): MARK A. BRITTAIN, KEVIN C. GOWER, WARREN MAULE

SERIAL NO. 12/019043	FILING DATE: 01/24/2008
CONTRACTOR: INTERNATIONAL BUSINESS MACHINES CORPORATION	CONTRACT NO. HR0011-07-9-0002

The Invention identified above is a "Subject Invention" under the Patent Rights Clause included in the above contract with:

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Signed this	29th	_ day of	JANUARY	, <u>200</u> 8
			INTERNATIONAL MACHINES CORI CONTRACTOR BY: CONTRACTOR	
			John D Flynn IP Senior Counse IBM	I - STG
			<u>BUSINESS ADDF</u> 11501 Burnet Roa Austin, TX 78758	ad

IBM Disclosure No.: <u>AUS820071628</u> IBM Docket No.: <u>AUS920070820US1</u>

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RECORDED: 08/08/2008