

Form PTO-1595 (Rev. 07/05)
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RECORDATION FORM COVER SHEET PATENTS ONLY

To the Director of the U.S. Patent and Trademark Office: Please record the attached documents or the new address(es) below.

1. Name of conveying party(ies)

Thunder Creative Technologies, Inc.

Additional name(s) of conveying party(ies) attached? Yes No

3. Nature of conveyance/Execution Date(s):

Execution Date(s) 06/02/2008

- Assignment Merger
- Security Agreement Change of Name
- Joint Research Agreement
- Government Interest Assignment
- Executive Order 9424, Confirmatory License
- Other _____

2. Name and address of receiving party(ies)

Name: Robert D. Washburn

Internal Address: _____

Street Address: 18245 Kingsport Dr.

City: Malibu

State: California

Country: US Zip: 90265

Additional name(s) & address(es) attached? Yes No

4. Application or patent number(s):

This document is being filed together with a new application

A. Patent Application No.(s)

11/112,426 10/898,006 60/684,878 11/425,978
60/479,702 60/499,824 11/420,214
10/871,157 10/933,825 60/684,879
60/489,570 60/602,548 60/693,137

B. Patent No.(s)

6,897,704
7,312,637
7,260,000

Additional numbers attached? Yes No

5. Name and address to whom correspondence concerning document should be mailed:

Name: Francisco A. Rubio-Campos

Internal Address: _____

Street Address: The Eclipse Group LLP

10605 Balboa Blvd., Suite 300

City: Granada Hills

State: CA Zip: 91344

Phone Number: (949) 448-9410

Fax Number: (818) 332-4205

Email Address: frc@eclipsegrp.com

6. Total number of applications and patents involved: 16**7. Total fee (37 CFR 1.21(h) & 3.41) \$ 640.00**

- Authorized to be charged by credit card
- Authorized to be charged to deposit account
- Enclosed
- None required (government interest not affecting)

8. Payment Information

a. Credit Card Last 4 Numbers 0465
Expiration Date 05/2010

b. Deposit Account Number 50-2542

Authorized User Name Francisco A. Rubio-Campos

9. Signature:


Signature

8/19/08
Date

Francisco A. Rubio-Campos
Name of Person Signing

Total number of pages including cover sheet, attachments, and documents:

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to:
Mail Stop Assignment Recordation Services, Director of the USPTO, P.O.Box 1450, Alexandria, V.A. 22313-1450

PATENT

REEL: 021411 FRAME: 012

700381772

OP \$640.00 11112426

**Recordation Form Cover Sheet
Patents Only**

Continuation of Box 2

Name and address of receiving party(ies)

Name: Robert F. McClanahan
Street Address: 27781 Sequoia Glen Dr.
City: Valencia
State: California
Country: US
Zip: 91354

ASSIGNMENT

For valuable consideration, Thunder Creative Technologies, Inc. a corporation organized and existing under and by the virtue of the laws of the state of Delaware, and having a place of business located at 18425 Kingsport Drive Malibu, CA 90265 (hereinafter referred to as "Assignor") for good and valuable consideration, the sufficiency of which is hereby acknowledged do hereby assigns to Robert D. Washburn and Robert F. McClanahan and their successors and assigns (collectively hereinafter called "the Assignees"), the entire rights, titles and interests throughout the world in the inventions and improvements which are the subject of :

- | | | |
|----|---|---------------------|
| A. | ELECTRONIC ISOLATOR | [See schedule "A"]: |
| B. | SYSTEM FOR CROSSTALK NOISE REDUCTION ON TWISTED PAIR, ETHERNET, POLYPHASE AND SHIELDED WIRE | [See Schedule "B"] |
| C. | LOW DISTORTION DIGITAL TO ANALOG CONVERTER AND DIGITAL SIGNAL SYNTHESIZER SYSTEMS | [See Schedule "C"] |
| D. | COMMON MODE TRANSMISSION LINE TERMINATION | [See Schedule "D"] |
| E. | ENHANCED TIMING MARGIN MEMORY INTERFACE | [See Schedule "E"] |
| F. | TRANSFORMER COUPLED CLOCK INTERFACE CIRCUIT FOR MEMORY MODULE | [See Schedule "F"] |
| G. | CONTROL SIGNAL INTERFACE CIRCUIT FOR COMPUTER MEMORY MODULES | [See Schedule "G"] |
| H. | POINT-OF-LOAD POWER CONDITIONING FOR MEMORY MODULES | [See Schedule "H"] |

and all related applications and patents, this assignment including said applications, any and all United States or Patent Cooperation Treaty applications, and all foreign patent applications and foreign patents granted for any of said inventions or improvements, including all divisions, continuations, reissues, continuations-in-part and extensions thereof, and the right to claim priority based on the filing dates of said applications and based on the filing dates of any provisional applications of which said applications claims the benefit under the International Convention for the Protection of Industrial Property, the Patent Cooperation Treaty, the European Patent Convention, and all other treaties of like purposes; and the Assignees are authorized to apply in all countries in their own name for patents and like rights and improvements; and the Assignor agrees without further compensation, upon request to perform such lawful acts, to promptly provide Assignees with all pertinent facts and documents relating

Schedule "A"

ELECTRONIC ISOLATOR						
Matter No.	Country	Serial No.	Filing Date	Patent No.	Issue Date	Type
2	US	09/866,563	05-25-2001	6,897,704	05-24-2005	Utility
3	EP	02746447.8	05-24-2002	n/a	n/a	National
4	JP	2003-501019	05-24-2002	n/a	n/a	National
5	PCT	US02/16443	05-24-2002	n/a	n/a	PCT
16	US	11/112,426	04-21-2005	n/a	n/a	Divisional

Schedule "B"

SYSTEM FOR CROSSTALK NOISE REDUCTION ON TWISTED PAIR, ETHERNET, POLYPHASE AND SHIELDED WIRE						
Matter No.	Country	Serial No.	Filing Date	Patent No.	Issue Date	Type
6	US	60/479,702	06-19-2003	n/a	n/a	Prov'l
9	US	10/871,157	06-18-2004	n/a	n/a	Utility
10	PCT	US04/19501	06-18-2004	n/a	n/a	PCT
25	EP	04756588.3	06-18-2004	n/a	n/a	National

Schedule "C"

LOW DISTORTION DIGITAL TO ANALOG CONVERTER AND DIGITAL SIGNAL SYNTHESIZER SYSTEMS						
Matter No.	Country	Serial No.	Filing Date	Patent No.	Issue Date	Type
7	US	60/489,570	07-23-2003	n/a	n/a	Prov'l
11	US	10/898,006	07-23-2004	n/a	n/a	Utility
12	PCT	US04/23785	07-23-2004	n/a	n/a	PCT
26	EP	04757249.0	07-23-2004	n/a	n/a	National

Schedule "D"

COMMON MODE TRANSMISSION LINE TERMINATION						
Matter No.	Country	Serial No.	Filing Date	Patent No.	Issue Date	Type
8	US	60/499,824	09-03-2003	n/a	n/a	Prov'l
14	US	10/933,825	09-02-2004	n/a	n/a	Utility
15	PCT	US04/25061	09-03-2004	n/a	n/a	PCT
27	EP	04783346.2	09-03-2004	n/a	n/a	EP

Schedule "E"

ENHANCED TIMING MARGIN MEMORY INTERFACE						
Matter No.	Country	Serial No.	Filing Date	Patent No.	Issue Date	Type
13	US	60/602,548	08-17-2004	n/a	n/a	Provl
21	US	11/206,638	08-17-2005	7,312,637	12-25-2007	Utility
22	PCT	US05/29328	08-17-2005	n/a	n/a	PCT
40	EP	05786565.1	08-17-2005	n/a	n/a	EP

Schedule "F"

TRANSFORMER COUPLED CLOCK INTERFACE CIRCUIT FOR MEMORY MODULE						
Matter No.	Country	Serial No.	Filing Date	Patent No.	Issue Date	Type
17	US	60/684,878	05-25-2005	n/a	n/a	Provl
30	US	11/420,214	05-24-2006	n/a	n/a	Utility
31	PCT	US06/028597	05-25-2006	n/a	n/a	PCT

Schedule "G"

CONTROL SIGNAL INTERFACE CIRCUIT FOR COMPUTER MEMORY MODULES						
Matter No.	Country	Serial No.	Filing Date	Patent No.	Issue Date	Type
18	US	60/684,879	05-25-2006	n/a	n/a	Prov'l-1
32	US	11/420,454	05-25-2006	7,260,000	08-21-2007	Utility
33	PCT	US06/20353	05-25-2006	n/a	n/a	PCT

Schedule "H"

POINT-OF-LOAD POWER CONDITIONING FOR MEMORY MODULES						
Matter No.	Country	Serial No.	Filing Date	Patent No.	Issue Date	Type
19	US	60/693,137	06-22-2005	n/a	n/a	Provl
28	US	11/425,978	06-22-2006	n/a	n/a	Utility
29	PCT	US06/24683	06-22-2006	n/a	n/a	PCT