PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	RELEASE BY SECURED PARTY

CONVEYING PARTY DATA

Name	Execution Date
Bank of New York Mellon Trust Company, N.A. (Formerly, Bank of New York Trust Company, N.A.)	08/08/2008

RECEIVING PARTY DATA

Name:	Conexant Systems, Inc.
Street Address:	4000 MacArthur Blvd.
City:	Newport Beach
State/Country:	CALIFORNIA
Postal Code:	92660

PROPERTY NUMBERS Total: 67

Property Type	Number
Patent Number:	RE39202
Patent Number:	5095525
Patent Number:	5198703
Patent Number:	5272661
Patent Number:	5325401
Patent Number:	5367203
Patent Number:	5379243
Patent Number:	5521499
Patent Number:	5528633
Patent Number:	5542041
Patent Number:	5554950
Patent Number:	5986691
Patent Number:	6043844
Patent Number:	6271726

Patent Number:	6501809
Patent Number:	6526070
Patent Number:	6553040
Patent Number:	6556591
Patent Number:	6606724
Patent Number:	6665752
Patent Number:	6724817
Patent Number:	6724829
Patent Number:	6727959
Patent Number:	6731712
Patent Number:	6765623
Patent Number:	6769093
Patent Number:	6785700
Patent Number:	6795512
Patent Number:	6810502
Patent Number:	6816940
Patent Number:	6856656
Patent Number:	6865711
Patent Number:	6882690
Patent Number:	6922438
Patent Number:	6943635
Patent Number:	6973615
Patent Number:	7010802
Patent Number:	7016376
Patent Number:	7065703
Patent Number:	7103831
Patent Number:	7106810
Patent Number:	7123103
Patent Number:	7142060
Patent Number:	7177382
Patent Number:	7206321
Patent Number:	7206361
Patent Number:	7231005
Patent Number:	7298222
Patent Number:	7301379

Patent Number:	7310369
Patent Number:	7310768
Patent Number:	7352797
Application Number:	10210591
Application Number:	10284121
Application Number:	10340577
Application Number:	10340854
Application Number:	10675566
Application Number:	10677110
Application Number:	10867355
Application Number:	10882083
Application Number:	11026670
Application Number:	11096217
Application Number:	11194107
Application Number:	11233674
Application Number:	11244568
Application Number:	11303876
Application Number:	11522583

CORRESPONDENCE DATA

Fax Number: (949)483-4176

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 858-713-3610

Email: haw-minn.lu@conexant.com

Correspondent Name: Haw-minn Lu

Address Line 1: 9645 Scranton Road, Suite 140

Address Line 4: San Diego, CALIFORNIA 92121

ATTORNEY DOCKET NUMBER:	RELEASES
NAME OF SUBMITTER:	Haw-minn

Total Attachments: 7
source=release_3#page1.tif
source=release_3#page2.tif
source=release_3#page3.tif
source=release_3#page4.tif
source=release_3#page5.tif
source=release_3#page6.tif
source=release_3#page7.tif

RELEASE OF SECURITY INTEREST

This Release of Security Interest (this "*Release*"), dated as of August 8, 2008, is granted by The Bank of New York Trust Mellon Company, N.A. (formerly known as The bank of New York Trust Company, N.A.), as Collateral Agent (in such capacity, the "**Collateral Agent**") for various lenders ("**Holders**") under the Security Agreement by and among the Collateral Agent, Conexant Systems, Inc., a Delaware corporation (the "**Conexant**"), and certain of Conexant's Subsidiaries, dated as of November 13, 2006 (as amended, restated, supplemented or otherwise modified from time to time, the "**Security Agreement**"). Each capitalized term used herein and not defined herein shall have the meaning ascribed thereto in the Security Agreement.

WHEREAS, the Collateral Agent is, on behalf of the Holders, the holder of security interests in the Patents and/or Patent Applications listed on Schedule A (hereinafter collectively referred to as the "**Patent Collateral**"), pursuant to the Security Agreement recorded in the U.S. Patent and Trademark Office on November 22, 2006 at Reel 018711 Frame 0818.
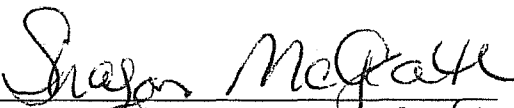
WHEREAS, Conexant has informed the Collateral Agent of the sale (the "**Transaction**") of the Patent Collateral and certain assets related exclusively to its Set-top Box business ("**Purchased Assets**") to NXP B.V., a Dutch company (the "**Purchaser**"), pursuant to that certain Assets Purchase Agreement (the "**Purchase Agreement**"), among Conexant and the Purchaser.

NOW, THEREFORE, Pursuant to the authority granted to the Collateral Agent under Section 10 of the Security Agreement and in accordance with the provisions of Section 11.14 of the Security Agreement, the Collateral Agent, on behalf of the Holders hereby releases the security interests granted in the Patent Collateral pursuant to the Security Agreement recorded in the U.S. Patent and Trademark Office on November 22, 2006 at Reel 018711 Frame 0818.

Conexant or Conexant's authorized representative will file UCC Financing Statement Amendment(s) and/or UCC Termination Statements with the applicable filing office(s) in order to memorialize the release of any security interest of the Collateral Agent in the Patents Collateral and Purchased Assets.

IN WITNESS WHEREOF, the Collateral Agent has caused this Release to be executed as of the date set forth above.

THE BANK OF NEW YORK MELLON TRUST COMPANY, N.A.,
as Collateral Agent

By: _Sharon McGrath_
Name: _Sharon McGrath_
Title: _Vice President_

Schedule A

(Patent Collateral)

Case No.	Title	Application Number	Filing Date	Patent Number	Issue Date
01CXT0228D	Integrated Programmable Tuner	10/210,591	07/03/2002		
02CXT0048D	Multi-Rate Encoding and Decoding Scheme	10/284,121	10/29/2002		
00CXT0294D	Signal Derotating Receiver	10/340,577	01/10/2003		
00CXT0298D	Guard Interval Analysis Method and Apparatus	10/340,854	01/10/2003		
02CXT0070D	Architecture for a Flexible and High-Performance Gateway Cable Modem	10/675,566	09/30/2003		
02CXT0017D	Fast Channel Scanning and Acquisition Algorithms for Cable Modem Applications	10/677,110	10/01/2003		
04CXT0021D	Method and Apparatus for Efficient Computation of Check Equations in Periodical Low Density Parity Check (LDPC) Codes	10/867,355	06/14/2004		
02CXT0037D	Simultaneous Multiple Channel Receiver	10/882,083	06/29/2004		
02CXT0071D	Satellite Multi-Choice Switch System	11/026,670	12/31/2004		
05CXT0006D	System and Method for a Highly Linear, Low Power Mixer	11/096,217	03/31/2005		
04CXT0083D	System and Method for Detecting Known Sequence in Transmitted Sequence	11/194,107	07/28/2005		
05CXT0092D	De-Blocking and De-Ringing Systems and Methods	11/233,674	09/23/2005		
04CXT0027D	Systems and Method for a Highly Integrated, Multi-Mode tuner	11/244,568	10/06/2005		
05CXT0093D	Multi-Channel	11/303,876	12/16/2005		

Case No.	Title	Application Number	Filing Date	Patent Number	Issue Date
	LDPC Decoder Architecture				
05CXT0082D	Multipath Processing Systems and Methods	11/522,583	09/18/2006		
97RSS240	Digital Video Converter Box for Subscriber/Home With Multiple Television Sets	09/491,032	01/25/2000	RE39,202	07/18/2006
00CXT0147N	Memory Transformation Apparatus and Method	07/371,167	06/26/1989	5,095,525	03/10/1992
97RSS263	System for Detecting Voltage Pulses of a Particular Magnitude	802,072	12/03/1991	5,198,703	03/30/1993
97RSS243	Finite Field Parallel Multiplier	07/990,524	12/15/1992	5,272,661	12/21/1993
97RSS245	L-Band Tuner With Quadrature Downconverter for PSK Data Applications	07/850,544	03/13/1992	5,325,401	06/18/1994
97RSS298	System for Determining the Time at Which an Analog Voltage Crosses a Voltage Threshold	006,823	01/21/1993	5,367,203	11/22/1994
97RSS247	Method and Apparatus for Performing Finite Field Division	938,109	08/31/1992	5,379,243	01/03/1995
97RSS222	Digital Controlled Phase Shifter	994,931	12/22/1992	5,521,499	05/28/1996
97RSS249	Tuner with Quadrature Downconverter for Pulse Amplitude Modulated Data Applications	08/263,602	06/22/1994	5,528,633	01/18/1996
97RSS294	Apparatus for, and Methods of, Providing a Universal Format of Pixels and for Scaling Fields in the Pixels	08/319,427	12/06/1994	5,542,041	07/30/1996
97RSS573	Delay Line providing an Adjustable delay in Response to Binary Input Signals	382,677	02/02/1995	5,554,950	09/10/1996
97RSS106	Cable Modem Optimized for High-Speed Data Transmissions	08/990,279	12/15/1997	5,986,691	11/16/1999

Case No.	Title	Application Number	Filing Date	Patent Number	Issue Date
	From the Home to the Cable Head				
95E021	A Perceptually Motivated Constrained Trellis Based Rate Control Method and Apparatus for Low Bit Rate Video Coding	08/802,042	02/18/1997	6,043,844	03/28/2000
99RSS148	Wideband Variable Gain Amplifier	09/480,831	01/10/2000	6,271,726	08/07/2001
98RSS131	Clock/Data Smoother for FEC Decoder and Full Rate DDS Clock	09/272,760	03/19/1999	6,501,809	12/31/2002
99RSS107	Method and Apparatus for Upstream Burst Transmission Synchronization in Cable Modems	09/415,612	10/09/1999	6,526,070	02/25/2003
99RSS107	Method and Apparatus for Upstream Burst Transmission Synchronization in Cable Modems	09/908,759	07/19/2001	6,553,040	04/29/2003
99RSS107	Method and Apparatus for Upstream Burst Transmission Synchronization in Cable Modems	09/909,044	07/19/2001	6,556,591	04/29/2003
99RSS198	Architecture and Method for the Improved Decoding of a Serially Concatenated Block and Convolutional Code	09/492,962	01/28/2000	6,606,724	08/12/2003
00CXT0348D	Interrupt Driven Interface Coupling a Programmable Media Access Controller and a Process Controller	09/516,465	03/01/2000	6,665,752	12/16/2003
04CXT0031A	Adaptive Image Data Compression	09/588,266	05/06/2000	6,724,817	04/20/2004
98RSS130	Automatic Power Control for Cable Modem Upstream	09/272,632	03/18/1999	6,724,829	04/20/2004

Case No.	Title	Application Number	Filing Date	Patent Number	Issue Date
98RSS409	System of and Method for Gamma Correction of Real-Time Video	09/450,643	12/16/2002	6,727,959	04/27/2004
00CXT0009D	Fully Integrated Broadband Tuner	09/497,717	02/04/2000	6,731,712	05/04/2004
00CXT0297D	Method and Apparatus For Correcting Phase Imbalance in Received In-Phase and Quadrature Signals	09/698,567	10/26/2000	6,765,623	07/20/2004
00CXT0366D	Sychronization of a Communications System	09/788,171	07/15/2004	6,769,093	07/20/2006
04CXT0034A	Implementation of Wavelet Functions in Hardware	09/736,891	12/13/2000	6,785,700	08/31/2004
00CXT0361D	System for Carrier Phase Tracking of Coded Symbols Using Reliability Metrics for Symbol Estimates	09/715,877	11/17/2000	6,795,512	09/21/2004
99RSS198	Iterative Decoder Employing Multiple External Code Error Checks to Lower the Error Floor	09/865,958	05/25/2001	6,810,502	10/26/2004
00CXT0316D	Cable Modem Having A Programmable Media Access Controller	09/785,035	02/16/2001	6,816,940	11/09/2004
00CXT0362D	Iterative Carrier Phase Tracking Decoding System	09/729,652	12/04/2000	6,856,656	02/15/2005
00CXT0369D	System of and Method for Decoding Trellis Codes	10/013,492	12/31/2001	6,865,711	03/08/2005
99RSS163	Soft Trellis Slicer for Improving the Performance of a Decision-Directed Phase Tracker	09/668,231	09/22/2000	6,882,690	04/19/2005
00CXT0371D	System for MPSK Symbol Hypothesis	09/815,149	03/22/2001	6,922,438	07/26/2005
03CXT0005D	Optimum RF VCO Structure	10/676,803	09/30/2003	6,943,635	09/13/2005
00CXT0370D	System of and Method for Decoding Trellis	10/013,490	12/31/2001	6,973,615	12/06/2005

Case No.	Title	Application Number	Filing Date	Patent Number	Issue Date
	Codes				
00CXT0330D	Programmable Pattern Match Engine	09/516,284	03/01/2000	7,010,802	03/07/2006
00CXT0550D	Method and Apparatus for Scheduling Upstream Data Packets in a Broadband Communication System	09/892,069	06/26/2001	7,016,376	03/21/2006
00CXT0366D	Sychronization of a Communications System	10/893,673	02/14/2001	7,065,703	07/27/2004
99RSS197	Burst Reliability and Error Locator for Trellis Codes	10/350,303	01/22/2003	7,103,831	09/05/2006
00CXT0292D	Method and Apparatus for a Demodulator Circuit	10/105,731	03/22/2002	7,106,810	09/12/2006
05CXT0005D	Systems and Methods For Accurate Analog Quadrature Signal Generation Using a Delay Locked Loop	11/096,214	03/31/2005	7,123,103	10/17/2006
03CXT0033D	A Low-Cost High Performance Front-End Active Splitter for a System Containing Multiple Reception Units	10/750,989	12/31/2003	7,142,060	11/28/2006
00CXT0009D	Fully Integrated Broadband Tuner	10/786,921	02/25/2004	7,177,382	02/13/2007
00CXT0352D	Method and Apparatus for Flexibly Filtering Scheduling Messages in a Communication System	09/966,374	09/27/2001	7,206,321	04/17/2007
00CXT0539D	Method and Apparatus for a Multicarrier Receiver Circuit with Guard Interval Size Detection	10/170,165	09/27/2001	7,206,361	04/17/2007
00CXT0364D	Symbol Reliability Determination and Symbol Pre-Selection Based on Reliability Criteria	10/368,017	02/14/2003	7,231,005	06/12/2007

Case No.	Title	Application Number	Filing Date	Patent Number	Issue Date
05CXT0005D	Systems and Method for Automatic Quadrature Phase Imbalance Compensation Using Delay Locked Loop	11/460,298	07/27/2006	7,298,222	11/20/2007
04CXT0074D	Delay-Locked Loop(DLL) Locking Scheme Using Phase-Frequency Detector(PFD) State Hopping Via Pulse Swallowing	11/194,085	07/29/2005	7,301,379	11/27/2007
00CXT0374D	SNR-Related Parameter Estimation Method and System	09/870,926	05/30/2001	7,310,369	12/18/2007
99RSS198	Iterative Decoder Employing Multiple External Code Error Checks to Lower the Error Floor	10/892,738	07/16/2004	7,310,768	12/18/2007
03CXT0014D	Procedure For BPSK Modulation with Reduced Envelope Peaking	10/611,325	06/30/2003	7,352,797	04/01/2008