

PATENT ASSIGNMENT

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CONVEYING PARTY DATA	
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Morningside, Columbia University New York	07/02/2008
RECEIVING PARTY DATA	
Name:	National Science Foundation
Street Address:	4201 Wilson Blvd
Internal Address:	Room 1265
City:	Arlington
State/Country:	VIRGINIA
Postal Code:	22230
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	10258221
CORRESPONDENCE DATA	
Fax Number:	(703)292-9041
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	703-292-7896
Email:	fhixson@nsf.gov
Correspondent Name:	Faith E Hixson
Address Line 1:	4201 Wilson Blvd
Address Line 2:	Room 1265
Address Line 4:	Arlington, VIRGINIA 22230
NAME OF SUBMITTER:	Faith E. Hixson
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Sign and Fax this to (301) 480-0272

Invention Title: Low Latency FIFO Circuit For Mixed Clock Systems

IR 100-060

Inventor (s): Steven Nowick

U.S. Filing/Issue Date: 10/21/2002

Patent or Application Serial No: 10/258,221

Grant/Contract Number(s): 9734803, COR9734803

Foreign Applications filed/intended in (countries):

The invention identified above is a Subject invention under 35 U.S.C. 200, et seq., and the Standard Patent Rights clause at 37 CFR 401.14, FAR 52.227-11 or FAR 52.227-12 (if applicable) which are included among the terms of the above identified grant or contract award from the United State Government. The document is confirmatory of:

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The Government is hereby granted an irrevocable power to inspect and make copies of the above-identified patent application.

Signed this 22nd day of May, 20 06

By Scott G. Hamilton (Signature)
(Name of Grantee/Contractor Official)

Title SCOTT G. HAMILTON
SENIOR DIRECTOR
SCIENCE & TECHNOLOGY VENTURES

For Columbia Univ New York Morningside
(Grantee/Contractor Organization)

At 412 Low Library MC 4308 535 W. 116th St New York, NY 10027 US
(Business Address)

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