Client Code: KM2111.004DV1

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Andigilog, Inc.	160 Greentree Drive, Suite 101
Additional name(s) of conveying party(ies) attached?	Dover, DE 19904
() Yes (X) No	Additional name(s) of receiving party(ies) attached?
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	(X) Patent Application No.: 11/796,600 Filing Date: April 27, 2007
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7. Total fee (37 CFR 1.21(h)): \$40.00	8. Deposit account number: 11-1410
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For good and valuable consideration, the receipt of which is hereby acknowledged, Andigilog, Inc., a Delaware corporation, with an office at 8380 S. Kyrene Road, Suite 101, Tempe, AZ 85284 ("Assignor"), does hereby sell, assign, transfer, and convey unto Dolpan Audio, LLC, a Delaware limited liability company, having an address at 160 Greentree Drive, Suite 101, Dover, DE 19904 ("Assignee"), or its designees, all right, title, and interest in and to any and all of the following (collectively, the "Patent Rights"):

- (a) the provisional patent applications, patent applications and patents listed in the table below (the "Patents");
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- (c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);
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Patent or

Application No.	Country	Filing Date	Title of Patent and First Named Inventor
6252209 (09/234587)	US	6/26/2001 (1/21/1999)	Adaptive temperature control circuit with PWM output Liepold, Carl F.
7030584 (10/985,754)	US	4/18/2006 (11/10/2004)	Controller arrangement Alberkrack, Jade H.
7276867 (11/080344)	US	10/2/2007 (3/15/2005)	Controller arrangement with adaptive non- overlapping commutation Alberkrack, Jade H.; Brannen, Robert Alan
7148642 (11/412405)	us .	12/12/2006 (4/27/2006)	Controller arrangement with automatic power down Brannen, Robert Alan; Alberkrack, Jade H.
7237951 (11/096701)	us	7/3/2007 (3/31/2005)	Substrate based temperature sensing Cave, David L
11/796,600	US	4/27/2007	Substrate based temperature sensing Cave, David L
7262570 (11/080343)	us	8/28/2007 (3/15/2005)	Motor controller with enhanced noise immunity unbuffered hall sensors Alberkrack, Jade H.; Brannen, Robert Alan
10/704,368	US	11/8/2003	Temperature sensing circuit Liepold, Carl F.; Aykroyd, Craig M; McLin, Jonathan Daniel
10/897,217	US	7/22/2004	Temperature manager Liepold, Carl F.
11/446,036	us	6/2/2006	Bandgap circuit with temperature correction Cave, David L
11/446,567	US	6/2/2006	Trimmable bandgap circuit Cave, David L

<u>Patent or</u>

Application No.	Country	Filing Date	Title of Patent and First Named Inventor
11/484,869	US	7/11/2006	Over-voltage protected semiconductor device Alberkrack, Jade H; Cave, David L
11/484,879	บร	7/11/2006	Over-voltage protected semiconductor device fabrication Cave, David L; Alberkrack, Jade H
11/492,701	US	7/25/2006	Synchronous substrate injection clamp Alberkrack, Jade H; Cave, David L; Bushey, Thomas Peter; Brannen, Robert A.
11/700,731	US	1/30/2007	Digital delay circuit Brannen, Robert Alan
11/796,602	US	4/27/2007	DC motor variable kick-start Brannen, Robert Alan; Alberkrack, Jade H; Bushey, Thomas Peter
11/799,331	US	5/1/2007	Noise suppression for hall sensor arrangements Brannen, Robert Alan; Alberkrack, Jade H; Bushey, Thomas Peter
12/080,126	US		Semiconductor structure Tuten, Derrick; Cave, David L
61/043,199	US	4/8/2008	Method for measurement system error correction Tuten, Derrick
9,413,754	Taiwan	10/25/2005	Controller arrangement Alberkrack, Jade H.

Assignor represents, warrants and covenants that:

- (1) Assignor has the full power and authority, and has obtained all third party consents, approvals and/or other authorizations required to enter into this Agreement and to carry out its obligations hereunder, including the assignment of the Patent Rights to Assignee; and
- (2) Assignor owns, and by this document assigns to Assignee, all right, title, and interest to the Patent Rights, including, without limitation, all right, title, and interest to sue for infringement of the Patent Rights. Assignor has obtained and properly recorded previously executed assignments for the Patent Rights as necessary to fully perfect its rights and title therein in accordance with governing law and regulations in each respective jurisdiction. The Patent Rights are free and clear of all liens, claims, mortgages, security interests or other encumbrances, and restrictions. There are no actions, suits, investigations, claims or proceedings threatened, pending or in progress relating in any way to the Patent Rights. There are no existing contracts, agreements, options, commitments, proposals, bids, offers, or rights with, to, or in any person to acquire any of the Patent Rights.

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WITNESS my hand and official seal.

PATENT

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RECORDED: 10/15/2008

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County of Santa Clara	}
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