Form PTO-1595 (Rev. 09/08) OMB No. 0651-0027 (exp. 10/31/2008)



10-17-2008

	103530246				
To the Director of the U.S. Patent and Trademark Office: Pleas	se record the attached documents or the new address(es) below.				
Name of conveying party(ies): 2. Name and address of receiving party(ies)					
Micron Technology, Inc. (9/26/2008)	Name: Aptina Imaging Corporation				
	Internal Address:				
Additional name(s) of conveying party(ies) attached? Yes X No	Street Address:				
3. Nature of conveyance/Execution Date(s):	c/o Citco Trustees (Cayman) Limited				
Execution Date(s): in parentheses after inventor name	Regatta Office Park				
X Assignment Merger Change of Name	West Bay Road				
Security Agreement Joint Research Agreement	City: Grand Cayman				
Government Interest Assignment	State:				
Executive Order 9424, Confirmatory License					
	Country: Cayman Islands Zip: Y1-1205				
Other	Additional name(s) & address(es) Yes X No attached?				
4. Application or patent number(s):	This document is being filed together with a new application.				
A. Patent Application No.(s)	B. Patent No.(s)				
11/217,877	B. Tatone No.(b)				
·					
Additional numbers attached	d? Yes X No				
5. Name and address to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved:				
Name: Thomas J. D'Amico					
DICKSTEIN SHAPIRO LLP	7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00				
Internal Address: Atty. Dkt.: M4065.1311/P1311					
Street Address: 1825 Eye Street, NW	Authorized to be charged to deposit account				
	Enclosed				
	None required (government interest not affecting title)				
City: Washington	8. Payment Information				
State: DC Zip: 20006-5403	-				
Phone Number: (202) 420-2232	-				
Fax Number: (202) 420-2201	Deposit Account Number				
Email Address: DAmicoT@dicksteinshapiro.com	Authorized User Name				
9. Signature:	- 1				
Ta X					
Signature	October 10, 2008				
	Date				
Thomas J. D'Amico - 28,371 Name of Person Signing	fotal number of pages including cover sheet, attachments, and documents:				
Table of Folder organing	18/14/2998 WASFAUL 00000938 11217877				
	BA Er. ROD:				
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DSMDB-2510944v01

PATENT ASSIGNMENT AGREEMENT

1. **Definitions**

- (a) "Agreement" shall mean this Patent Assignment Agreement.
- (b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.
- (c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.
 - (d) "Effective Date" shall mean October 3, 2008.

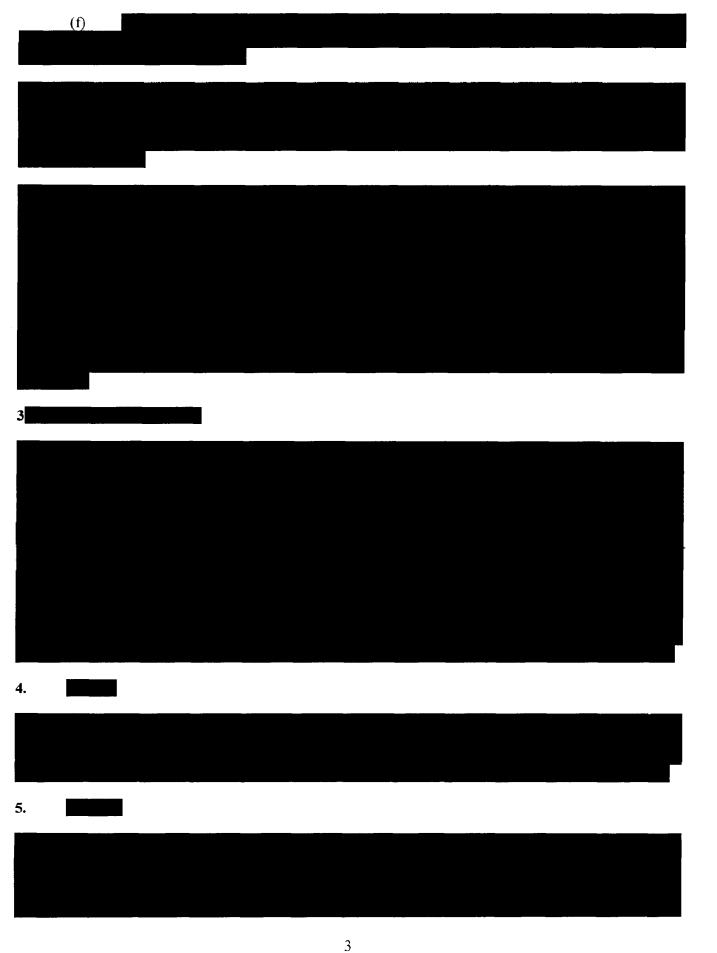


- (g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

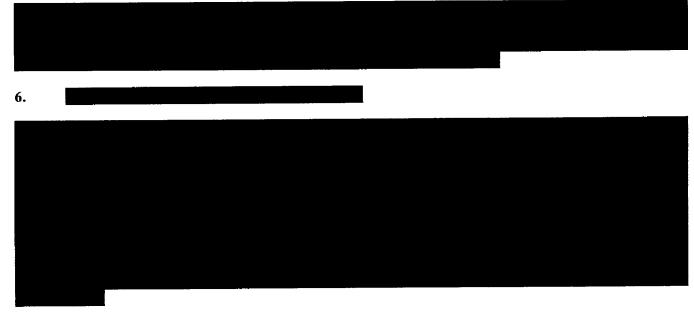
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- (j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (k) . 2.
- (a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee.
- (b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.
- (c)
- (d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.
- (e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any,

 Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.



PATENT REEL: 021703 FRAME: 0651



7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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	Agreed to by:
	Micron Technology, Inc.
	Signed:
	Date: 9/16/42 , 2008
Notarization State of Idaho County of Ada	
On 9/26/08, before me, far a land (notary name)	, personally appeared the above-named Skin R. App Life.
who executed this Assignment in my presence and ack authorized capacity for the purposes set forth herein.	nowledged to me that he did so of his own free will and in his
	Signed:
REVIEWED NTI LEGAL	My commission expires: 8/12///
R. Spinning	Agreed to by:
OT AA LIGHT OF OF OR THE STATE OF THE STATE	Aptina Imaging Corporation Signed: Sour for John John John John John John John John
Notarization State of Idaho County of Ada	
On 9/26/08, before me, Poula R. Spans (notary name)	, personally appeared the above-named Thrus L Laur, Tr.
authorized capacity for the purposes set forth herein.	nowledged to me that he did so of his own free will and in his
A R. S.	My commission expires:

[Signature page to the Patent Assignment Agreement]

PATENT REEL: 021703 FRAME: 0654

Aptina Applications

FILE NUMBER	TITLE	STATUS .	COUNTRY NAME	APPLICATION NUMBER	DATE FILED	
	MANUFACTURING MICROELECTRONIC IMAGING UNITS AT THE WAFER LEVEL					
2004-0782-00/JP	MIGROELECTRONIC, IMAGINGUNITESAND, METHODS SUBJECT, WAS UPACTURING MIGROELECTRONIC, MAGINGUNITS AT THE WAFER LEVEL.	Pending (Japan		Aug.11,2006	
2004-0782.00/KR	MICROELECTRONIC IMAGING UNITS AND METHODS OF MANUFACTURING MICROELECTRONIC IMAGING UNITS AT THE WAFER LEVEL	Pending	Republic of Korea	1020087007940	Aug 31, 2006	
2004-078Z,00/FC	MICROELECTRONIC MAGINGLINITE AND METHODE ORIE HANUFICITURING MICROELECTRONIC MAGINGUINTS AT THE WAFER LEVEL	Completed (PCT	PCT/US2008034011	Aug 31, 2006.	
2004-0782.00/SG	MICROELECTRONIC IMAGING UNITS AND METHODS OF MANUFACTURING MICROELECTRONIC IMAGING UNITS AT THE WAFER LEVEL	Pending	Singapore	2008016784	Aug 31, 2006	
2004-0782.00/TW	MICROELECTRONIC :: MAGING WITE AND METHORS DIP SWAP MANUE ASSERING MICROELECTRONIC MACING UNITS AT THE WAFER LEVEL	Pedding	awah R.O.C.	095132427	Sep 1, 2005 ** 1,	
2004-0782.00/US	MICROELECTRONIC IMAGING UNITS AND METHODS OF MANUFACTURING MICROELECTRONIC IMAGING UNITS AT THE WAFER LEVEL	Allowed	United States of America	11217877	Sep 1, 2005	4

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RECORDED: 10/10/2008

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