

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Marvell Semiconductor, Inc.	09/03/2008
RECEIVING PARTY DATA	
Name:	Marvell International Ltd.
Street Address:	Argyle House, 41A Cedar Avenue
City:	Hamilton
State/Country:	BERMUDA
Postal Code:	Hm12
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12104254
CORRESPONDENCE DATA	
Fax Number:	(503)796-2900
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	(503) 222-9981
Email:	patent@schwabe.com
Correspondent Name:	Schwabe, Williamson & Wyatt, P.C.
Address Line 1:	1211 SW Fifth Avenue, Suite 1900
Address Line 4:	Portland, OREGON 97204
ATTORNEY DOCKET NUMBER:	MP1862-157236
NAME OF SUBMITTER:	Kevin T. LeMond
Total Attachments: 3	
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ASSIGNMENT
Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application(s) identified below (hereafter sometimes called the "Patent Application(s)").

Marvell International Ltd., a corporation of Bermuda, having a place of business at Argyle House, 41A Cedar Avenue, Hamilton, Hm12, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s) and all inventions described and claimed therein or entitled to the benefit thereof.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) US or equivalent and other good and valuable consideration paid to it, Assignor hereby sells and assigns to the Assignee the entire right, title and interest in and to the Patent Application(s) identified below, and all inventions described and claimed therein or entitled to the benefit thereof, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, reexaminations, divisions and continuations of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

<u>Marvell Semiconductor, Inc.</u> <u>Case No.</u>	<u>U. S. Serial No.</u>	<u>U. S. Filing Date</u>	<u>Title</u>
MP1862	12/104,254	16 April 2008	LOW BASE RESISTANCE BIPOLAR JUNCTION TRANSISTOR ARRAY
MP1862PR	60/912,046	16 April 2007	HIGH DENSITY LOW BASE RESISTANCE BJT
MP1887PR	60/913,924	25 April 2007	PCM BJT CELLS WITH LOW BASE RESISTANCE
MP1887PR2	60/938,331	16 May 2007	PCM BJT CELLS WITH LOW BASE RESISTANCE

By its undersigned representative, the Assignor agrees

- a. to execute all papers necessary in connection with the Patent Application(s) and any continuing, divisional, reissue, reexamination or corresponding application

thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;

b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application(s) or any continuation, division, reissue or reexamination thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and

c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application(s) and on any continuation, division, reissue or reexamination of any of the Patent Application.

The Assignor hereby covenants that it has the full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

Marvell Semiconductor, Inc., a California corporation, certifies that it is the Assignee of the entire right, title and interest in the Patent Application(s) identified above by virtue of Assignment from the inventors of the Patent Application(s) identified above. Assignment to be recorded in the Patent and Trademark Office as identified below.

<u>Marvell Semiconductor, Inc. Case No.</u>	<u>U. S. Serial No.</u>	<u>Assignment Execution Date</u>
MP1862	12/104,254	April 14, 2008 and April 15, 2008
MP1862PR	60/912,046	April 14, 2008 and April 15, 2008
MP1887PR	60/913,924	April 14, 2008 and April 15, 2008
MP1887PR2	60/938,331	April 14, 2008 and April 15, 2008

The undersigned has reviewed the documents in the Patent Application(s) identified above and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this certificate on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize

the validity of the application or any patent issuing thereon.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the undersigned's name.

Marvell Semiconductor, Inc.

By:


Eric Janofsky

Title: Vice President Intellectual Property and General Patent Counsel

Date: September 3, 2008