

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	10/03/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citgo Trustees (Cayman) Limited
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 2	
Property Type	Number
Patent Number:	6870565
Application Number:	11051554
CORRESPONDENCE DATA	
Fax Number:	(202)420-2201
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	202-420-2200
Email:	andersont@dicksteinshapiro.com
Correspondent Name:	DICKSTEIN SHAPIRO LLP
Address Line 1:	1825 EYE STREET, NW
Address Line 4:	WASHINGTON, DISTRICT OF COLUMBIA 20006
ATTORNEY DOCKET NUMBER:	M4065.0856/P856
NAME OF SUBMITTER:	Thomas D. Anderson
<p>Total Attachments: 7</p> <p>source=asgnt#page1.tif</p> <p>source=asgnt#page2.tif</p>	

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PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3

[REDACTED]

[REDACTED]

4.

[REDACTED]

[REDACTED]

5.

[REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/26/08, 2008

Notarization

State of Idaho

County of Ada

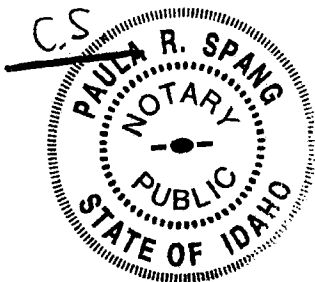
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven R. Appleton

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas L. Lowe, Jr.

Date: 9/26/08, 2008

Notarization

State of Idaho

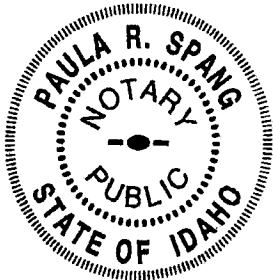
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Lowe, Jr.

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
PBIT-0044.02/US	NONLINEAR FLASH ANALOG TO DIGITAL CONVERTER USED IN AN ACTIVE PIXEL SYSTEM	Abandoned	United States of America	11175041	Jul 6, 2005
PBIT-0046.00/PR	ACTIVE PIXEL SENSOR WITH COLUMN PARALLEL CENTROID A TO D CONVERTER	Abandoned	United States of America	60106490	Oct 30, 1998
PBIT-0047.00/PR	FLOATING PINNED PHOTOGATE ACTIVE PIXEL SENSOR	Abandoned	United States of America	60100751	Sep 16, 1998
PBIT-0051.00/PR	ACTIVE PIXEL SENSOR WITH INTERSPERSED COLOR CODING	Abandoned	United States of America	60106278	Oct 29, 1998
PBIT-0051.00/US	ACTIVE PIXEL SENSOR WITH INTERSPERSED COLOR CODING	Pending	United States of America	09430734	Oct 29, 1999
PBIT-0053.00/US	COLOR CORRECTION OF MULTIPLE COLORS USING A CALIBRATED TECHNIQUE	Pending	United States of America	09209982	Dec 9, 1998
PBIT-0054.00/PR	Pxel for micropower, microdot CMOS active sensor	Abandoned	United States of America	60104295	Oct 14, 1998
PBIT-0056.01/US	LOW POWER SIGNAL CHAIN FOR IMAGE SENSORS	Pending	United States of America	11059526	Feb 17, 2005
PBIT-0059.00/PR	Use of Clear Plastic QFP for Packaging of CMOS Active Pixel Image Sensors	Abandoned	United States of America	60111597	Nov 18, 1998
PBIT-0060.00/PR	High-Speed Dual Ported Readout for CMOS Image Sensor	Abandoned	United States of America	60109922	Nov 24, 1998
PBIT-0060.01/US	SEMICONDUCTOR IMAGING SENSOR ARRAY DEVICES WITH DUAL-PORT DIGITAL READOUT	Pending	United States of America	11051554	Feb 7, 2005
PBIT-0061.00/PR	Techniques to Improve Digital Image Sensor Performance and Proven in Niagra	Abandoned	United States of America	60125255	Mar 19, 1999
PBIT-0061.00/US	HIGH-SPEED SAMPLING OF SIGNALS IN ACTIVE PIXEL SENSORS	Pending	United States of America	09527422	Mar 17, 2000
PBIT-0061.01/US	HIGH-SPEED SAMPLING OF SIGNALS IN ACTIVE PIXEL	Pending	United States of America	11473130	Jun 23, 2006

Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
6853045	1998-0272.02/US	PIXEL CELL WITH HIGH STORAGE CAPACITANCE FOR A CMOS IMAGER	United States of America	Feb 8, 2005
6858480	1998-0449.04/US	RETROGRADE WELL STRUCTURE FOR A CMOS IMAGER	United States of America	Feb 22, 2005
6858829	AVGO-0031.00/US	AVALANCHE PHOTODIODE ARRAY BIASING DEVICE AND AVALANCHE PHOTODIODE STRUCTURE	United States of America	Feb 22, 2005
6859227	PBIT-0084.00/US	ACTIVE PIXEL SENSOR WITH REDUCED FIXED PATTERN NOISE	United States of America	Feb 22, 2005
6861634	2002-0307.00/US	CMOS ACTIVE PIXEL SENSOR WITH A SAMPLE AND HOLD CIRCUIT HAVING MULTIPLE INJECTION CAPACITORS AND A FULLY DIFFERENTIAL CHARGE MODE LINEAR SYNTHESIZER WITH SKEW CONTROL	United States of America	Mar 1, 2005
6861637	2002-0432.01/US	PROVIDING CURRENT TO COMPENSATE FOR SPURIOUS CURRENT WHILE RECEIVING SIGNALS THROUGH A LINE	United States of America	Mar 1, 2005
6870149	PBIT-0072.01/US	Superposed Multi-Junction Color APS	United States of America	Mar 22, 2005
6870496	PBIT-0088.02/US	LOW VOLTAGE ANALOG-TO-DIGITAL CONVERTERS WITH INTERNAL REFERENCE VOLTAGE AND OFFSET	United States of America	Mar 22, 2005
6870565	PBIT-0060.00/US	SEMICONDUCTOR IMAGING SENSOR ARRAY DEVICES WITH DUAL PHOTODIGITAL READOUT	United States of America	Mar 22, 2005
6872640	2003-0168.00/US	SOI CMOS DEVICE WITH REDUCED DIBL	United States of America	Mar 29, 2005
6873363	PBIT-0068.00/US	TECHNIQUE FOR FLAGGING OVERSATURATED PIXELS	United States of America	Mar 29, 2005
6873364	PBIT-0056.00/US	LOW-POWER SIGNAL CHAIN FOR IMAGE SENSORS	United States of America	Mar 29, 2005
6878568	2002-0057.01/US	CMOS IMAGER AND METHOD OF FORMATION	United States of America	Apr 12, 2005
6881942	PBIT-0159.00/US	READOUT OF ARRAY-BASED ANALOG DATA IN SEMICONDUCTOR-BASED DEVICES	United States of America	Apr 19, 2005