PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Micron Technology, Inc.	10/03/2008

RECEIVING PARTY DATA

Name:	Aptina Imaging Corporation	
Street Address:	c/o Citco Trustees (Cayman) Limited	
Internal Address:	Regatta Office Park, West Bay Road	
City:	Grand Cayman	
State/Country:	CAYMAN ISLANDS	
Postal Code:	Y1-1205	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	11016376

CORRESPONDENCE DATA

Fax Number: (610)407-0701

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 610-407-0700

sseiler@ratnerprestia.com Email:

Correspondent Name: Kenneth N. Nigon

Address Line 1: 1235 Westlakes Drive, Suite 301

Address Line 2: RatnerPrestia

Address Line 4: Berwyn, PENNSYLVANIA 19312

ATTORNEY DOCKET NUMBER:	MICR-207US
NAME OF SUBMITTER:	Kenneth N. Nigon

Total Attachments: 11

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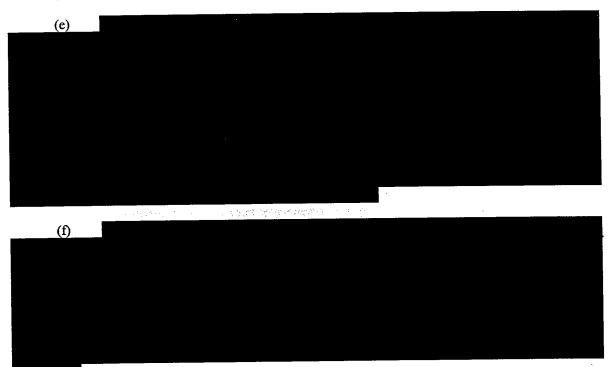
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PATENT ASSIGNMENT AGREEMENT

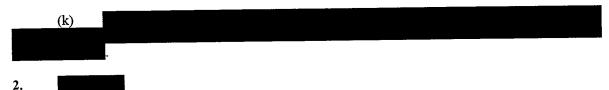
1. Definitions

- (a) "Agreement" shall mean this Patent Assignment Agreement.
- (b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.
- (c) "Assignor" shall mean **Micron Technology**, **Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.
 - (d) "Effective Date" shall mean October 3, 2008.



- (g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

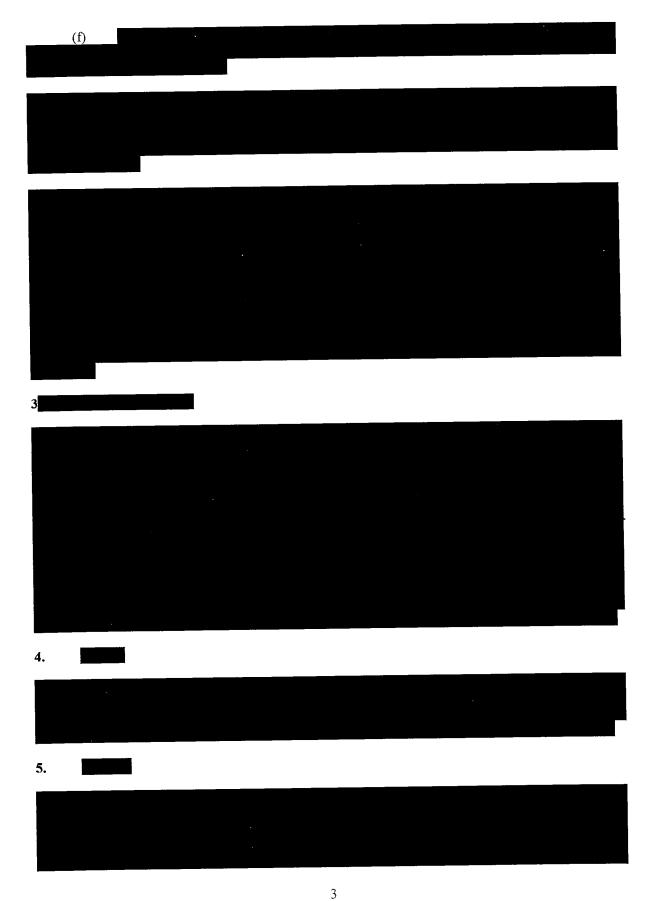


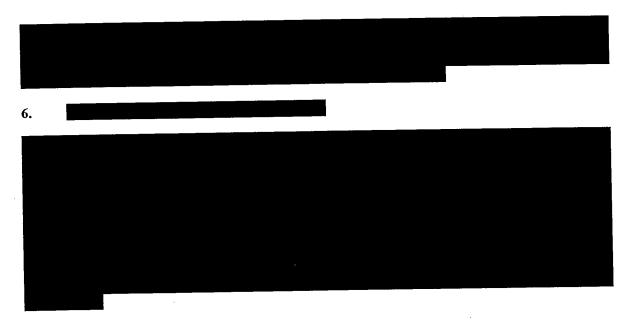
- (a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee.
- (b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.



- (d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee.
- (e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any,

 Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.





7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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	Agreed t	o by:		
	Micron	Technology, Inc.		
	Signed:	She Con		-
	Date:	spolus		_, 2008
Notarization State of Idaho County of Ada On 9/26/08, before me, find a light (notary name)				•
who executed this Assignment in my presence and ack authorized capacity for the purposes set forth herein.		. 1		
REVIEWED MTI LEGAL	М	gned: / commission expire	es: 8/12/11	
R. SO	Agreed Aptina Signed: Date:	Imaging Cornors	S- Lowr Ja	<u>-</u> _, 2008
Notarization State of Idaho County of Ada	,			
On 9/26/08, before me, Paula R. Spans (notary name)	_, persona	ally appeared the abo	ove-named Thomas	Lane, Jr.
who executed this Assignment in my presence and ack authorized capacity for the purposes set forth herein.		1	o of his own free wil	and in his
AR. SOMETHINGS OF THE PROPERTY TO THE PROPERTY		gned:	98 8/12/11	

[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"

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ATTACHMENT "B"

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Aptina Applications

FILE NUMBER	ATITLE AND AND AND AND	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
AVG0-0085.00/JP	Method And System For	Pending .	Japan	2007546662	Oct 31, 2005
	Determining Motion Based On Difference Image Correlation				
AVGO-0085.00/KR	Method And System For Determining Motion Based On Difference Image Correlation	Pending	Republic of Korea	1020077016224	Oct 31, 2005
AVGO-0085.00/PC	Method And System For Determining Motion Based On Difference Image Correlation	Completed	PCT	PcTUS2005039493	Oct 31, 2005
AVGO-0085.00/TW	Method And System For Determining Motion Based On Difference Image Correlation	Pending	Taiwan R.O.C.	94128034	Aug 17, 2005
AVGO-0086.00/EP	Low Noise Active Reset Readout For Image Sensors	Pending	European Patent Office	009667767	Sep 20, 2000.
AVGO-0086.00/JP	Low Noise Active Reset Readout For Image Sensors	Pending	Japan	2001525963	Sep 20, 2000
AVGO-0086.00/WO	Low Noise Active Reset Readout For Image Sensors	Completed	POT	US0025848	Sep 20: 2000
AVGO-0087.00/CN	Multi-Lens Imaging Systems And Method	Pending	China	2005800427538	Oct 31, 2005
AVGO-0087.00/EP	Multi-Lens Imaging Systems And Method	Pending	European Palent Office	058171240	Oct 31, 2005
AVGO-0087.00/JP	Multi-Lens Imaging Systems And Method	Pending	Japan	2007546661	Oct 31, 2005
AVGO-0087.00/KR	Multi-Lens Imaging Systems And Method	Pending	Republic of Korea	1020077015516	Oct 31, 2005
AVGO-0087.00/PC	Multi-Lens Imaging Systems And Method	Completed	PCT	PCTUS2005039249	Oct 31, 2005
AVGO-0087 00/SG	Multi-Lens Imaging Systems And Method	Pending	Singapore	2007044522	Oct 31; 2005
AVGO-0087.00/TW	Multi-Lens Imaging Systems And Method	Pending	Taiwan R.O.C.	94128033	Aug 17, 2005
AVG0-0087 00/US	Multi-Lens Imaging Systems	Pending	United States of America	11016376	Dec 15, 2004
AVGO-0088.00/EP	IMAGE SENSOR UTILIZING A LOW FPN HIGH GAIN CAPACITIVE TRANSIMPEDANCE AMPLIFIER	Pending	European Patent Office	019998269	Oct 17, 2003
AVGO-0088.00/PC	MAGE SENSOR UTILIZING	Completed	PCT	/(PCTUS0143384)	Nov 21, 2001

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ATTACHMENT "C"

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ATTACHMENT "D"

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ATTACHMENT "E"

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RECORDED: 11/25/2008