

## PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	10/03/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman) Limited
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	11232319
CORRESPONDENCE DATA	
Fax Number:	(610)407-0701
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	610-407-0700
Email:	eschmittinger@ratnerprestia.com
Correspondent Name:	RatnerPrestia
Address Line 1:	1235 Westlakes Drive
Address Line 2:	Suite 301
Address Line 4:	Berwyn, PENNSYLVANIA 19312
ATTORNEY DOCKET NUMBER:	MICR-233US
NAME OF SUBMITTER:	Kenneth N. Nigon
Total Attachments: 11 source=MICR-233USAssignment#page1.tif source=MICR-233USAssignment#page2.tif	

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## PATENT ASSIGNMENT AGREEMENT

### 1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: \_\_\_\_\_

Date: \_\_\_\_\_

, 2008

Notarization

State of Idaho

County of Ada

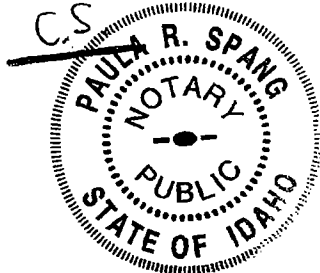
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven R. Appleton

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: \_\_\_\_\_

My commission expires: \_\_\_\_\_

REVIEWED  
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: \_\_\_\_\_

Date: \_\_\_\_\_

, 2008

Notarization

State of Idaho

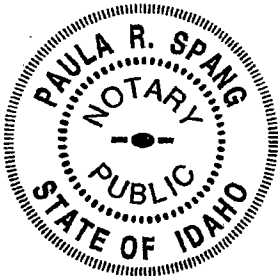
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Louie, Jr.

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: \_\_\_\_\_

My commission expires: \_\_\_\_\_



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"





ATTACHMENT "B"



## Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
AVGO-0146.00/GB	Imaging Device with Blur Reduction System	Pending	United Kingdom	06183354	Sep 18, 2006
AVGO-0146.00/HK	Imaging Device with Blur Reduction System	Pending	Hong Kong	071098326	Sep 10, 2007
AVGO-0146.00/JP	Imaging Device with Blur Reduction System	Pending	Japan	2006255355	Sep 21, 2006
AVGO-0146.00/KR	Imaging Device with Blur Reduction System	Pending	Republic of Korea	20060091193	Sep 20, 2006
AVGO-0146.00/TW	Imaging Device with Blur Reduction System	Pending	Taiwan R.O.C.	95134733	Sep 20, 2006
AVGO-0146.00/US	Imaging Device with Blur Reduction System	Pending	United States of America	11232319	Sep 21, 2005
AVGO-0147.00/US	System For Programmable Gate Array With Sensor Array	Pending	United States of America	11250323	Oct 15, 2005
AVGO-0149.00/FR	BINARY IMAGE SCALING	Abandoned	France	953042686	Jun 20, 1995
AVGO-0149.00/IT	BINARY IMAGE SCALING	Abandoned	Italy	953042686	Jun 20, 1995
AVGO-0149.00/JP	METHOD FOR BINARY IMAGE SCALING	Abandoned	Japan	PH07169393	Jul 5, 1995
AVGO-0150.00/EP	CMOS ACTIVE PIXEL SENSOR HAVING IN-PIXEL LOCAL EXPOSURE CONTROL	Pending	European Patent Office	993091479	Nov 17, 1999
AVGO-0150.00/JP	CMOS ACTIVE PIXEL SENSOR HAVING IN-PIXEL LOCAL EXPOSURE CONTROL	Pending	Japan	H11327857	Nov 18, 1999
AVGO-0151.00/JP	AMORPHOUS SILICON ACTIVE PIXEL SENSOR WITH RECTANGULAR READOUT LAYER IN A HEXAGONAL GRID LAYOUT	Pending	Japan	2000021718	Jan 31, 2000
AVGO-0152.00/JP	MULTI-PIXEL COLOR DETECTION/ELEVATED PIN PHOTO DIODE ACTIVE PIXEL SENSOR	Pending	Japan	H11341086	Dec 11, 1999
AVGO-0153.00/CN	DRIVE CIRCUIT WITH OVER-VOLTAGE PROTECTION FOR USE WITH PIXEL CELLS AND OTHER CIRCUITS	Abandoned	China	991118405	Jul 30, 1999
AVGO-0153.00/FR	DRIVE CIRCUIT WITH	Abandoned	France	993096080	Nov 30, 1999

ATTACHMENT "C"



ATTACHMENT "D"



ATTACHMENT "E"

