

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	09/26/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citgo Trustees (Cayman) Limited
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	7092021
CORRESPONDENCE DATA	
Fax Number:	(202)420-2201
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	202 420 2200
Email:	damicot@dicksteinshapiro.com
Correspondent Name:	Thomas J. D'Amico
Address Line 1:	1825 Eye Street NW
Address Line 4:	Washington, DISTRICT OF COLUMBIA 20006
ATTORNEY DOCKET NUMBER:	M4065.0831/P831
NAME OF SUBMITTER:	Maxine Rozar

OP \$40.00 7092021

Total Attachments: 7
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PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k)

2.

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c)

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/26/08, 2008

Notarization

State of Idaho
County of Ada

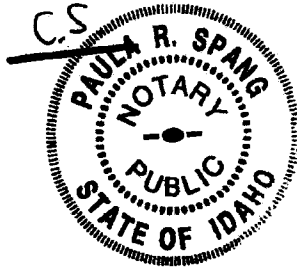
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Skon R. Appleton
(date) (notary name)

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas J. Lewis, Jr.

Date: 9/26/08, 2008

Notarization

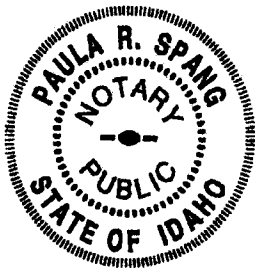
State of Idaho
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas J. Lewis, Jr.
(date) (notary name)

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"

IMAGING PATENTS

Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
		VOLTAGE OPERATION		
7078746	2003-0130.00/US	IMAGE SENSOR WITH FLOATING DIFFUSION GATE CAPACITOR	United States of America	Jul 18, 2006
7081608	2003-0714.00/US	PIXEL WITH DIFFERENTIAL READOUT	United States of America	Jul 25, 2006
7081922	AVGO-0032.00/US	Apparatus and Method for Obtaining Average Scene Intensity Information from a Photo Defector Array	United States of America	Jul 25, 2006
7084912	AVGO-0017.00/US	METHOD FOR REDUCING COHERENT ROW-WISE AND COLUMN-WISE FIXED PATTERN NOISE IN CMOS IMAGE SENSORS	United States of America	Aug 1, 2006
7084914	PBIT-0167.00/US	VARIABLE PIXEL CLOCK ELECTRONIC SHUTTER CONTROL	United States of America	Aug 1, 2006
7087944	2002-0057.00/US	IMAGER SENSOR HAVING A CHARGE STORAGE REGION PROVIDED WITHIN AN IMPLANT REGION	United States of America	Aug 8, 2006
7088392	AVGO-0028.00/US	DIGITAL IMAGE SYSTEM AND METHOD FOR IMPLEMENTING AN ADAPTIVE DEMOSAICING METHOD	United States of America	Aug 8, 2006
7088394	PBIT-0148.00/US	CHARGE MODE ACTIVE PIXEL SENSOR READ-OUT CIRCUIT	United States of America	Aug 8, 2006
7088917	AVGO-0091.00/US	BUBBLE MACRO MODE LENS	United States of America	Aug 8, 2006
7091058	1997-1490.04/US	METHOD OF FORMING A PHOTSENSOR COMPRISING A PLURALITY OF TRENCHES	United States of America	Aug 15, 2006
7091466	2003-0719.00/US	APPARATUS AND METHOD FOR PIXEL BINNING IN AN IMAGE SENSOR	United States of America	Aug 15, 2006
7091531	2003-0833.00/US	HIGH DYNAMIC RANGE PIXEL AMPLIFIER	United States of America	Aug 15, 2006
7091536	2002-0058.00/US	ISOLATION PROCESS AND STRUCTURE FOR CMOS IMAGERS	United States of America	Aug 15, 2006
7092021	PBIT-0112.00/US	FRAME SHUTTERING SCHEME FOR INCREASED FRAME RATE	United States of America	Aug 15, 2006
7095122	2004-0465.00/US	REDUCED-DIMENSION MICROELECTRONIC COMPONENT ASSEMBLIES WITH WIRE BONDS AND METHODS OF MAKING SAME	United States of America	Aug 22, 2006