

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	10/03/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman) Limited
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	10294686
CORRESPONDENCE DATA	
Fax Number:	(202)420-2201
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	202-420-2200
Email:	bakerm@dsmo.com
Correspondent Name:	DICKSTEIN SHAPIRO LLP
Address Line 1:	1825 EYE STREET NW
Address Line 4:	Washington, DISTRICT OF COLUMBIA 20006-5403
ATTORNEY DOCKET NUMBER:	M4065.0653/P653
NAME OF SUBMITTER:	Madeline E. Baker
Total Attachments: 7 source=Document#page1.tif source=Document#page2.tif source=Document#page3.tif	

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PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)

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(f)

A large rectangular area of the document is completely redacted with black ink, covering approximately six lines of text.

(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k)

2.

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c)

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. **General**

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: _____

Date: 9/26/08, 2008

Notarization

State of Idaho

County of Ada

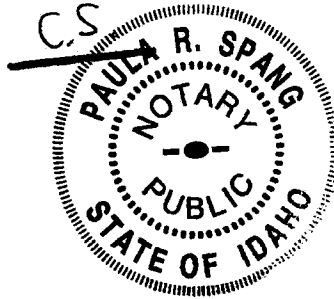
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven R. Appleton

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: _____

My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: _____

Date: 9/26/08, 2008

Notarization

State of Idaho

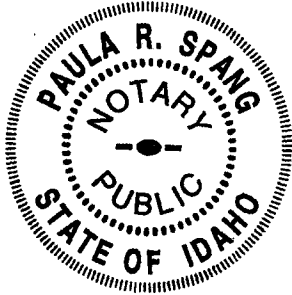
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Lauer, Jr.

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: _____

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "B"

IMAGING PATENT APPLICATIONS

Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
	MULTIPLICATION LAYER AND LOW VOLTAGE READOUT ELECTRONICS				
2002-0595.01/CN	CMOS APS WITH STACKED AVALANCHE MULTIPLICATION LAYER AND LOW VOLTAGE READOUT ELECTRONICS	Pending	China	2008101094617	Aug 22, 2003
2002-0595.02/US	CMOS APS WITH STACKED AVALANCHE MULTIPLICATION LAYER AND LOW VOLTAGE READOUT ELECTRONICS	Pending	United States of America	12078643	Apr 2, 2008
2002-0597.00/US	CMOS IMAGE SENSOR APPARATUS WITH ON-CHIP REAL-TIME PIPELINED JPEG COMPRESSION MODULE	Pending	United States of America	10227809	Aug 27, 2002
2002-0597.01/US	CMOS IMAGE SENSOR APPARATUS WITH ON-CHIP REAL-TIME PIPELINED JPEG COMPRESSION MODULE	Abandoned	United States of America	11524534	Sep 21, 2006
2002-0698.00/JP	WIDE DYNAMIC RANGE PINNED PHOTODIODE ACTIVE PIXEL SENSOR (APS)	Pending	Japan		Nov 14, 2002
2002-0698.00/US	WIDE DYNAMIC RANGE PINNED PHOTODIODE ACTIVE PIXEL SENSOR (APS)	Pending	United States of America	10294686	Nov 15, 2002
2002-0859.00/US	METHOD AND APPARATUS FOR CONTROLLING INTEGRATION TIME IN IMAGERS	Pending	United States of America	10662445	Sep 16, 2003
2002-1008.02/US	TOTAL INTERNAL REFLECTION CMOS IMAGER	Abandoned	United States of America	11293504	Dec 5, 2005
2002-1024.00/US	METHOD AND CIRCUIT FOR DETERMINING THE RESPONSE CURVE KNEE POINT IN ACTIVE PIXEL IMAGE SENSORS WITH EXTENDED DYNAMIC RANGE	Allowed	United States of America	10633673	Aug 5, 2003
2002-1024.01/US	METHOD AND CIRCUIT FOR	Pending	United States of America	10785546	Apr 18, 2007

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