

PATENT ASSIGNMENT

Electronic Version v1.1

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Conexant, Inc.	10/16/2008

RECEIVING PARTY DATA

Name:	Xocyst Transfer AG L.L.C.
Street Address:	2711 Centerville Road
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City:	Wilmington
State/Country:	DELAWARE
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PROPERTY NUMBERS Total: 99

Property Type	Number
Patent Number:	5654991
Patent Number:	5712870
Patent Number:	5732105
Patent Number:	5883565
Patent Number:	5999080
Patent Number:	5883921
Patent Number:	5896053
Patent Number:	5982807
Application Number:	10005483
Patent Number:	6233273
Patent Number:	6690715
Patent Number:	6377608
Patent Number:	6426677
Patent Number:	6452452

PATENT

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Patent Number:	6529047
Patent Number:	6538507
Patent Number:	6560448
Patent Number:	6678310
Patent Number:	6603801
Patent Number:	6563858
Patent Number:	6570427
Patent Number:	6577670
Patent Number:	6600372
Patent Number:	6614836
Patent Number:	6661857
Patent Number:	6668328
Patent Number:	6735422
Patent Number:	6674998
Patent Number:	6891440
Patent Number:	7068987
Patent Number:	6748200
Application Number:	12147975
Patent Number:	6724834
Patent Number:	6735420
Patent Number:	6754195
Patent Number:	7170880
Patent Number:	7161987
Patent Number:	6756656
Patent Number:	6905889
Patent Number:	6763228
Application Number:	10377324
Patent Number:	6791962
Application Number:	10880366
Application Number:	10880367
Patent Number:	7251235
Patent Number:	6831517
Patent Number:	6831589
Patent Number:	7072616
Patent Number:	6842607

Patent Number:	7373172
Patent Number:	6876319
Patent Number:	6931343
Patent Number:	6973296
Patent Number:	6977944
Application Number:	11280573
Patent Number:	7057469
Patent Number:	7058144
Patent Number:	7103112
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Patent Number:	7162507
Patent Number:	7173988
Patent Number:	7212512
Patent Number:	7254373
Patent Number:	7269153
Patent Number:	7274652
Application Number:	11849579
Patent Number:	7313121
Patent Number:	7321762
Patent Number:	7324612
Patent Number:	7343011
Patent Number:	7394864
Application Number:	12164930
Application Number:	10273799
Patent Number:	7369485
Application Number:	12061404
Application Number:	10383339
Application Number:	10421265
Patent Number:	7400640
Application Number:	12172811
Patent Number:	7388903
Application Number:	12140070
Application Number:	10611304
Patent Number:	7400621

Application Number:	12172765
Application Number:	10621557
Application Number:	10680876
Application Number:	10688527
Application Number:	10689018
Application Number:	10693051
Application Number:	10778854
Application Number:	10779606
Patent Number:	6635949
Patent Number:	7064363
Patent Number:	6974740
Patent Number:	7084481
Patent Number:	6785324
Patent Number:	6900087
Patent Number:	6487657

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Total Attachments: 10
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ASSIGNMENT OF PATENT RIGHTS

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- (g) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Assigned

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- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

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Patent or application no.	Country	Filing Date	Title of Patent and Inventors
5,654,991 (08/509,588)	US	8/5/1997 (7/31/1995)	Fast acquisition bit timing loop method and apparatus Andren, Carl F.; Lucas, Leonard Victor; Fakatselis, John C.; Snell, Jim
KR10-0433751 (KR10-1996-0032113)	KR	5/20/2004 (7/31/1996)	Fast acquisition bit timing loop method and apparatus Andren, Carl Frank; Lucas, Leonard Victor; Fakatselis, John Christ; Snell, Jim
5,712,870 (08/509,462)	US	1/27/1998 (7/31/1995)	Packet header generation and detection circuitry Petrick, Al
5,732,105 (08/509,586)	US	3/24/1998 (7/31/1995)	Method of estimating signal quality in a DPSK demodulator Andren, Carl Frank; Frogge, Perry Wesley; Lucas, Leonard Victor; Snell, Jim
5,883,565 (08/941,704)	US	3/16/1999 (10/1/1997)	Frequency dependent resistive element Furino, Jr., James P.
5,999,080 (09/246,815)	US	12/7/1999 (2/9/1999)	Frequency dependent resistive element Furino, Jr., James P.
5,883,921 (08/509,587)	US	3/16/1999 (7/31/1995)	Short burst acquisition circuit and method for direct sequence spread spectrum links Andren, Carl Frank; Lucas, Leonard Victor; Fakatselis, John Christ; Snell, Jim
DE69630088 (DE69630088)	DE	9/24/2003 (7/18/1996)	Short burst acquisition circuit for direct sequence spread spectrum links Andren, Carl Frank; Fakatselis, John Christ; Lucas, Leonard Victor; Snell, Jim
KR10-0430157 (KR10-1996-0032115)	KR	4/22/2004 (7/31/1996)	Short burst acquisition circuit and method for direct sequence spread spectrum links Andren, Carl Frank; Lucas, Leonard Victor; Fakatselis, John Christ; Snell, Jim
5,896,053 (08/873,899)	US	4/20/1999 (6/13/1997)	Single ended to differential converter and 50% duty cycle signal generator and method Prentice, John S.
DE69827866 (DE98103451.5)	DE	12/1/2004 (2/26/1998)	High data rate spread spectrum transceiver and associated methods Snell, James Leroy

GB0866588 (GB98103451.5)	GB	12/1/2004 (2/26/1998)	High data rate spread spectrum transceiver and associated methods Snell, James Leroy
ZL98105495.1 (CN98105495.1)	CN	11/8/2006 (3/16/1998)	High data rate spread spectrum transceiver and associated methods Snell, James Leroy
JPH10-067463	JP	3/17/1998	High data rate spread spectrum transceiver and its relating methods Snell, James Leroy
5,982,807 (08/819,846)	US	11/9/1999 (3/17/1997)	High data rate spread spectrum transceiver and associated methods Snell, James Leroy
RE40,231 (10/005,483)	US	(11/9/2001)	High data spread spectrum transceiver and associated methods Snell, James Leroy; Andren, Carl F.; Lucas, Leonard Victor
KR10-0530277 (KR10-1998-0009022)	KR	11/15/2005 (3/17/1998)	High data rate spread spectrum transceiver and associated methods Snell, James Leroy
6,233,273 (09/342,583)	US	5/15/2001 (6/29/1999)	Rake receiver with embedded decision feedback equalizer Webster, Mark A.; Nelson, George R.; Halford, Karen W.; Andren, Carl F.
6,690,715 (09/823,845)	US	2/10/2004 (3/30/2001)	RAKE receiver with embedded decision feedback equalizer Webster, Mark A.; Nelson, George R.; Halford, Karen W.; Andren, Carl F.
6,377,608 (09/163,802)	US	4/23/2002 (9/30/1998)	Pulsed beacon-based interference reduction mechanism for wireless communication networks Zyren, James G.
6,426,677 (09/952,184)	US	7/30/2002 (9/14/2001)	Linearization bias circuit for BJT amplifiers Prentice, John S.
6,452,452 (09/612,848)	US	9/17/2002 (7/10/2000)	Negative feedback gain control for common electrode transistor Furino, Jr., James P.
6,529,047 (09/911,060)	US	3/4/2003 (7/21/2001)	Mixer driver circuit Prentice, John S.
6,538,507 (10/079,983)	US	3/25/2003 (2/21/2002)	Automatic gain control circuit with high linearity and monotonically correlated offset voltage Prentice, John S.; Landy, Patrick J.
6,560,448 (09/678,901)	US	5/6/2003 (10/2/2000)	DC compensation system for a wireless communication device configured in a zero intermediate frequency architecture Baldwin, Keith R.; Landy, Patrick J.; Webster, Mark A.; Schultz, R. Douglas; Prentice, John S.
6,678,310 (09/231,184)	US	1/13/2004 (1/14/1999)	Wireless local area network spread spectrum transceiver with multipath mitigation Andren, Carl; Webster, Mark A.
6,603,801 (09/231,228)	US	8/5/2003 (1/14/1999)	Spread spectrum transceiver for use in wireless local area network and having multipath mitigation Andren, Carl; Webster, Mark A.

6,563,858 (09/231,608)	US	5/13/2003 (1/14/1999)	Method of performing antenna diversity in spread spectrum in wireless local area network Fakatselis, John; Lucas, Leonard V.
6,570,427 (09/943,668)	US	5/27/2003 (8/31/2001)	Variable transconductance amplifier Prentice, John S.
6,577,670 (09/378,532)	US	6/10/2003 (8/20/1999)	Programmable filtering mechanism to allow bandwidth overlap between direct sequence spread spectrum communication device and frequency-hopping transmitter Roberts, Richard D.
6,600,372 (10/007,479)	US	7/29/2003 (12/3/2001)	Attenuator control circuit Prentice, John S.
6,614,836 (09/494,000)	US	9/2/2003 (1/28/2000)	Biased-corrected rake receiver for direct sequence spread spectrum waveform Halford, Steven D.; Webster, Mark A.; Nelson, George R.
6,661,857 (09/612,823)	US	12/9/2003 (7/10/2000)	Rapid estimation of wireless channel impulse response Webster, Mark A.; Baldwin, Keith R.; Nelson, George R.
6,668,328 (09/574,945)	US	12/23/2003 (5/19/2000)	Computer system having a power supply for coupling signals to a power line network and transmitting infrared signal to at least one peripheral card Bell, Russell W.
6,735,422 (09/677,975)	US	5/11/2004 (10/2/2000)	Calibrated DC compensation system for a wireless communication device configured in a zero intermediate frequency architecture Baldwin, Keith R.; Landy, Patrick J.; Webster, Mark A.; Schultz, R. Douglas; Prentice, John S.
6,674,998 (09/747,138)	US	1/6/2004 (12/21/2000)	System and method for detecting and correcting phase error between differential signals Prentice, John S.
6,891,440 (09/747,163)	US	5/10/2005 (12/21/2000)	Quadrature oscillator with phase error correction Straub, A. Michael; Prentice, John S.
7,068,987 (09/918,409)	US	6/27/2006 (7/30/2001)	Packet acquisition and channel tracking for a wireless communication device configured in a zero intermediate frequency architecture Baldwin, Keith R.; Webster, Mark A.
6,748,200 (10/407,350)	US	6/8/2004 (4/4/2003)	Automatic gain control system and method for a ZIF architecture Webster, Mark A.; Yeh, Alex C.; Garrett, Albert L.
12/147,975	US	6/27/2008	Packet acquisition and channel tracking for a wireless communication device configured in a zero intermediate frequency architecture Baldwin, Keith R.; Webster, Mark A.
6,724,834 (10/081,045)	US	4/20/2004 (2/22/2002)	Threshold detector for detecting synchronization signals at correlator output during packet acquisition Garrett, Albert L.; Baldwin, Keith R.
6,735,420 (10/024,949)	US	5/11/2004 (12/18/2001)	Transmit power control for multiple rate wireless communications Baldwin, Keith R.

6,754,195 (10/143,134)	US	6/22/2004 (5/10/2002)	Wireless communication system configured to communicate using a mixed waveform configuration Webster, Mark A.; Seals, Michael J.
7,170,880 (10/191,221)	US	1/30/2007 (7/9/2002)	Sample rate change between single-carrier and multi-carrier waveforms Webster, Mark A.; Seals, Michael J.
7,161,987 (10/191,901)	US	1/9/2007 (7/9/2002)	Single-carrier to multi-carrier wireless architecture Webster, Mark A.; Seals, Michael J.
6,756,656 (10/194,496)	US	6/29/2004 (7/11/2002)	Inductor device with patterned ground shield and ribbing Lowther, Rex Everett
6,905,889 (10/740,548)	US	6/14/2005 (12/22/2003)	Inductor device with patterned ground shield and ribbing Lowther, Rex Everett
6,763,228 (10/027,386)	US	7/13/2004 (12/21/2001)	Precision automatic gain control circuit Prentice, John S.; Landy, Patrick J.
10/377,324	US	2/28/2003	Transmit power management in shared-communications channel networks Wentink, Maarten Menzo
6,791,962 (10/353,391)	US	9/14/2004 (1/29/2003)	Direct link protocol in wireless local area networks Wentink, Maarten Menzo
10/880,366	US	6/30/2004	Link margin notification using return frame Wentink, Menzo
10/880,367	US	6/30/2004	Direct link relay in a wireless network Wentink, Menzo
7,251,235 (10/880,370)	US	7/31/2007 (6/30/2004)	Event-based multichannel direct link Wentink, Menzo
EP04794757.7	EP	10/13/2004	Link margin notification using return frame Wentink, Menzo
6,831,517 (10/650,337)	US	12/14/2004 (8/27/2003)	Bias-management system and method for programmable RF power amplifier Hedberg, David J.; Turner, James B.
6,831,589 (10/808,653)	US	12/14/2004 (3/24/2004)	Radar detector having a multi-period periodicity validator and method therefor Shearer III, Daniel Davidson MacFarlane
7,072,616 (10/444,383)	US	7/4/2006 (5/23/2003)	Multi-protocol interchip interface Godfrey, Timothy Gordon
6,842,607 (10/444,519)	US	1/11/2005 (5/23/2003)	Coordination of competing protocols Godfrey, Timothy Gordon; Bourk, Terrance Raymond
7,373,172 (11/429,556)	US	5/13/2008 (5/5/2006)	Multi-protocol interchip interface Godfrey, Timothy Gordon

6,876,319 (10/306,020)	US	4/5/2005 (11/27/2002)	Integrated modulator and demodulator configuration Webster, Mark A.; Ponton, Kent A.; Chiuchiolo, Jr., Paul J.
6,931,343 (10/666,410)	US	8/16/2005 (9/19/2003)	On-signal quadrature modulator calibration Webster, Mark A.; Seals, Michael J.; Cochran, Bruce A.
6,973,296 (10/011,794)	US	12/6/2005 (12/4/2001)	Soft decision gain compensation for receive filter attenuation Webster, Mark A.; Chiuchiolo, Paul J.; Garrett, Albert L.
6,977,944 (10/295,596)	US	12/20/2005 (11/15/2002)	Transmission protection for communications networks having stations operating with different modulation formats Brockmann, Ronald A.; Hoeben, Maarten; Wentink, Maarten Menzo
11/280,573	US	11/16/2005	Transmission protection for communications networks having stations operating with different modulation formats Brockmann, Ronald A.; Hoeben, Maarten; Wentink, Maarten Menzo
7,057,469 (10/321,116)	US	6/6/2006 (12/17/2002)	High speed differential voltage controlled oscillator Prentice, John S.
7,058,144 (10/121,762)	US	6/6/2006 (4/12/2002)	Intelligent control system and method for compensation application in a wireless communications system Baldwin, Keith R.
7,103,112 (10/011,580)	US	9/5/2006 (12/4/2001)	Transmit frequency domain equalizer Webster, Mark A.; Chiuchiolo, Paul J.; Phares, Harold P.
7,136,392 (09/943,803)	US	11/14/2006 (8/31/2001)	System and method for ordering data messages having differing levels of priority for transmission over a shared communication channel Wentink, Maarten Menzo
7,155,232 (10/680,888)	US	12/26/2006 (10/8/2003)	Transmit request signaling between transceivers Godfrey, Timothy Gordon
7,162,507 (09/922,084)	US	1/9/2007 (8/3/2001)	Wireless network site survey tool Carter, Trent R.
7,173,988 (10/338,362)	US	2/6/2007 (1/8/2003)	Adaptive phase and gain imbalance cancellation Cochran, Bruce A.; Webster, Mark A.; Seals, Michael J.
7,212,512 (10/113,743)	US	5/1/2007 (4/2/2002)	Frequency correction system for a wireless device communicating in a wireless local area network Lucas, L. Victor; Andren, Carl F.
7,254,373 (10/672,438)	US	8/7/2007 (9/26/2003)	Antenna diversity based on packet errors Paljug, Michael J.; Yin, Fanqiang
7,269,153 (10/442,606)	US	9/11/2007 (5/21/2003)	Method for minimizing time critical transmit processing for a personal computer implementation of a wireless local area network adapter Schultz, Richard Douglas; Nelson, Jr., George Rodney
7,274,652 (09/586,571)	US	9/25/2007 (6/2/2000)	Dual packet configuration for wireless communications Webster, Mark A.; Halford, Steven D.; Roberts, Richard D.

11/849,579	US	9/4/2007	Dual packet configuration for wireless communications Webster; Mark A.; Halford; Steven D.; Roberts; Richard D.
7,313,121 (10/801,042)	US	12/25/2007 (3/15/2004)	Acknowledging data transmissions in the presence of multiple shared-communications channels Fischer, Michael Andrew; Wentink, Maarten Menzo
7,321,762 (10/766,409)	US	1/22/2008 (1/27/2004)	Mechanism for reserving multiple channels of a single medium access control and physical layer Hoeben, Maarten
7,324,612 (10/785,622)	US	1/29/2008 (2/23/2004)	Carrier tracking circuit and method including dual numerically controlled oscillators and feedforward phase correction coefficient Shearer III, Daniel Davidson MacFarlane; Seals, Michael J.
7,343,011 (10/424,803)	US	3/11/2008 (4/29/2003)	Secure telecommunications system for wireless local area networks Ferguson, Niels Thomas
7,394,864 (10/143,126)	US	7/1/2008 (5/10/2002)	Mixed waveform configuration for wireless communications Webster, Mark A.; Seals, Michael J.
TW195932 (TW91111286)	TW	2/11/2004 (5/28/2002)	Mixed waveform configuration for wireless communications Webster, Mark A.; Seals, Michael J.
CN02813664.0	CN	7/2/2002	Mixed waveform configuration for wireless communications Webster, Mark A.; Seals, Michael J.
12/164,930	US	6/30/2008	Mixed waveform configuration for wireless communications Webster, Mark A.; Seals, Michael J.
10/273,799	US	10/18/2002	Efficiency improvement for shared communications networks Wentink, Maarten Menzo
7,369,485 (10/324,218)	US	5/6/2008 (12/19/2002)	Wireless receiver for sorting packets Halford, Steven D.; Frogge, Perry W.
12/061,404	US	4/2/2008	Wireless receiver for sorting packets Halford, Steven D.; Frogge, Perry W.
10/383,339	US	3/7/2003	Shared-communications channel utilization for applications having different class of service requirements Wentink, Maarten Menzo; Brockmann, Ronald A.
10/421,265	US	4/23/2003	Partitioned medium access control Fischer, Michael Andrew; Godfrey, Timothy Gordon
7,400,640 (10/701,126)	US	7/15/2008 (11/14/2003)	Partitioned medium access control implementation Fischer, Michael Andrew; Godfrey, Timothy Gordon
12/172,811	US	7/14/2008	Partitioned medium access control implementation Fischer, Michael Andrew; Godfrey, Timothy Gordon
7,388,903 (10/448,184)	US	6/17/2008 (5/29/2003)	Adaptive transmission rate and fragmentation threshold mechanism for local area networks Godfrey, Timothy Gordon

12/140,070	US	6/16/2008	Adaptive transmission rate and fragmentation threshold mechanism for local area networks Godfrey, Timothy Gordon
10/611,304	US	7/1/2003	Partial queuing using an interface with bounded latency Fischer, Michael Andrew; Godfrey, Timothy Gordon
7,400,621 (10/617,324)	US	7/15/2008 (7/10/2003)	Technique for achieving connectivity between telecommunication stations Godfrey, Timothy Gordon; Fischer, Michael Andrew
12/172,765	US	7/14/2008	Technique for achieving connectivity between telecommunication stations Godfrey, Timothy Gordon; Fischer, Michael Andrew
10/621,557	US	7/17/2003	Dynamic assignment of station addresses transmitted over shared-communications channels Fischer, Michael Andrew; Godfrey, Timothy Gordon
10/680,876	US	10/8/2003	Advance notification of transmit opportunities on a shared-communications channel Godfrey, Timothy Gordon
10/688,527	US	10/17/2003	Dynamic transmission protection in the presence of multiple modulation schemes Wentink, Maarten Menzo
10/689,018	US	10/20/2003	Technique for optimizing backoff for a shared resource Wentink, Maarten Menzo
10/693,051	US	10/24/2003	Technique for installing a station device driver La Gesse, Robert Derek; Godfrey, Timothy Gordon
10/778,854	US	2/13/2004	Decision directed flicker noise cancellation Webster, Mark A.; Yeh, Alex C.; Baldwin, Keith R.
10/779,606	US	2/18/2004	Technique for output power dithering for improved transmitter performance Seals, Michael J.; Harriman, Adam K.
6,635,949 (10/039,200)	US	10/21/2003 (1/4/2002)	Symmetric inducting device for an integrated circuit having a ground shield Lowther, Rex Everett; Young, William R.
7,064,363 (10/855,948)	US	6/20/2006 (5/28/2004)	Symmetric inducting device for an integrated circuit having a ground shield Lowther, Rex Everett; Young, William R.
6,974,740 (10/855,951)	US	12/13/2005 (5/28/2004)	Symmetric inducting device for an integrated circuit having a ground shield Lowther, Rex Everett; Young, William R.
7,084,481 (10/855,953)	US	8/1/2006 (5/28/2004)	Symmetric inducting device for an integrated circuit having a ground shield Lowther, Rex Everett; Young, William R.
6,785,324 (09/426,847)	US	8/31/2004 (10/26/1999)	Transceiver including reactive termination for enhanced cross-modulation performance and related methods Schultz, Richard Douglas; Matarazzo, Raphael Leite B.
6,900,087 (10/645,709)	US	5/31/2005 (8/21/2003)	Symmetric inducting device for an integrated circuit having a ground shield Lowther, Rex Everett; Young, William R.

TW104951 (TW87102737)	TW	8/1/1999 (2/25/1998)	High data rate spread spectrum transceiver and associated methods Snell, James Leroy
JP2002-502994	JP	5/31/2001	Dual packet configuration for wireless communications Webster, Mark A.; Halford, Steven D.; Roberts, Richard D.
NL1008351	NL	2/19/1998	Data communication network Brockmann, Ronald Alexander; Zwemmer, Arnold Roderick; Hoebe, Maarten
EP99200388.9	EP	2/11/1999	Data communication network Brockmann, Ronald Alexander
6,487,657 (09/252,308)	US	11/26/2002 (2/18/1999)	Data communication network Brockmann, Ronald Alexander
DE60007678.4 (DE00200980.1)	DE	1/14/2004 (3/17/2000)	Method of providing secured data traffic in a computer Brockmann, Ronald Alexander; Hoebe, Maarten; Zwemmer, Arnoud Roderick
GB1039363 (GB00200980.1)	GB	1/14/2004 (3/17/2000)	Method of providing secured data traffic in a computer Brockmann, Ronald Alexander; Hoebe, Maarten; Zwemmer, Arnoud Roderick
NL1039363 (NL00200980.1)	NL	1/14/2004 (3/17/2000)	Method of providing secured data traffic in a computer Brockmann, Ronald Alexander; Hoebe, Maarten; Zwemmer, Arnoud Roderick

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee, do all things as may be necessary, or otherwise reasonable and customary, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights. The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

For the avoidance of doubt, and notwithstanding anything to the contrary, this Assignment of Patent Rights does not (and will not) supersede, extinguish, or alter in any way, the license and covenant not to sue granted from the Assignee to the Assignor (and certain associated third parties), as set forth in the Patent Purchase Agreement signed May 13, 2008 ("**PPA**") to which this Patent Assignment of Patent Rights is an exhibit.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Newport Beach, California on October 16, 2008.

ASSIGNOR:

Conexant, Inc.

By: *Asmuna T Boulanger*
Name: Asmuna T Boulanger
Title: Director, VP and Secretary
(Signature MUST be notarized)

STATE OF Cal. Former
COUNTY OF Orange) ss.

On October 16, 2008 before me, Terri A Apati, Notary Public in and for said State, personally appeared Asmuna T Boulanger (who proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

I certify under PENALTY OF PERJURY under the law of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature *Terri A Apati* (Seal)

