

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
ATI Technologies ULC	01/19/2009
RECEIVING PARTY DATA	
Name:	QUALCOMM Incorporated
Street Address:	5775 Morehouse Drive
City:	San Diego
State/Country:	CALIFORNIA
Postal Code:	92121-1714
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	11469141
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ATTORNEY DOCKET NUMBER:	091128
NAME OF SUBMITTER:	Thomas Lasher
Total Attachments: 15 source=AMD Master Patent Assignment#page1.tif source=AMD Master Patent Assignment#page2.tif source=AMD Master Patent Assignment#page3.tif source=AMD Master Patent Assignment#page4.tif source=AMD Master Patent Assignment#page5.tif	

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PATENT ASSIGNMENT

TO WHOM IT MAY CONCERN:

For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, be it known that **ADVANCED MICRO DEVICES, INC.**, a Delaware corporation, located at One AMD Place, Sunnyvale, California USA 94088, **ATI TECHNOLOGIES ULC** (formerly ATI Technologies, Inc.), a wholly owned subsidiary of Advanced Micro Devices, Inc., located at 1 Commerce Valley Drive East, Markham, Ontario, Canada, and **ATI INTERNATIONAL SRL**, a subsidiary of ATI Technologies ULC, located at Beaumont House, Hastings, Chris Church, Barbados, West Indies (collectively, "**Assignors**"), have sold, conveyed, assigned, transferred and delivered, and by these presents do hereby sell, convey, assign, transfer and deliver, unto **QUALCOMM INCORPORATED**, a Delaware corporation having its corporate head office located at 5775 Morehouse Dr., San Diego, CA 92121 ("**Assignee**"), its successors, legal representatives and/or assigns, the whole of Assignors' worldwide right, title and interest in and to: (i) the patents and patent applications listed in Exhibit A attached hereto and the inventions claimed in such patents and patent applications; (ii) all divisional applications, continuation applications, continued prosecution applications, continuation-in-part applications, substitute applications, renewal applications, reissued patents, reexaminations, and extensions of such patents and patent applications that have been or shall be issued in the United States and all foreign countries; and (iii) all rights of priority resulting from the filing of said patents and patent applications (collectively, the "**Patents**").

Said sale, conveyance, assignment and transfer includes, without limitation, the rights to enforce, assert and sue for past, present and future infringement of the Patents, and the rights to recover and collect for past, present and future damages related to the Patents.

Assignors agree to execute or procure any further necessary assurance of the title to said Patents and to, at any time, upon the reasonable request and at the expense of Assignee, execute all papers that may be necessary or reasonably desirable to perfect the title to said Patents which may be granted therefor in Assignee, its successors, assigns or other legal representatives, and will make all rightful oaths or declarations affirming such transfer of ownership, and do all lawful acts requisite for procuring the same therein, without further compensation, but at the reasonable request and expense of Assignee, its successors, assigns or other legal representatives.

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IN WITNESS WHEREOF, ADVANCED MICRO DEVICES, INC. has caused this Patent Assignment to be signed on its behalf on this 19th day of January, 2009.

Robert J. Rivet
(Signature)

Robert J. Rivet

(Print or type name)

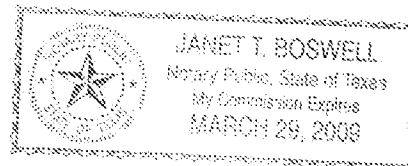
Executive Vice President and
Chief Operations and Administrative Officer
(Print or type title)

STATE OF)
) SS:
COUNTY OF)

Before me this 19th day of January, 2009, personally appeared Robert J. Rivet, personally known to me or proved to me on the basis of satisfactory evidence to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity therefor and that he signed the same of his own free will for the purpose therein expressed.

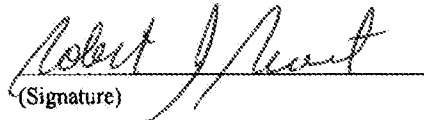
WITNESS my hand and official seal:

Janet T. Boswell
Notary Public



[SIGNATURE PAGE TO PATENT ASSIGNMENT]

IN WITNESS WHEREOF, ATI TECHNOLOGIES ULC has caused this Patent Assignment to be signed on its behalf on this 19th day of January, 2009.

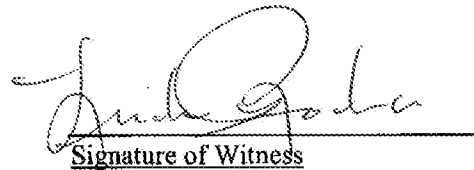

(Signature)

Robert J. Rivet
(Print or type name)

President and Chief Executive Officer
(Print or type title)

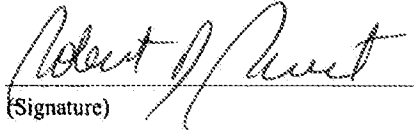
DECLARATION OF WITNESS

I, Linda Rocha, hereby declare that I was personally present and did
see Robert Rivet duly sign and execute the foregoing Patent Assignment.


Signature of Witness

[SIGNATURE PAGE TO PATENT ASSIGNMENT]

IN WITNESS WHEREOF, ATI INTERNATIONAL SRL has caused this Patent Assignment to be signed on its behalf on this 19th day of January, 2009.


(Signature)

Robert J. Rivet
(Print or type name)

Executive Vice President and Chief Operations
and Administrative Officer of Parent Seller
(Print or type title)

DECLARATION OF WITNESS

I, Linda Rocha, hereby declare that I was personally present and did
see Robert Rivet duly sign and execute the foregoing Patent Assignment.


Signature of Witness

[SIGNATURE PAGE TO PATENT ASSIGNMENT]

IN WITNESS WHEREOF, **Assignee** has caused this Patent Assignment to be signed on its behalf on this 14 day of January, 2009.

ASSIGNEE:

QUALCOMM Incorporated

(Signature)

Thomas Rouse

(Print or type name)

VP, Chief Patent Counsel

(Print or type title)

DECLARATION OF WITNESS

I, McKone Johnson, hereby declare that I was personally present and did see Thomas Rouse duly sign and execute the foregoing Patent Assignment.

Signature of Witness

[SIGNATURE PAGE TO PATENT ASSIGNMENT]

PATENT
REEL: 022245 FRAME: 0591

EXHIBIT A**PATENTS**U.S.

Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Dynamic Clock Control Circuit for Graphics Engine Clock and Memory Clock and Method	10/794,201 7,343,508	03/05/2004 03/11/2008	US2005-0195181 A1 09/08/2005	030039	USA
Dynamic Clock Control Circuit and Method	11/928,111	10/30/2007	US2008-0049009 A1 02/28/2008	030039C1	USA
Optimal Initial Rasterization Starting Point	09/244,270 7,224,364	02/03/1999 05/29/2007		M 7019 S	USA
Method and Apparatus for Enlarging an Output Display on a Display	10/802,485 7,212,210	03/17/2004 05/01/2007	US2005-0206656 A1 09/22/2005	030022 T	USA
Reducing Power Consumption by Estimating Engine Load and Reducing Engine Clock Speed	09/767,086 6,715,089	01/22/2001 03/30/2004	US2002-0099964 A1 07/25/2002	000148BT	USA
System and Method for Reducing Power Consumption by Estimating Engine Load and Reducing Engine Clock Speed	10/813,595 7,167,994	03/30/2004 01/23/2007	US2005-0044435 A1 02/24/2005	000148BT.1	USA
Apparatus and Method for Reducing Power Consumption of a Processor by Estimating Engine Load	11/555,847	11/02/2006	US2007-0208962 A1 09/06/2007	000148C2	USA

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PATENT**REEL: 022245 FRAME: 0592**

Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
System for Reduced Power Consumption by Monitoring Instruction Buffer and Method Thereof	10/083,875 7,114,086	02/27/2002 09/26/2006	US2003-0131269 A1 07/10/2003	020008 T	USA
System for Reduced Power Consumption By Phase Locked Loop and Method Thereof	10/083,903 7,036,032	02/27/2002 04/25/2006	US2003-0131274 A1 07/10/2003	020010 T	USA
System For Reduced Power Consumption by Monitoring Video Content and Method Thereof	10/083,917 7,017,053	02/27/2002 03/21/2006	US2003-0128198 A1 07/10/2003	020009 T	USA
Method and Apparatus for Rotating an Image on a Display	09/772,789 7,113,194	01/30/2001 09/26/2006	US2002-0101439 A1 08/01/2002	000136 BT	USA
System, Method, and Apparatus for Multi-Level Hierarchical Z Buffering	10/279,902 7,091,971	10/25/2002 08/15/2006	US2003-0151606 A1 08-14-2003	010008 M	USA
Portable Device for Providing Dual Display and Method Thereof	10/037,366 7,012,610	01/04/2002 03/14/2006	US2003-0128197 A1 07/10/2003	020013 T	USA
System for Alpha Blending and Method Thereof	10/146,767 6,927,778	05/16/2002 08/09/2005	US2003-0214508 A1 11/20/2003	020011 T	USA
Method and Apparatus for Rendering an Object Using Texture Variant Information	09/088,601 6,762,768	06/01/1998 07/13/2004	US2003-0146917 A1 08/07/2003	980068-C1	USA
System for Accessing Graphics Data from Memory and method Thereof	09/512,928 6,704,022	02/25/2000 03/09/2004		000025 B T	USA
Method and Apparatus for Controlling Compressed Z Information in a Video Graphics System	10/303,593 6,636,226	11/25/2002 10/21/2003	US2003-0085893 A1 05/08/2003	990076 C1	USA
Method and Apparatus for Video Graphics Antialiasing	09/141,797 6,188,394	08/28/1998 02/13/2001		980062	USA

Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Method and Apparatus for Controlling Compressed Z Information in a Video Graphics Systems that Supports Anti-Aliasing	09/356,790 6,407,741	07/20/1999 06/18/2002		990090	USA
Method and Apparatus for Video Graphics Antialiasing with Memory Overflow Optimization	09/316,438 6,429,876	05/21/1999 08/06/2002		990074	USA
Method and Apparatus for Controlling Compressed Z Information in a Video Graphics System	09/369,730 6,492,991	08/06/1999 12/10/2002		990076	USA
Method and Apparatus for Video Graphics Antialiasing Using a Single Sample Frame Buffer and Associated Sample Memory	09/619,129 6,614,449	07/18/2000 09/02/2003		000113BM	USA
Method and System for Efficient Rendering of Image Component Polygons	09/451,191 6,501,474	11/29/1999 12/31/2002		M 7063 S	USA
Method and Apparatus for Displaying Images	09/385,828 6,476,822	08/30/1999 11/05/2002		990101 BT	USA
Pipeline Processing System and Method	09/467,945 6,462,743	12/21/1999 10/08/2002		M 7012 S	USA
Pixel Clustering for Improved Graphics Throughput	08/918,276 6,417,848	08/25/1997 07/09/2002		M 5122 S	USA
Graphics Display List Handler and Method	09/211,637 6,339,427	12/15/1998 01/15/2002		980018 BT	USA
Method and Apparatus for Full Scene Anti-Aliasing	09/026,866 6,317,525	02/20/1998 11/13/2001		980030	USA

Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Graphics Processing Device and method with Graphics Versus Video Color Space Conversion Discrimination	09/553,682 6,310,659	04/21/2000 10/30/2001		000085 BT	USA
Graphics Processing Device With Integrated Programmable Synchronization Signal Generation	09/553,144 6,518,970	04/20/2000 02/11/2003		990133	USA
Image generator using display memory	09/712,339 6,297,831	11/13/2000 10/02/2001		FR7-D1	USA
Image generator using display memory	08/795,538 6,181,354	02/06/1997 01/30/2001			USA
Method and Apparatus for Texture Blending in a Video Graphics Circuit	09/208,194 6,259,462	12/09/1998 07/10/2001		990005 BM	USA
Method and Apparatus for Determining Level of Detail for Texture Filtering	09/088,083 6,078,335	06/01/1998 06/20/2000		980064	USA
Method and Apparatus for Capturing Mobile Multimedia Signals	11/468,982	08/31/2006	US2008-0057918 A1 03/06/2008	060082	USA
Method and Apparatus for Rotating an Image on a Display	11/465,043	08/16/2006	US2008-0043032 A1 02/21/2008	000136 C1	USA
An apparatus and method for detecting and recovering errors caused by electrostatic discharge	11/278,199	03/31/2006	US2007-0250750 A1 10/25/2007	060013 T	USA
Method and apparatus for reducing power consumption of a co-processor	11/388,928	03/24/2006	US2007-0226522 A1 09/27/2007	060021 T	USA
Power conservation	11/237,065	09/28/2005	US2007-0073956 A1 03/29/2007	050078 T	USA

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Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Antialiasing Of Two-Dimensional Vector Images	11/876,870	10/23/2007		070079	USA
Method for Varying the Pitch of a Musical Tone Produced Through Playback of a Stored Waveform	08/555,537 5,814,750	11/09/1995 09/29/1998		M3429 US	USA
Method and apparatus for white balancing digital images	11/331,932	01/13/2006	US2007-0165945 A1 07/19/2007	050066 T	USA
Method and Apparatus for Image Processing in Handheld Device	10/667,912	09/22/2005	US2005-0062858 A1 03/24/2005	030010 T	USA
Portable Device with Priority Based Power Savings Control and Method Thereof	11/469,141	08/31/2006	US2008-0057894 A1 03/06/2008	060034	USA
Method & Apparatus for Content Delivery to Devices	11/467,451	08/25/2006	US2008-0049660 A1 02/28/2008	060032	USA
Method and Apparatus for Recording Information in Battery Operated Devices	11/420,283	05/25/2006	US2007-0274245 A1 11/29/2007	060033	USA
Automatic Image Reorientation	11/739,896	04/25/2007	US2008-0266326 A1 10/30/2008	060080	USA
Server Initiated Power Mode Switching in Portable Communication Devices	11/847,051	08/29/2007		060055	USA
Method and apparatus for camera shake effect image stabilization	11/862,621	09/27/2007		070067	USA
Wireless Energy Transfer	11/928,796	10/30/2007		070066	USA
Method and apparatus with fast camera auto focus	11/964,986	12/27/2007		070059	USA
Method and apparatus with depth map generation	11/964,992	12/27/2007		070112	USA

Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Portable computing device with integral current generator and method of using the same	11/964,915	12/27/2007		070116	USA
Method and Apparatus for Portable Phone Based Noise Cancellation	11/967,610	12/31/2007		070070	USA
Virtual Stereoscopic Camera	11/767,561	06/25/2007	US2008-0316299 A1 12/25/2008	070012	USA
Optimal Two-Layer Coherent Demodulation for DQPSK (Differential Quadratic Phase Shift Keying)	11/851,753	09/07/2007		070092	USA
OFDM Channel Estimation	12/013,400	01/11/2008		070018	USA
OFDM Channel Estimation	11/467,730	08/28/2006	US2008-0049598 A1 02/28/2008	060025N	USA
Power Control for time Division Multiplexing Devices	12/170,491	07/10/2008		070143	USA
Fast Stream Switching	12/240,475	09/29/2008		070143CIP	USA
Route mapping system and method	12/268,632	11/11/2008		070142	USA
Method and apparatus for scrolling text display of voice call or message during video display session	12/211,973	09/17/2008		070145	USA
Optimal Blind Channel Estimation for DQPSK Demodulation	12/132,765	06/04/2008		070113	USA
Blind Channel Estimation for PSK and D-PSK Modulated Multicarrier Communications Systems	12/166,636	07/02/2008		070134	USA
D-PSK Demodulation Based on Correlation Angle Distribution	12/212,299	09/17/2008		070141	USA
Non-Linear Tone Generator	08/555,536 5,834,672	11/09/1995 11/10/1998		M 3513 US	USA

Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Method and apparatus for processing bad pixels	11/388,937	03/24/2006	US2007-0222871 A1 09/27/2007	060007	USA
Method and Apparatus for Decompression of a Two Dimensional Video Texture Map	08/846,645 6,154,216	04/30/1997 11/28/2000		97043-1	USA
Processing Real-time Command Information	10/791,519	03/02/2004	US20050210172 09/22/2005	010077M	USA

Foreign

Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Dynamic Clock Control Circuit and Method	05708674.6	03/02/2005	EP1723491 09/15/2005	030039EP	EPO
Method and Apparatus for Controlling Compressed Z Information in a Video Graphics System	00306650.3 EP1074945	08/04/2000 01/05/2005	EP1074945 02/07/2001		EPO
Method and Apparatus for Image Processing in Handheld Device	04769432.8	09/20/2004	EP1673727 06/28/2006	030010 EP D1	EPO
Method and Apparatus for Recording Information in Battery Operated Devices	07734730.0	05/23/2007		060033EP	EPO
Method and Apparatus for White Balancing Digital Images	07700445.5	01/04/2007	EP1985106 10/29/2008	050066	EPO
Power Conservation	06808961.4	09/28/2006	EP 1934675 06/25/2008	050078EP	EPO
Reducing Power Consumption by Estimating Engine Load and Reducing Engine Clock Speed	02230339.5	01/18/2002	EP1237067 09/04/2002	000148TEP	EPO
Wireless Energy Transfer	08253515.4	10/29/2008		070066 EP	EPO
Method and Apparatus for Controlling Compressed Z Information in a Video Graphics System	FR00306650.3	08/04/2000			FRANCE
Method and Apparatus for Controlling Compressed Z Information in a Video Graphics System	DE00306650.3	08/04/2000	DE60017222 12/08/2005		GERMANY

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Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Method and Apparatus for Controlling Compressed Z Information in a Video Graphics System	GB00306650.3	08/04/2000			GREAT BRITAIN
Method and Apparatus for Recording Information in Battery Operated Devices	TBA	05/23/2007		060033IN	INDIA
Route mapping system and method	2403/CHE/2008	10/07/2008		070142 IN	INDIA
Automatic Image Reorientation	PCT/CA2008/000785	04/25/2008	WO2008131539 11/06/2008	060080 PCT	PCT
Dynamic Clock Control Circuit and Method	PCT/IB2005/000566	03/02/2005	WO2005085977 09/15/2005	030039	PCT
Method and Apparatus for Image Processing in Handheld Device	PCT/IB2004/003064	09/20/2004	WO2005029407 03/31/2005	030010 PCT	PCT
Method and Apparatus for Recording Information in Battery Operated Devices	PCT/IB2007/001442	05/23/2007	WO2007138470 12/06/2007	060033 WO	PCT
Method and Apparatus for White Balancing Digital Images	PCT/IB2007/000016	01/04/2007	WO2007080466 07/19/2007	050066 PCT	PCT
OFDM Channel Estimation	PCT/US2007/018856	08/27/2007	WO2008027344 03/06/2008	060025PCT	PCT
Optimal Two-Layer Coherent Demodulation for D-PSK	PCT/US2008/009160	07/30/2008		070092	PCT
Portable Device with Priority Based Power Savings Control and Method Thereof	PCT/US2007/077125	08/29/2007	WO2008027975 03/06/2008	060034 PCT	PCT
Portable Phone Based Noise Cancellation	PCT/US2008/014000	12/23/2008		070070	PCT

Title	Application/ Registration Number	Filing Date/ Issue Date	Publication Number and Date	AMD Ref. No.	Country
Power Conservation	PCT/IB2006/002780	09/28/2006	WO 2007036801 04/05/2007	050078PCT	PCT