

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	10/03/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman) Limited
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12183633
CORRESPONDENCE DATA	
Fax Number:	(610)407-0701
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	610-407-0700
Email:	sseiler@ratnerprestia.com
Correspondent Name:	Jack J. Jankovitz
Address Line 1:	1235 Westlakes Drive, Suite 301
Address Line 2:	RatnerPrestia
Address Line 4:	Berwyn, PENNSYLVANIA 19312
NAME OF SUBMITTER:	Jack J. Jankovitz
<p>Total Attachments: 11</p> <p>source=micr-136us1assignment#page1.tif</p> <p>source=micr-136us1assignment#page2.tif</p> <p>source=micr-136us1assignment#page3.tif</p> <p>source=micr-136us1assignment#page4.tif</p>	

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PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/26/08, 2008

Notarization

State of Idaho
County of Ada

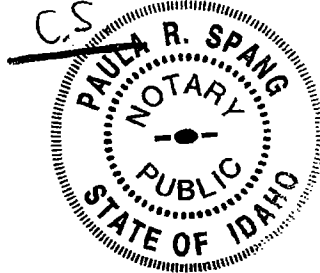
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven B. Appleton
(date) (notary name)

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas L. Louis, Jr.

Date: 9/26/08, 2008

Notarization

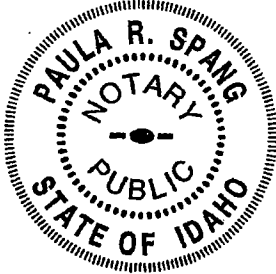
State of Idaho
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Louis, Jr.
(date) (notary name)

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"



ATTACHMENT "B"



Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
AVGO-0052.00/JP	Method For Detecting And Correcting Defective Pixels In A Digital Image Sensor	Pending	Japan	2003324172	Sep 17, 2003
AVGO-0053.00/EP	CMOS Image sensor using gradient index chip scale lenses	Pending	European Patent Office	030078067	Apr 4, 2003
AVGO-0053.00/JP	CMOS image sensor using gradient index chip scale lenses	Pending	Japan	2003293248	Aug 13, 2003
AVGO-0053.00/PR	CMOS Image sensor using gradient index chip scale lenses	Completed	United States of America	60403411	Aug 13, 2002
AVGO-0053.01/US	CMOS image sensor using gradient index chip scale lenses	Pending	United States of America	12183833	Jul 31, 2008
AVGO-0054.00/JP	IMAGING APPARATUS AND ITS MANUFACTURING METHOD	Pending	Japan	2003146149	May 23, 2003
AVGO-0055.00/US	Apparatus and method for detecting and compensating for luminant intensity changes within an image	Pending	United States of America	10388813	Mar 14, 2003
AVGO-0056.01/US	Multifunction lens	Pending	United States of America	12176760	Jul 21, 2008
AVGO-0067.00/JP	WAFER SCALE MANUFACTURING METHOD	Pending	Japan	2004059701	Mar 3, 2004
AVGO-0058.00/EP	System and method for shape reconstruction from optical images	Pending	European Patent Office	030253033	Nov 3, 2003
AVGO-0058.01/EP	System and method for shape reconstruction from optical images	Pending	European Patent Office	070016340	Nov 3, 2003
AVGO-0059.00/DE	A Method And Apparatus For Distributed Analyses Of Images	Pending	Germany	1020050450776	Sep 21, 2005
AVGO-0059.00/GB	A Method And Apparatus For Distributed Analyses Of Images	Pending	United Kingdom	05225040	Nov 3, 2005
AVGO-0059.00/JP	A Method And Apparatus For Distributed Analyses Of Images	Pending	Japan	2005360843	Dec 14, 2005
AVGO-0059.01/US	A Method And Apparatus For Distributed Analyses Of	Pending	United States of America	12138579	Jun 13, 2008

ATTACHMENT "C"



ATTACHMENT "D"



ATTACHMENT "E"

