

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	10/03/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman) Limited
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	6818930
CORRESPONDENCE DATA	
Fax Number:	(202)420-2201
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	202-420-2200
Email:	bakerm@dsmo.com
Correspondent Name:	Dickstein Shapiro LLP
Address Line 1:	1825 Eye Street, NW
Address Line 4:	Washington, DISTRICT OF COLUMBIA 20006
ATTORNEY DOCKET NUMBER:	M4065.0556/P556
NAME OF SUBMITTER:	Madeline E. Baker
Total Attachments: 7 source=Document#page1.tif source=Document#page2.tif source=Document#page3.tif	

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## PATENT ASSIGNMENT AGREEMENT

### 1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k)

2.

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c)

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3

[REDACTED]

[REDACTED]

4.

[REDACTED]

[REDACTED]

5.

[REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/26/08, 2008

Notarization

State of Idaho

County of Ada

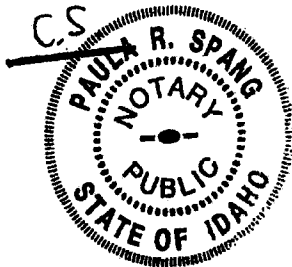
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven R. Appleton

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11

REVIEWED  
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas J. Lauer Jr.

Date: 9/26/08, 2008

Notarization

State of Idaho

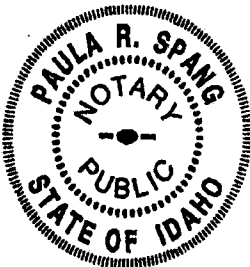
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas J. Lauer, Jr.

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

**ATTACHMENT "A"**

**IMAGING PATENTS**



## Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
6780666	2003-0194.00/US	IMAGE PHOTO DIODE CAPACITOR STRUCTURE WITH REDUCED PROCESS VARIATION SENSITIVITY	United States of America	Aug 24, 2004
6781398	PBIT-0164.00/US	On-Chip ADC Test For Image Sensors	United States of America	Aug 24, 2004
6783900	2002-0566.00/US	COLOR FILTER IMAGING ARRAY AND METHOD OF FORMATION	United States of America	Aug 31, 2004
6784013	1998-1077.01/US	CMOS IMAGER AND METHOD OF FORMATION	United States of America	Aug 31, 2004
6784930	AVGO-0196.00/US	ACTIVE PIXEL SENSOR WITH ENHANCED RESET	United States of America	Aug 31, 2004
6787752	PBIT-0139.00/US	PSEUDO-RANDOM ASSIGNMENT BETWEEN ELEMENTS OF THE IMAGER PROCESSOR AND THE A/D CONVERTER CELLS	United States of America	Sep 7, 2004
6787819	1998-0449.02/US	RETROGRADE WELL STRUCTURE	United States of America	Sep 7, 2004
6791378	2002-0565.00/US	CHARGE RECYCLING AMPLIFIER FOR HIGH DYNAMIC RANGE CMOS IMAGER	United States of America	Sep 14, 2004
6792142	PBIT-0129.00/US	IMAGE SENSING SYSTEM WITH HISTOGRAM MODIFICATION	United States of America	Sep 14, 2004
6794218	AVGO-0054.01/US	GLASS ATTACHMENT OVER MICROLENS ARRAYS	United States of America	Sep 21, 2004
6795118	PBIT-0107.00/US	TESTING OF SOLID-STATE IMAGE SENSORS	United States of America	Sep 21, 2004
6795367	PBIT-0116.00/US	LAYOUT TECHNIQUE FOR ADDRESS SIGNAL LINES IN DECODERS INCLUDING STITCHED BLOCKS	United States of America	Sep 21, 2004
6798280	2002-0585.01/US	CHARGE RECYCLING AMPLIFIER FOR A FOR HIGH DYNAMIC RANGE CMOS IMAGER	United States of America	Sep 28, 2004
6809786	PBIT-0101.00/US	LOOK-AHEAD ROLLING SHUTTER SYSTEM IN CMOS SENSORS	United States of America	Oct 26, 2004
6815743	1998-1077.02/US	CMOS IMAGER AND METHOD OF FORMATION	United States of America	Nov 9, 2004
6818930	2002-0013.00/US	GATED ISOLATION STRUCTURE FOR IMAGERS	United States of America	Nov 16, 2004
6819280	PBIT-0159.01/US	READOUT OF ARRAY-BASED ANALOG DATA IN SEMICONDUCTOR-BASED	United States of America	Nov 16, 2004