

03-26-2009

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FORM COVER SHEET
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U.S. DEPARTMENT OF COMMERCE
U.S. Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks: Please record the attached original documents or copy thereof.

1. Name of conveying party(ies):
Anup Bhalla, Francois Hebert, and Daniel S. Ng

2. Name and address of receiving party(ies)
Name: Alpha & Omega Semiconductor LTD.
Internal Address: _____

Additional name(s) of conveying party(ies) attached? Yes No

3. Nature of conveyance:
 Assignment Merger
 Security Agreement Change of Name
 Other _____

Street Address: 495 Mercury Drive
City: Sunnyvale State: CA Zip: 94085

Execution Date: May 25, 2006

Additional name(s) & address(es) attached? Yes No

4. Application number(s) or patent number(s):

If this document is being filed together with a new application, the execution date of the application is: May 25, 2006

A. Patent Application No.(s)

B. Patent No.(s)

Additional numbers attached? Yes No

5. Name and address of party to whom correspondence concerning document should be mailed:

6. Total number of applications and patents involved:

Name: Bo-In Lin

7. Total fee (37 CFR 3.41).....\$ 40.00

Internal Address: _____

Enclosed

Authorized to be charged to deposit account

Street Address: 13445 Mandoli Drive

8. Deposit account number: _____

City: Los Altos Hills, State: CA Zip: 94022

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9. Statement and signature.

To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

Ching-lu Lin
Name of Person Signing

Signature

May 31, 2006
Date

Total number of pages including cover sheet, attachments, and documents: 4

Mail documents to be recorded with required cover sheet information to:
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Washington, D.C. 20231

03/18/2009 SDENB03 00000074 12381813
01 FC:0021

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PATENT
REEL: 022461 FRAME: 0688

3-16-09

ASSIGNMENT

INVENTOR
AND CITYWhereas, I, Anup Bhalla of 1406 Block Drive
Santa Clara, California 95050

have invented:

TITLE:

**PLANAR SPLIT-GATE HIGH-PERFORMANCE MOSFET
STRUCTURE AND MANUFACTURING METHOD**DATE INVENTOR
SIGNED THE
DECLARATIONand executed a United States patent application therefor
on 5/25/, 2006;

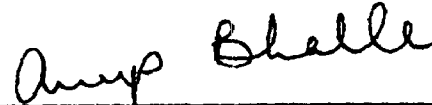
Whereas, Alpha & Omega Semiconductor, LTD, a Bermuda Company, having its registered address at Cannon's Court 22, Victoria Street, Hamilton HM12, Bermuda, (hereinafter called AOS), desires to acquire the entire right, title and interest of said application and invention, and to any United States and foreign patents to be obtained therefor;

Now therefore, for a valuable consideration, receipt whereof is hereby acknowledged, I, the above named, hereby sell, assign, and transfer to AOS, its successors and assigns, the entire right, title and interest in the said application and invention therein disclosed for the United States and foreign countries, and all rights of priority resulting from the filing said United States application, and I request the Commissioner of Patents to issue any Letters Patent granted upon the inventions set forth in said application to AOS its successors and assigns; and I hereby agree that AOS may apply for foreign Letters Patent on said invention and I will execute all papers necessary in connection with the United States and foreign applications when called upon to do so by

CITY AND

Signed and Sealed at Sunnyvale, California

DATE

on 5/25/, 2006.


SIGNATURE OF INVENTOR

ASSIGNMENT

INVENTOR
AND CITYWhereas, I, Francois Hebert of 18 Melrose Court,
San Mateo, California 94402

have invented:

TITLE:

**PLANAR SPLIT-GATE HIGH-PERFORMANCE MOSFET
STRUCTURE AND MANUFACTURING METHOD**DATE INVENTOR
SIGNED THE
DECLARATION

and executed a United States patent application therefor

on May 25, 2006;

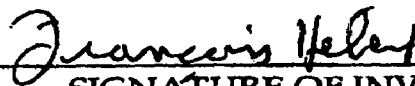
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Now therefore, for a valuable consideration, receipt whereof is hereby acknowledged, I, the above named, hereby sell, assign, and transfer to AOS, its successors and assigns, the entire right, title and interest in the said application and invention therein disclosed for the United States and foreign countries, and all rights of priority resulting from the filing said United States application, and I request the Commissioner of Patents to issue any Letters Patent granted upon the inventions set forth in said application to AOS its successors and assigns; and I hereby agree that AOS may apply for foreign Letters Patent on said invention and I will execute all papers necessary in connection with the United States and foreign applications when called upon to do so by

CITY AND

Signed and Sealed at Sunnyvale, California

DATE

on May 25, 2006.


SIGNATURE OF INVENTOR

ASSIGNMENT

INVENTOR
AND CITYWhereas, I, Daniel Ng of 3887 Via Montalvo,
Campbell, California 95008

have invented:

TITLE:

**PLANAR SPLIT-GATE HIGH-PERFORMANCE MOSFET
STRUCTURE AND MANUFACTURING METHOD**DATE INVENTOR
SIGNED THE
DECLARATION

and executed a United States patent application therefor

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DATE

on 5/25, 2006.


SIGNATURE OF INVENTOR