PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE: NEW ASSIGNMENT

NATURE OF CONVEYANCE: ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Micron Technology, Inc.	10/03/2008

RECEIVING PARTY DATA

Name:	Aptina Imaging Corporation		
Street Address:	c/o Citco Trustees (Cayman) Limited		
Internal Address:	Regatta Office Park, West Bay Road		
City:	Grand Cayman		
State/Country:	CAYMAN ISLANDS		
Postal Code:	Y1-1205		

PROPERTY NUMBERS Total: 1

Property Type	Number	
Application Number:	11140157	

CORRESPONDENCE DATA

Fax Number: (610)407-0701

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 610-407-0700

Email: sseiler@ratnerprestia.com

Correspondent Name: Kenneth N. Nigon

Address Line 1: 1235 Westlakes Drive, Suite 301

Address Line 2: RatnerPrestia

Address Line 4: Berwyn, PENNSYLVANIA 19312

ATTORNEY DOCKET NUMBER: MICR-309US

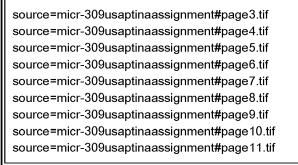
NAME OF SUBMITTER: Kenneth N. Nigon

Total Attachments: 11

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PATENT REEL: 022554 FRAME: 0971 OF \$40.00 1114015/

500836528



PATENT ASSIGNMENT AGREEMENT

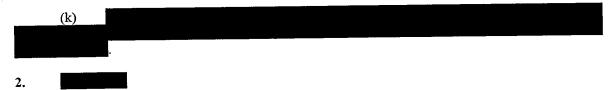
1. Definitions

- (a) "Agreement" shall mean this Patent Assignment Agreement.
- (b) "Assignee" shall mean Aptina Imaging Corporation, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.
- (c) "Assignor" shall mean Micron Technology, Inc., a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.
 - (d) "Effective Date" shall mean October 3, 2008.

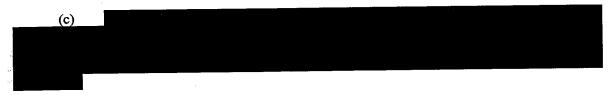


- (g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.
- (i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

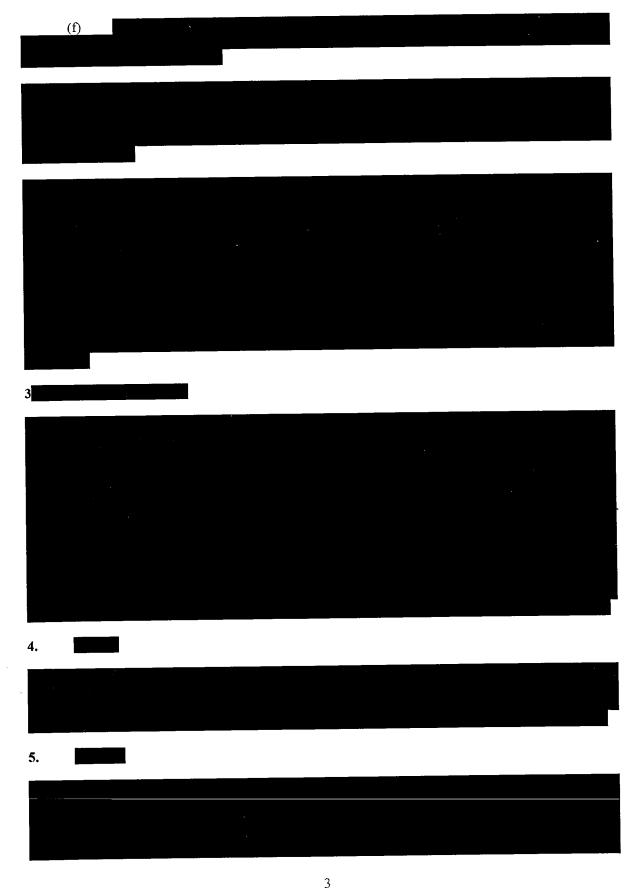


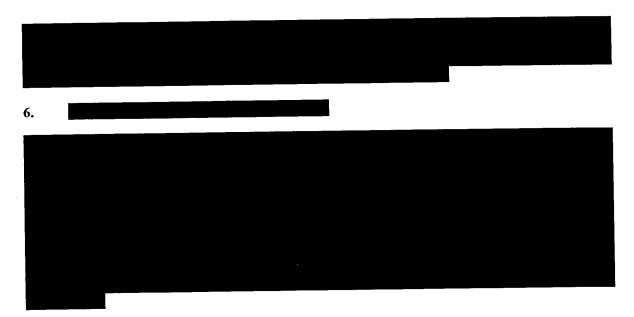
- (a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee.
- (b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.



- (d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee.
- (e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any,

 Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.





7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

[the rest of this page intentionally left blank]

	Agreed to by:	
	Micron Technology, Inc.	
	Signed:	
	Date: 2/24/12	, 2008
Notarization State of Idaho County of Ada On 9/26/08, before me, far 14 D. Song	nersonally appeared the above-named Sking Ix	Appleton
who executed this Assignment in my presence and ack		
authorized capacity for the purposes set forth herein.	Signed:	>-
REVIEWED NTI LEGAL	My commission expires: 8/12/11	
C. S. R. S. S. S. Milling	Agreed to by:	
NACOTION TO A CONTROLL OF THE PROPERTY OF THE	Aptina Imaging Corporation Signed: Signed: 9/26/09	_, 2008
Notarization State of Idaho County of Ada		
On 2/26/08, before me, Paula R. Spans (notary name)		
who executed this Assignment in my presence and ack authorized capacity for the purposes set forth herein.	mowledged to me that he did so of his ewn free will	and in his
A R. Somme	Signed: My commission expires: 8/12/11	
A R. SOMETHING TANDERS OF TOTAL THE STATE OF TOTAL		

[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"

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ATTACHMENT "B"

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Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
AVGO-0185.00/JP	Methods for packaging an	Pending	Јарап	2006147874	May 29 ; 2006_
	image sensor and a packaged image sensor				2000
AVGO-0185.00/TW	Methods for packaging an image sensor and a packaged image sensor	Pending	Taiwan R.O.C.	95118098	May 22, 2006
AVG0-0185,00/US	Methods for packaging an image sensor and a packaged image sensor	Pending	United States of America	11140157	May 27, 2005
AVGO-0190.00/JP	Autofucus Detecting System And Assisitant Module Therefor	Pending	Japan	P2004305977	Oct 20, 2004
AVGO-0191 ¹ 00/PC	Shallow Semiconductor Sensor With Fluorescent Molecule: Layer Thal Ellminates: Optical Ande Electronic Crosstalk	.Pánding	PCT	PCTUS20070116781	May 16, 2007
				096119537	May 31, 2007
AVGO-0191.00/TW	Shallow Semiconductor Sensor With Fluorescent Molecule Layer That Eliminates Optical And Electronic Crosstalk	Pending	Taiwan R.O.C.	030113337	
AVGG-0191:00/US	Shallow Semiconductor. Sensor With Fluorescent Mojecule Layer That Eliminates Optical And Electronic Crosstalk	Pending	United States of America	11443162	May 31, 2006
AVGO-0192.00/JP	CONDUCTIVE GUARD RINGS FOR ELEVATED ACTIVE PIXEL SENSORS	Pending	Japan	P2000352745	Nov 20, 2000
AVGO-0193.00/EP	Image Sensor Device Having Improved Noise Suppression Capability And AsMethod For Suppressing Noise in An Image (Sensor Device 1)		European Patent Office	1077967900	Juli1=2007
AVGO-0193.00/PC	Image Sensor Device Having Improved Noise Suppression Capability And A Method For Supressing Noise In An Image Sensor Device	·	PCT	PCTUS2007015799	Jul 11, 2007
/AVGO-0193:00/ЛW	mage Sansor/Device Having Improved Noise/Suppression Capability And A Method For- Supressing Noise in An image		s traiwanikio.c.	096126048	U0117, 2007.

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ATTACHMENT "C"

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ATTACHMENT "D"

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ATTACHMENT "E"

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