Form PTO-1595 (Rev. 07/05)

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OMB No. 0651-0027 (exp. 6/30/2008)	Officer States Latent and Trademark Office					
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SHAMARAO, Prashant	Name: Netlogic Microsystems, Inc.					
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3. Nature of conveyance/Execution Date(s):	Street Address: 1875 Charleston Road					
Execution Date(s) 04 April 2009						
✓ Assignment						
Security Agreement Change of Name	City: Mountain View					
Joint Research Agreement	State: California					
Government Interest Assignment						
Executive Order 9424, Confirmatory License	Country: <u>U.S.A.</u> Zip: <u>94043-1215</u>					
Other	Additional name(s) & address(es) attached? Yes No					
	document is being filed together with a new application.					
A. Patent Application No.(s)	B. Patent No.(s)					
Additional numbers at	ached? ☐ Yes ✔ No					
5. Name and address to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved: 1					
Name: HAVERSTOCK & OWENS, LLP	7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00					
Internal Address:	Authorized to be charged by credit card					
	Authorized to be charged to deposit account					
Street Address: 162 North Wolfe Road	Finchesed					
Office Address. 192 North West 1080	None required (government interest not affecting title)					
City: Sunnyvale	8. Payment Information					
State: CA Zip: 94086	a. Credit Card Last 4 Numbers Expiration Date					
Phone Number: (408) 530-9700	· · · · · · · · · · · · · · · · · · ·					
Fax Number: (408) 530-9797	b. Deposit Account Number					
Email Address: <u>bsako@hollp.com</u>	Authorized User Name					
9. Signature: /Product T Sake/	42 A					
/Bradley T. Sako/ Signature	13 April 2009 Date					
Bradley T. Sako Name of Person Signing	Total number of pages including cover sheet, attachments, and documents:					

Documents to be recorded (including cover sheet) should be faxed to (571) 273-0140, or mailed to: Mail Stop Assignment Recordation Services, Director of the USPTO, P.O.Box 1450, Alexandria, V.A. 22313-1450 /15/2009 WABDELR1 00000049 12386128

ASSIGNMENT OF PATENT APPLICATION

(Page 1 of 2)

FOR GOOD AND VALUABLE CONSIDERATION,

I, the undersigned hereby agrees to assign for good and valuable consideration, receipt of which is hereby expressly acknowledged, and do hereby sell, assign and transfer unto

Netlogic Microsystems, Inc. 1875 Charleston Road Mountain View, California, 94043-1215,USA

a corporation organized under the laws of the state of <u>Delaware</u> as Assignee, and its successors, assigns and legal representatives, the entire right, title and interest, for all countries in and to certain inventions relating to

SUBSTRATE BIAS CIRCUIT AND METHOD FOR INTEGRATED CIRCUIT DEVICE

described in an application for Letters Patent of the United States, identified as Attorney Docket No. NETL-08900, at HAVERSTOCK & OWENS, LLP, 162 North Wolfe Road, Sunnyvale, CA 94086 and

<u>X</u>	attached hereto,	, or			
:	filed on		, as	Application	Serial No.

and the invention(s) and improvement(s) set forth therein, and any and all non-provisional, continuations, continuations-in-part, divisionals, and renewals of and substitutes for said application for said Letters Patent or provisional application, and all the rights and privileges under any and all Letters Patent that may be granted therefor in any country, and any reissues, or reexaminations, or extensions of said Letters Patent. I request that any and all Letters Patent for said inventions be issued to said Assignee, its successors, assigns and legal representatives, or to such nominees as it may designate.

I agree that, when requested, I will, without charge to said Assignee but at its expense, sign all papers, take all rightful oaths, and do all acts which may be necessary, desirable or convenient for securing and maintaining patents for said inventions in any and all countries and for vesting title thereto in said Assignee, its successors, assigns and legal representatives or nominees.

I authorize and empower the said Assignee, its successors, assigns and legal representatives or nominees, to invoke and claim for any application for patent or other form of protection for said inventions filed by it or them, the benefit of the right of priority provided by the International Convention for the Protection of Industrial Property, as amended, or by any convention which may henceforth be substituted for it, and to invoke and claim such right of priority without further written or oral authorization from me.

PATENT REEL: 022591 FRAME: 0037

ASSIGNMENT OF PATENT APPLICATION (Page 2 of 2)

For the invention(s) related to: SUBSTRATE BIAS CIRCUIT AND METHOD FOR INTEGRATED CIRCUIT DEVICE

to Assignee: Netlogic Microsystems, Inc

I hereby consent that a copy of this assignment shall be deemed a full legal and formal equivalent of any assignment, consent to file or like document which may be required in any country for any purpose and more particularly in proof of the right of the said Assignee or nominee to claim the aforesaid benefit of the right of priority provided by the International convention which may henceforth be substituted for it.

I hereby authorize HAVERSTOCK & OWENS, LLP, attorneys for Assignee, to insert the filing date and Serial number into the first paragraph of this assignment, after the application for Letters Patent has been filed, and the U.S. Patent Office has assigned such application a Serial Number.

I covenant with said Assignee, its successors, assigns and legal representatives, that the rights and property herein conveyed are free and clear of any encumbrance, and that I have full right to convey the same as herein expressed.

IN WITNESS WHEREOF, I have hereunto signed my name on the day and year set forth below.

Inventor's Signature

Prashant Shamarao

RECORDED: 04/13/2009

Inventor's Printed Name

04/04/09 DATE

> PATENT REEL: 022591 FRAME: 0038