

PATENT ASSIGNMENT

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NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Genesis Microchip (Delaware) Inc.	03/13/2009
RECEIVING PARTY DATA	
Name:	Tamiras Per Pte. Ltd., LLC
Street Address:	160 Greentree Drive, Suite 101
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State/Country:	DELAWARE
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	11021130
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NAME OF SUBMITTER:	Kevan L. Morgan
Total Attachments: 13 source=32751_Assignment2_GMI2#page1.tif source=32751_Assignment2_GMI2#page2.tif source=32751_Assignment2_GMI2#page3.tif	

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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Genesis Microchip (Delaware) Inc., a Delaware corporation, having an office at 1310 Electronics Drive, Carrollton, TX 75006 ("**Assignor**"), does hereby sell, assign, transfer, and convey unto Tamiras Per Pte. Ltd., LLC, a Delaware limited liability company, with an address at 160 Greentree Drive, Suite 101, Dover, DE 19904 ("**Assignee**"), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the "**Patent Rights**"):

(a) the provisional patent applications, patent applications and patents listed in the table below (the "**Patents**");

(b) all patents and patent applications, excluding the provisional patent applications, patent applications and patents listed in Table 2 below, (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority;

(c) all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisions, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, patent applications, and counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceedings brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e);

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

- (1) damages,
- (2) injunctive relief, and
- (3) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
5,847,701 (08/872,774)	US	12/8/1998 (6/10/1997)	Method and apparatus implemented in a computer system for determining the frequency used by a graphics source for generating an analog display signal Eglit, Alexander Julian
5,987,624 (08/872,764)	US	11/16/1999 (6/10/1997)	Method and apparatus for automatically determining signal parameters of an analog display signal received by a display unit of a computer system Eglit, Alexander Julian
TW123426 (TW87106266)	TW	12/1/2000 (4/23/1998)	A method and apparatus for automatically determining signal parameters of an analog display signal received by a display unit of a computer system Eglit, Alexander Julian
KR10-339765 (KR10-1998-0022456)	KR	5/24/2002 (6/10/1998)	Method and apparatus for automatically determining signal parameters of an analog display signal received by a display unit of a computer system Eglit, Alexander Julian
6,005,544 (09/023,815)	US	12/21/1999 (2/13/1998)	Digital display unit in a computer system for enabling a user to conveniently select a desired monitor mode for displaying images encoded in a received analog display signal Eglit, Alexander Julian

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
6,011,538 (08/877,708)	US	1/4/2000 (6/18/1997)	Method and apparatus for displaying images when an analog-to-digital converter in a digital display unit is unable to sample an analog display signal at a desired high sampling frequency Eglit, Alexander Julian
6,028,571 (09/035,261)	US	2/22/2000 (3/5/1998)	Digital display unit in a computer system with an improved method and apparatus for determining a source mode using which a received analog display signal was generated Eglit, Alexander Julian
6,046,738 (08/909,825)	US	4/4/2000 (8/12/1997)	Method and apparatus for scanning a digital display screen of a computer screen at a horizontal scanning frequency lower than the origin frequency of a display signal Eglit, Alexander Julian; Han, Robin Sungsoo
6,054,980 (09/227,284)	US	4/25/2000 (1/6/1999)	Display unit displaying images at a refresh rate less than the rate at which the images are encoded in a received display signal Eglit, Alexander Julian
6,147,668 (09/100,503)	US	11/14/2000 (6/20/1998)	Digital display unit of a computer system having an improved method and apparatus for sampling analog display signals Eglit, Alexander Julian
6,307,498 (09/655,195)	US	10/23/2001 (9/5/2000)	Digital display unit of a computer system having an improved method and apparatus for sampling analog display signals Eglit, Alexander Julian
6,157,376 (09/164,081)	US	12/5/2000 (9/30/1998)	Method and apparatus for generating a target clock signal having a frequency of x/y times the frequency of a reference clock signal Eglit, Alexander Julian
6,232,952 (09/164,080)	US	5/15/2001 (9/30/1998)	Method and apparatus for comparing frequently the phase of a target clock signal with the phase of a reference clock signal enabling quick synchronization Eglit, Alexander Julian

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
6,268,848 (09/177,897)	US	7/31/2001 (10/23/1998)	Method and apparatus implemented in an automatic sampling phase control system for digital monitors Eglit, Alexander Julian
6,272,193 (09/406,332)	US	8/7/2001 (9/27/1999)	Receiver to recover data encoded in a serial communication channel Eglit, Alexander Julian
DE60033606 (DE00302304.1)	DE	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel (Title in German: Empfänger zur Rückgewinnung von in einem seriellen Kommunikationskanal kodierten Daten) Eglit, Alexander Julian
FR1087564 (FR00302304.1)	FR	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
GB1087564 (GB00302304.1)	GB	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
NL1087564 (NL00302304.1)	NL	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
TW149369 (TW89105499)	TW	1/21/2002 (3/24/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
KR10-0400186 (KR10-2000-0017828)	KR	9/19/2003 (4/6/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
JP2000-180219	JP	6/15/2000	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
6,430,240 (09/866,681)	US	8/6/2002 (5/30/2001)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
6,459,426 (09/135,216)	US	10/1/2002 (8/17/1998)	Monolithic Integrated Circuit Implemented In A Digital Display Unit For Generating Digital Data Elements From An Analog Display Signal Received At High Frequencies Eglit, Alexander Julian; Chan, Tzoyao; Lattanzi, John
6,765,563 (10/109,891)	US	7/20/2004 (4/1/2002)	Monolithic Integrated Circuit Implemented In A Digital Display Unit For Generating Digital Data Elements From An Analog Display Signal Received At High Frequencies Eglit, Alexander Julian; Chan, Tzoyao; Lattanzi, John
6,483,447 (09/350,204)	US	11/19/2002 (7/7/1999)	Digital Display Unit Which Adjusts The Sampling Phase Dynamically For Accurate Recovery Of Pixel Data Encoded In An Analog Display Signal Eglit, Alexander Julian
6,545,688 (09/592,049)	US	4/8/2003 (6/12/2000)	Scanning An Image Within A Narrow Horizontal Line Frequency Range Irrespective Of The Frequency At Which The Image Is Received Loveridge, Graham David; Frisk, Nils Anders
EP01114075.3	EP	6/9/2001	Scanning An Image Within A Narrow Horizontal Line Frequency Range Irrespective Of The Frequency At Which The Image Is Received Loveridge, Graham David; Frisk, Nils Anders
JP2001-176217	JP	6/11/2001	Scanning An Image Within A Narrow Horizontal Line Frequency Range Irrespective Of The Frequency At Which The Image Is Received Loveridge, Graham David; Frisk, Nils Anders
KR10-0772079 (KR10-2001- 0032980)	KR	10/25/2007 (6/12/2001)	Scanning An Image Within A Narrow Horizontal Line Frequency Range Irrespective Of The Frequency At Which The Image Is Received Loveridge, Graham David; Frisk, Nils Anders

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
TW152019 (TW89112275)	TW	3/21/2002 (6/22/2000)	Scanning An Image Within A Narrow Horizontal Line Frequency Range Irrespective Of The Frequency At Which The Image Is Received Loveridge, Graham David; Frisk, Nils Anders
6,771,837 (09/405,084)	US	8/3/2004 (9/27/1999)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Berbecel, Gheorghe; Selby, Steve
DE60037860 (DE00203305.8)	DE	1/23/2008 (9/25/2000)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement (Title In German: Verfahren Und Vorrichtung Zur Skalierung Von Numerischen Bildern Mit Adaptiver Kontrastverstärkung) Berbecel, Gheorghe; Selby, Steve
FR1093086 (FR00203305.8)	FR	1/23/2008 (9/25/2000)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Berbecel, Gheorghe; Selby, Steve
GB1093086 (GB00203305.8)	GB	1/23/2008 (9/25/2000)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Berbecel, Gheorghe; Selby, Steve
NL1093086 (NL00203305.8)	NL	(9/25/2000)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Berbecel, Gheorghe; Selby, Steve
JP2000-295106	JP	9/27/2000	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Method And System For Revising Magnification Of Digital Image By Emphasis Of Adaptive Contrast Berbecel, Gheorghe; Selby, Steve
KR10-0745660 (KR10-2000- 0056741)	KR	7/27/2007 (9/27/2000)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Berbecel, Gheorghe; Selby, Steve
TW150617 (TW89119961)	TW	1/21/2002 (9/27/2000)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Berbecel, Gheorghe; Selby, Steve

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
6,778,170 (09/666,666)	US	8/17/2004 (4/7/2000)	Generating High Quality Images In A Display Unit Without Being Affected By Error Conditions In Synchronization Signals Contained In Display Signals Shu, Hong Jun; Kobayashi, Osamu; Wang, Wen-Jyh
6,845,450 (09/652,415)	US	1/18/2005 (8/31/2000)	Display Unit Storing And Using A Cryptography Key Kobayashi, Osamu; Noorbakhsh, Ali; Hang, Chia-Lun; Soong, Jih-Hsien; Chan, Tzoyao
7,206,943 (10/813,346)	US	4/17/2007 (3/29/2004)	Display Unit Storing And Using A Cryptography Key Kobayashi, Osamu; Noorbakhsh, Ali; Hang, Chia-Lun; Soong, Jih-Hsien; Chan, Tzoyao
6,922,188 (10/086,654)	US	7/26/2005 (2/27/2002)	Method And Apparatus For Auto-Generation Of Horizontal Synchronization Of An Analog Signal To A Digital Display Neal, Greg
7,019,764 (10/243,518)	US	3/28/2006 (9/12/2002)	Method And Apparatus For Auto-Generation Of Horizontal Synchronization Of An Analog Signal To Digital Display Neal, Greg
7,362,319 (11/011,399)	US	4/22/2008 (12/13/2004)	Method And Apparatus For Auto-Generation Of Horizontal Synchronization Of An Analog Signal To A Digital Display Neal, Greg
11/021,130	US	12/21/2004	Method And Apparatus For Auto-Generation Of Horizontal Synchronization Of An Analog Signal To A Digital Display Neal, Greg
12/331,303	US	12/9/2008	Method And Apparatus For Auto-Generation Of Horizontal Synchronization Of An Analog Signal To A Digital Display Neal, Greg


Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at _____
Carrollton on March 13, 2009.

ASSIGNOR:

GENESIS MICROCHIP (DELAWARE) INC.

By: 
Name: Steven Rose
Title: Vice-President
(Signature MUST be attested)

ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. 1746

The undersigned witnessed the signature of Steven Rose ("Assignor's Representative") to the above Assignment of Patent Rights on behalf of Genesis Microchip (Delaware) Inc. and makes the following statements:

1. I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.
2. Assignor's Representative is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me to execute the above Assignment of Patent Rights on behalf of Genesis Microchip (Delaware) Inc.
3. Assignor's Representative subscribed to the above Assignment of Patent Rights on behalf of Genesis Microchip (Delaware) Inc.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on March 13, 2009 (date)

Signature: 
Print Name: Debbie Ramos

**TABLE 2
EXCLUDED ASSETS**

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
5,225,910 (07/615,700)	US	7/6/1993 (11/20/1990)	Adaptive Operation Type Low Noise Television System Sugimori, Yoshio; Ito, Toshiya; Faroudja, Yves C.
JP2549025 (JPH03-096292)	JP	8/8/1996 (4/1/1991)	Adaptive Operation Type Low Noise Television System Sugimori, Yoshio; Ito, Toshiya; Faroudja, Yves C.
60/323,968	US	9/20/2001	Method and Apparatus for Synchronizing an Analog Video Signal to an LCD Monitor Neal, Greg
7,034,815 (10/071,409)	US	4/25/2006 (2/8/2002)	Method And Apparatus For Synchronizing An Analog Video Signal To An LCD Monitor Neal, Greg
11/264,261	US	10/31/2005	Method And Apparatus For Synchronizing An Analog Video Signal To An LCD Monitor Neal, Greg
60/620,094	US	10/18/2004	Virtual extended display identification data (EDID) Ali Noorbakhsh, David Keene, John Lattanzi, Ram Chilukuri
11/060,917	US	2/18/2005	Power Management in a display controller Ali Noorbakhsh, David Keene, John Lattanzi, Ram Chilukuri
11/061,151	US	2/18/2005	Virtual extended display information data (EDID) in a flat panel controller Ali Noorbakhsh, David Keene, John Lattanzi, Ram Chilukuri
11/061,165	US	2/18/2005	Method for acquiring extended display identification data (EDID) in a powered down EDID compliant display controller Ali Noorbakhsh, David Keene, John Lattanzi, Ram Chilukuri
60/561,042	US	4/9/2004	LCD Overdrive Data Compression For Reducing Memory Bandwidth And Data Threshold For Keeping Video Quality Wu, Che Ming; Wang, Vincent; Doung, Cheen

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
10/874,849	US	6/22/2004	Selective Use Of LCD Overdrive For Reducing Motion Artifacts In An LCD Device Wu, Che Ming; Wang, Vincent; DOUNG, Cheen
60/532,427	US	12/23/2003	Recursive hierarchical motion compensated frame rate conversion Hari N. Nair, Gordon Petrides, Peter Swartz, Steve Selby
7,346,109 (10/832,838)	US	3/18/2008 (4/26/2004)	Motion vector computation for video sequences Nari N. Nair, Gordon Petrides
11/924,463	US	10/25/2007	Motion vector computation for video sequences Hari N. Nair, Gordon Petrides
CN20041102089	CN	12/22/2004	Vector selection decision for pixel interpolation Hari N. Nair, Gordon Petrides
EP20040257855	EP	12/16/2004	Motion compensated frame rate conversion Hari N. Nair, Gordon Petrides
JP20040369560	JP	12/21/2004	Frame rate conversion for motion compensationHari N. Nair, Gordon Petrides
KR20040100133	KR	12/2/2004	Motion compensated frame rate conversion Hari N. Nair, Gordon Petrides
SG20040006853	SG	11/24/2004	Motion compensated frame rate conversion Hari N. Nair, Gordon Petrides
60/170,607	US	12/14/1999	Method of Processing Data Greicar, Richard K.
60/170,668	US	12/14/1999	Multi-Component Processor Greicar, Richard K.
PCT/US2000/033117	WO	12/5/2000	Multi-Component Processor Greicar, Richard K.
6,775,757 (09/678,857)	US	8/10/2004 (10/2/2000)	Multi-Component Processor Greicar, Richard K.
10/875,364	US	6/23/2004	Multi-Component Processor Greicar, Richard K.

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventor(s)</u>
4,982,280 (07/381,497)	US	1/1/1991 (7/18/1989)	Motion sequence pattern detector for video Thomas C. Lyon, Jack J. Campbell
JP3187409 (JP02-187018)	JP	5/11/2001 (7/12/1990)	Motion sequence pattern detector Thomas C. Lyon, Jack J. Campbell

ASSIGNMENT OF RIGHTS IN CERTAIN ASSETS

For good and valuable consideration, the receipt of which is hereby acknowledged, Genesis Microchip (Delaware) Inc., a Delaware corporation, having an office at 1310 Electronics Drive, Carrollton, TX 75006 ("**Assignor**"), does hereby sell, assign, transfer, and convey Tamiras Per Pte. Ltd., LLC, a Delaware limited liability company, with an address at 160 Greentree Drive, Suite 101, Dover, DE 19904 ("**Assignee**"), or its designees, all of Assignor's right, title, and interest which Assignor has, if any, in and to any and all of the following provisional patent applications, patent applications, patents, and other governmental grants or issuances of any kind (the "**Certain Assets**"):

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventors</u>
JP10-162106	JP	6/10/1998	Method And Apparatus For Automatically Determining Signal Parameters Of An Analog Display Signal Received By A Display Unit Of A Computer System Eglit, Alexander Julian
09/738,217	US	12/15/2000	Method And Apparatus For Comparing Frequently The Phase Of A Target Clock Signal With The Phase Of A Reference Clock Signal Enabling Quick Synchronization Eglit, Alexander Julian
EP1087564 (EP00302304.1)	EP	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
IE1087564 (IE00302304.1)	IE	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
LU1087564	LU	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
MC1087564 (MC00302304.1)	MC	2/28/2007 (3/22/2000)	Receiver To Recover Data Encoded In A Serial Communication Channel Eglit, Alexander Julian
60/184,999	US	2/25/2000	Display Unit Storing And Using A Cryptography Key Kobayashi, Osamu; Noorbakhsh, Ali; Hang, Chia-Lun; Soong, Jih-Hsien; Chan, Tzoyao

<u>Patent or Application No.</u>	<u>Country</u>	<u>Filing Date</u>	<u>Title of Patent and Inventors</u>
60/561,033	US	4/9/2004	LCD Overdrive Table Trangular Interpolation Andrew MacKinnon, Steve Selby
60/619,060	US	10/15/2004	Hybrid AGC For Video Decoder Mushirahad, Venkat Chary; Thomas, Sujana; Shettigara, Rajanatha
EP1093086 (EP00203305.8)	EP	1/23/2008 (9/25/2000)	Method And Apparatus For Digital Image Rescaling With Adaptive Contrast Enhancement Berbecel, Gheorghe; Selby, Steve

Assignor assigns to Assignee all of Assignor's rights to the inventions, invention disclosures, and discoveries which Assignor has, if any, in the assets listed above, together, with the rights, if any, to revive prosecution of claims under such assets and to sue or otherwise enforce any claims under such assets for past, present or future infringement.


Assignor hereby authorizes the respective mental agency in each jurisdiction to make available to Assignee all records regarding the Certain Assets.

The terms and conditions of this Assignment of Rights in Certain Assets will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

DATED this 13 day of March 2009.

ASSIGNOR:

Genesis Microchip (Delaware) Inc.

By: 
Name: Steven Rose
Title: Vice-President