

**PATENT ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
Name	Execution Date
Micron Technology, Inc.	10/03/2008
<b>RECEIVING PARTY DATA</b>	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman) Limited
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
<b>PROPERTY NUMBERS Total: 1</b>	
Property Type	Number
Application Number:	11707848
<b>CORRESPONDENCE DATA</b>	
Fax Number:	(610)407-0701
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	610-407-0700
Email:	sseiler@ratnerprestia.com
Correspondent Name:	Kenneth N. Nigon
Address Line 1:	1235 Westlakes Drive, Suite 301
Address Line 2:	RatnerPrestia
Address Line 4:	Berwyn, PENNSYLVANIA 19312
ATTORNEY DOCKET NUMBER:	MICR-240US1
NAME OF SUBMITTER:	Kenneth N. Nigon
Total Attachments: 11 source=micr-240us1aptinaassignment#page1.tif source=micr-240us1aptinaassignment#page2.tif	

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**PATENT ASSIGNMENT AGREEMENT**

**1. Definitions**

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

**Micron Technology, Inc.**

Signed: *[Signature]*

Date: 9/20/08, 2008

Notarization

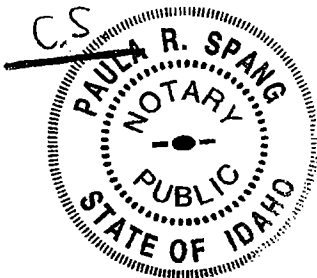
State of Idaho  
County of Ada

On 9/26/08, before me, *Paula R. Spang*, personally appeared the above-named *Steven R. Appleton*

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: *[Signature]*  
My commission expires: 8/12/11

REVIEWED  
MTI LEGAL



Agreed to by:

**Aptina Imaging Corporation**

Signed: *Thomas L. Lewis, Jr.*

Date: 9/26/08, 2008

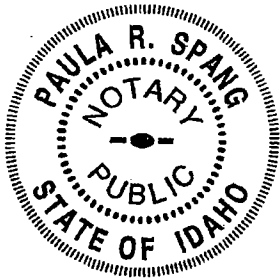
Notarization

State of Idaho  
County of Ada

On 9/26/08, before me, *Paula R. Spang*, personally appeared the above-named *Thomas L. Lewis, Jr.*

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: *[Signature]*  
My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"





ATTACHMENT "B"



## Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
	buffer-capacity-level-based compression adjustment				
AVGO-0117.00/TW	Image encoding with dynamic buffer-capacity-level-based compression adjustment	Pending	Taiwan R.O.C.	094126148	Jul 25, 2005
AVGO-0117.00/US	Image encoding with dynamic buffer-capacity-level-based compression adjustment	Allowed	United States of America	10987260	Nov 12, 2004
AVGO-0118.00/CN	OPTICAL ENHANCEMENT OF INTEGRATED CIRCUIT PHOTODETECTORS	Pending	China	2005100931682	Aug 19, 2005
AVGO-0118.00/GB	OPTICAL ENHANCEMENT OF INTEGRATED CIRCUIT PHOTODETECTORS	Pending	United Kingdom	05227582	Nov 8, 2005
AVGO-0118.00/JP	OPTICAL ENHANCEMENT OF INTEGRATED CIRCUIT PHOTODETECTORS	Pending	Japan	2005323267	Nov 8, 2005
AVGO-0118.00/TW	OPTICAL ENHANCEMENT OF INTEGRATED CIRCUIT PHOTODETECTORS	Pending	Taiwan R.O.C.	094125286	Jul 26, 2005
AVGO-0118.01/US	OPTICAL ENHANCEMENT OF INTEGRATED CIRCUIT PHOTODETECTORS	Allowed	United States of America	11716953	Mar 12, 2007
AVGO-0119.00/US	Pixel With Asymmetric Transfer Gate Channel Doping	Pending	United States of America	11323693	Dec 30, 2005
AVGO-0119.01/US	Pixel With Asymmetric Transfer Gate Channel Doping	Pending	United States of America	11707648	Feb 16, 2007
AVGO-0122.00/CN	System and method for processing images using centralized image correction data	Pending	China	2006100996234	May 25, 2006
AVGO-0122.00/GB	System and method for processing images using centralized image correction data	Pending	United Kingdom	06104038	May 25, 2006
AVGO-0122.00/HK	System and method for processing images using centralized image correction data	Pending	Hong Kong	071069385	Jun 28, 2007
AVGO-0122.00/JP	System and method for processing images using centralized image correction	Pending	Japan	2006142297	May 23, 2006

ATTACHMENT "C"



ATTACHMENT "D"



ATTACHMENT "E"

