PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Michael Laisne	06/01/2009
Karim Arabi	06/01/2009
Tsvetomir Petrov	06/01/2009

RECEIVING PARTY DATA

Name:	QUALCOMM Incorporated	
Street Address:	5775 Morehouse Drive	
Internal Address:	Patent Department	
City:	San Diego	
State/Country:	CALIFORNIA	
Postal Code:	92121	

PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	12480754

CORRESPONDENCE DATA

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NAME OF SUBMITTER: Peter M. Kamarchik

Total Attachments: 2

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PATENT REEL: 022797 FRAME: 0311 12480/5

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PATENT REEL: 022797 FRAME: 0312

ASSIGNMENT

Systems and Methods Utilizing Redundancy in Semiconductor Chip Interconnects

TO WHOM IT MAY CONCERN:

For the sum of One Dollar and other valuable consideration to us in hand paid, receipt of which is hereby acknowledged, be it known that We,

- Michael Laisne, a citizen of the United States, having a mailing address of 5775
 Morehouse Drive, San Diego, CA 92121; and a resident of San Diego, California.
- Karim Arabi, a citizen of the United States, having a mailing address of 5775 Morehouse
 Drive, San Diego, CA 92121; and a resident of San Diego, California.
- Tsvetomir Petrov, a citizen of the United States, having a mailing address of 5775
 Morehouse Drive, San Diego, CA 92121; and a resident of San Diego, California.

have sold, assigned and transferred and by these presents do sell, assign, transfer and set over unto QUALCOMM Incorporated, a Delaware corporation, having a place of business at 5775 Morehouse Drive, San Diego, California 92121-1714, its successors, legal representatives, or assigns, the entire right, title and interest in and to a certain invention relating to **Systems and Methods Utilizing Redundancy in Semiconductor Chip Interconnects** by me devised and the application for United States Patent therefore filed in the United States Patent and Trademark Office on <u>June 8, 2009</u>, and assigned Serial No. 12/480,754, (and I hereby authorize and instruct QUALCOMM Incorporated to hereafter complete this Assignment by inserting the serial number and date in the space

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ASSIGNMENT

Systems and Methods Utilizing Redundancy in Semiconductor Chip Interconnects

provided above when known), including, without limitation, all provisional patent applications, OR including Provisional Application Number 61/095,855, filed on September 10, 2008. and all original and reissue patents granted thereof, and all divisions, continuations and continuations-in-part thereof, including the subject matter of any and all claims which may be obtained in every such patent, and all foreign rights to said invention, and covenant that I have full right to do so, and agree that I will communicate to said corporation or its representatives all facts known to me respecting said invention, whenever requested, and testify in any legal proceedings, sign all lawful papers, make all rightful oaths and generally do everything possible to aid said corporation, its successors, assigns and nominees, to obtain and enforce proper patent protection for said invention in all countries.

The Commissioner of Patents and Trademarks is requested to issue the Letters Patent which may be granted for said invention or any part thereof unto said corporation in keeping with this Assignment,

Done at San Diego, California this / ST day of LOC, 2009.
Michael Laisne
Done at San Diego, California this day of
——————————————————————————————————————
Done at San Diego, California this day of, 2009.
The Aris Peter
Tsvetomir Petrov

REEL: 022797 FRAME: 0314

PATENT

RECORDED: 06/09/2009