

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	10/03/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman) Limited
Internal Address:	Regatta Office Park
City:	West Bay Road, Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	10814176
CORRESPONDENCE DATA	
Fax Number:	(202)420-2201
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	2024202200
Email:	DAmicoT@dicksteinshapiro.com
Correspondent Name:	Thomas J. D'Amico
Address Line 1:	1825 Eye Street NW
Address Line 4:	Washington, DISTRICT OF COLUMBIA 20006
ATTORNEY DOCKET NUMBER:	M4065.0825/P825-A
NAME OF SUBMITTER:	/tjd/
Total Attachments: 6 source=m40650825assgmnt#page1.tif source=m40650825assgmnt#page2.tif source=m40650825assgmnt#page3.tif	

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PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)

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(f)

A large rectangular area of the document is completely redacted with black ink, covering approximately six lines of text.

(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k)

[REDACTED]

2.

[REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c)

[REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED], Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. **General**

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: _____

Date: 9/20/08, 2008

Notarization
State of Idaho
County of Ada

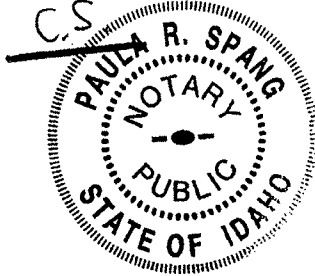
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven B. Appleton

(date) (notary name)
who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: _____

My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: _____

Date: 9/26/08, 2008

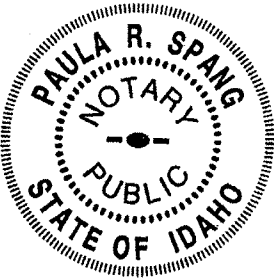
Notarization
State of Idaho
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Lowe, Jr.

(date) (notary name)
who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: _____

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

Aptina Applications

FILE NUMBER	TITLE	STATUS	COUNTRY NAME	APPLICATION NUMBER	DATE FILED
PBIT-0084.00/PR	A New Pixel Structure for Fram-Shuttering without Fixed Pattern Noise	Abandoned	United States of America	60130745	Apr 23, 1999
PBIT-0084.01/US	ACTIVE PIXEL SENSOR WITH REDUCED FIXED PATTERN NOISE	Pending	United States of America	11045155	Jan 31, 2005
PBIT-0088.03/US	LOW VOLTAGE ANALOG-TO-DIGITAL CONVERTERS WITH INTERNAL REFERENCE VOLTAGE AND OFFSET	Abandoned	United States of America	11060816	Feb 18, 2005
PBIT-0089.00/PR	Techniques to Increase Signal Dynamic Range in CMOS APS	Abandoned	United States of America	60151619	Aug 31, 1999
PBIT-0093.00/PR	Calibration Circuit for Successive Approximation ADC	Abandoned	United States of America	60171913	Dec 21, 1999
PBIT-0095.00/PR	P-type Reset/Readout Circuitry for Radiation Hard APS	Abandoned	United States of America	60151219	Aug 28, 1999
PBIT-0097.00/PR	Novel Lenses using coherent Optical Fiber Bundles	Abandoned	United States of America	60171849	Dec 22, 1999
PBIT-0099.00/PR	Adaptive Compression of High Dynamic Range Images Based on Histogram Equalization	Abandoned	United States of America	60179308	Jan 31, 2000
PBIT-0101.00/PR	Compact Realization of 2-Reset Pointer Rolling Shutter in CMOS Sensor	Abandoned	United States of America	60179378	Jan 31, 2000
PBIT-0101.01/US	LOOK AHEAD ROLLING SHUTTER SYSTEM IN CMOS SENSORS	Pending	United States of America	10958592	Oct 6, 2004
✓ PBIT-0105.01/US	COLOR FILTER ARRAY AND MICROLENS ARRAY	Pending	United States of America	10814176	Apr 1, 2004
PBIT-0110.00/PR	Improved Method for Flushed Reset	Abandoned	United States of America	60204372	May 16, 2000
PBIT-0110.01/US	IMAGE SENSORS WITH PIXEL RESET	Pending	United States of America	11473312	Jun 23, 2006
PBIT-0111.00/JP	FRAME SHUTTER FOR CMOS APS	Pending	Japan	2001562006	Aug 23, 2002
PBIT-0111.00/PC	FRAME SHUTTER FOR CMOS APS	Pending	PCT	pctus0140176	Feb 23, 2001