PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
TELASIC COMMUNICATIONS, INC.	06/01/2009

RECEIVING PARTY DATA

Name:	MICROELECTRONICS TECHNOLOGY, INC.
Street Address:	No. 1 Innovation Road II
Internal Address:	Hsinchu Science park, Hsinchu 300, Taiwan, R.O.C.
City:	Hsinchu
State/Country:	TAIWAN

PROPERTY NUMBERS Total: 79

Property Type	Number
Patent Number:	5128534
Patent Number:	5128674
Patent Number:	5130578
Patent Number:	5136205
Patent Number:	5164959
Patent Number:	5206647
Patent Number:	5220557
Patent Number:	5250911
Patent Number:	5251218
Patent Number:	5267272
Patent Number:	5271038
Patent Number:	5278837
Patent Number:	5303161
Patent Number:	5304951
Patent Number:	5313113

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Patent Number:	5315169
Patent Number:	5315231
Patent Number:	5343163
Patent Number:	5350952
Patent Number:	5410274
Patent Number:	5483150
Patent Number:	5572220
Patent Number:	5581213
Patent Number:	5684435
Patent Number:	5729576
Patent Number:	5774318
Patent Number:	5848160
Patent Number:	5859559
Patent Number:	5960040
Patent Number:	5870402
Patent Number:	5926123
Patent Number:	5963094
Patent Number:	5973631
Patent Number:	5990815
Patent Number:	5995535
Patent Number:	6040731
Patent Number:	6118811
Patent Number:	6157224
Patent Number:	6400229
Patent Number:	6535062
Patent Number:	6552343
Patent Number:	6580383
Patent Number:	6683904
Patent Number:	6693980
Patent Number:	6717450
Patent Number:	6825697
Patent Number:	6891424
Patent Number:	6931083
Patent Number:	6975189
Patent Number:	7071781

Patent Number:	7088148
Patent Number:	7095347
Patent Number:	7098684
Patent Number:	7098700
Patent Number:	7154421
Patent Number:	7253689
Patent Number:	4941153
Patent Number:	4975931
Patent Number:	5058107
Patent Number:	5621730
Patent Number:	5428305
Patent Number:	5592181
Patent Number:	5856760
Patent Number:	5859558
Patent Number:	5859568
Patent Number:	5859569
Patent Number:	6636730
Patent Number:	6879276
Patent Number:	6882294
Application Number:	10698257
Application Number:	10740334
Application Number:	10847433
Application Number:	10967963
Application Number:	11150445
Application Number:	11246027
Application Number:	11951238
Application Number:	12031249
Application Number:	11784433
Application Number:	11788451

CORRESPONDENCE DATA

Fax Number: (703)848-2981

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 703-584-3270

Email: cvillamar@rmsclaw.com

Correspondent Name: Carlos R. Villamar

Address Line 1:	7918 Jones Branch Drive, Suite 500
Address Line 4:	McLean, VIRGINIA 22102

ATTORNEY DOCKET NUMBER:	14485.13
NAME OF SUBMITTER:	Carlos R. Villamar

Total Attachments: 11
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Patent Assignment Agreement
(for United States patents, patent applications and other patent rights)

This Patent Assignment Agreement (this "**Agreement**") is made as of June 1, 2009 (the "**Effective Date**") by and between TelASIC Communications, Inc., a Delaware corporation ("**Assignor**"), and Microelectronics Technology Inc., a Republic of China company ("**Assignee**").

WHEREAS, Assignor and Assignee entered into that certain Asset Purchase Agreement concurrently with this Agreement (the "**Asset Purchase Agreement**") pursuant to which, among other things, Assignor sold, assigned, transferred, granted, bargained, setover, released, delivered, vested and conveyed to Assignee or cause to be sold, assigned, transferred, granted, bargained, setover, released, delivered and conveyed to Assignee all right, title and interest in the patents and patent applications registered in the United States as per Schedule A attached hereto and any and all other patent applications, patents and other patent rights of any nature (including, without limitation, any and all reissues, reexaminations, divisionals, renewals, extensions, provisionals, continuations and continuations in part) owned by Assignor (collectively, the "**Patents**").

WHEREAS, Assignor is the sole and exclusive owner of all rights, title and interest in and to the Patents.

NOW, THEREFORE, TO ALL WHOM IT MAY CONCERN, let it be known, that for and in consideration of the purchase price agreed to by the parties in the Asset Purchase Agreement, the receipt of which is hereby acknowledged and sufficient, and other good and valuable considerations, the Assignor an Assignee agree as follows:

1. ASSIGNMENT. Assignor hereby sells, assigns, transfers, grants, bargains, setsover, releases, delivers, vests and conveys to Assignee or causes to be sold, assigned, transferred, granted, bargained, setover, released, delivered and conveyed to Assignee all right, title and interest in the Patents, together with (a) all national, foreign and state registrations, applications for registration and renewals and extensions thereof (including the right to have any patent maturing therefrom granted in the name of the Assignee alone and reference herein to patent applications shall be taken to include reference to any such patent), (b) all common law rights related thereto, (c) all goodwill associated therewith, and (d) and all benefits, privileges, causes of action and remedies relating to any of the foregoing, whether before or hereafter accrued (including, without limitation, the exclusive rights to apply for and maintain all such registrations, renewals and/or extensions; to sue for all past, present or future infringements or other violations of any rights in any and/or all of the Patents; and to settle and retain proceeds from any such actions) (collectively, the "**Rights**"). Assignor retains no rights to use any of the Patents and agrees not to challenge the validity of Assignee's ownership therein.

2. COOPERATION. Assignor shall execute such written instruments, extend such other cooperation and perform such other acts as may be necessary or appropriate, in the reasonable opinion of Assignee, to convey, establish, evidence, maintain, defend and enforce Assignee's Rights in any and/or all of the Patents; and Assignor hereby irrevocably appoints Assignee and any of its officers as Assignor's attorney

in fact to undertake such acts in Assignor's name. Further, Assignor agrees that the change of ownership of the Patents may be recorded in the official United States Patent Office records.

3. NOTICES. All notices, requests, claims, demands and other communications hereunder shall be in writing and shall be given or made (and shall be deemed to have been duly given or made upon receipt) by delivery in person, by courier service, by cable, by facsimile, by telegram, by telex or by registered or certified mail (postage prepaid, return receipt requested) to the respective parties at the following addresses (or at such other address for a party as shall be specified in a notice given in accordance with this Section 3):

if to Assignor:

TelASIC Communications, Inc.
1940 East Mariposa Avenue, Suite 100
El Segundo, CA 90245
Facsimile: (310) 955-3770

Attention: Chief Executive Officer

with a copy to:

Gunderson Dettmer Stough Villeneuve Franklin & Hachigian, LLP
1200 Seaport Boulevard
Redwood City, CA 94063
Facsimile: (650) 321-2800
Attention: John Dietz, Esq.

if to Assignee:

Microelectronics Technology, Inc.
No. 1 Innovation Road II
Hsinchu Science par
Hsinchu 300, Taiwan, R. O. C.
Facsimile: 886-3-5770936
Attention: Mr. Shuhuei Fuong (Chief Financial Officer)

with a copy to:

Lee and Li, Attorneys-at-La
7F, 201 Tun Hua N. Roa
Taipei, Taiwan 10508, R. O. C
Facsimile: 886-2-27133966
Attention: Joyce Fan, Esq./Doris Lin, Esq.

and additional copy to:

Morrison & Foerster LLP
Suite 3501, Bund Center, No. 222
Yan An Road East Shanghai 200002
People's Republic of China
Facsimile: +86-21-2322-5300
Attention: Charles C. Comey, Esq.

4. **SEVERABILITY**. If any term or other provision of this Agreement is invalid, illegal or incapable of being enforced by any rule of Law or public policy, all other terms and provisions of this Agreement shall nevertheless remain in full force and effect so long as the economic or legal substance of the transactions contemplated by this Agreement is not affected in any manner materially adverse to any party. Upon a determination that any term or other provision is invalid, illegal or incapable of being enforced, the parties hereto shall negotiate in good faith to modify this Agreement so as to effect the original intent of the parties as closely as possible in a mutually acceptable manner in order that the transactions contemplated by this Agreement be consummated as originally contemplated to the greatest extent possible.

5. **ASSIGNMENT; BINDING EFFECT; BENEFIT**. Neither this Agreement nor any of the rights, interests or obligations hereunder shall be assigned by any of the parties hereto (whether by operation of Law or otherwise). Subject to the preceding sentence, this Agreement shall be binding upon and shall inure to the benefit of the parties hereto and their respective successors and assigns. Notwithstanding anything contained in this Agreement to the contrary, nothing in this Agreement, expressed or implied, is intended to confer on any person other than the parties hereto or their respective successors and assigns any rights, remedies, obligations or liabilities under or by reason of this Agreement.

6. **SPECIFIC PERFORMANCE**. The parties hereto agree that irreparable damage would occur in the event any provision of this Agreement was not performed in accordance with the terms hereof and that the parties shall be entitled to specific performance of the terms hereof in addition to any other remedy at law or in equity.

7. **GOVERNING LAW; FORUM**. This Agreement shall be governed by, and construed in accordance with, the laws of the State of California applicable to contracts executed in and to be performed in that state and without regard to any applicable conflicts of law. The parties hereto agree that any dispute arising out of, or in connection with, this Agreement shall be resolved solely and exclusively by confidential binding arbitration as follows: If the dispute relates to matters arising from a claim asserted by Assignee, the arbitration shall be held in Los Angeles, California and shall be governed by the International Dispute Resolution Procedures of the American Arbitration Association applicable at the time of the commencement of the arbitration (the "**AAA Rules**"). If the dispute relates to matters arising from a claim asserted by Assignor, the arbitration shall be held in Taipei, Taiwan, Republic of China and shall be with the Arbitration Association of the Republic of China and governed by and conducted in accordance with the Arbitration Act of the Republic of China and the Arbitration Rules of the Arbitration Association of the Republic of China. The arbitration panel shall consist of three arbitrators, two of which shall be selected by the parties hereto respectively, and such arbitrators shall select the third. The arbitration shall be conducted in English. Each party shall bear its own attorneys' fees, expert witness fees, and costs associated with any arbitration.

8. **AMENDMENTS**. This Agreement may not be amended or modified, nor may compliance with any condition or covenant set forth herein be waived, except by a writing duly and validly executed by each party hereto, or in the case of a waiver, the party waiving compliance.

9. **FURTHER ASSURANCES**. Each party hereto shall execute and cause to be delivered to each other party hereto such instruments and other documents, and shall take such other actions, as such other party

may reasonably request (prior to, at or after the Closing) for the purpose of carrying out or evidencing any of the transactions contemplated by this Agreement.

10. COUNTERPARTS. This Agreement may be executed and delivered (including by facsimile transmission) in two or more counterparts, each of which when executed and delivered shall be deemed to be an original but all of which taken together shall constitute one and the same agreement.

11. ENTIRE AGREEMENT. This Agreement (including the schedules) constitutes the entire agreement among the parties with respect to the subject matter hereof and supersede all prior agreements and understandings among the parties with respect thereto. No addition to or modification of any provision of this Agreement shall be binding upon any party hereto unless made in writing and signed by all parties hereto.

[*Remainder of page intentionally left blank — signature page follows*]

IN WITNESS WHEREOF, the Assignor has hereunto authorized the undersigned to affix the corporate seal and sign this instrument on its behalf this 1st day of June, 2009.

TELASIC COMMUNICATIONS, INC.

By:_____
Prabhat K. Dubey, Chief Executive Officer

IN WITNESS WHEREOF, the Assignee has hereunto authorized the undersigned to affix the corporate seal and sign this instrument on its behalf this 1st day of June, 2009.

By:_____

Name: Allen Yen

Title: President and Chief Executive Officer

Schedule A

U.S. Patents:

Title/Description	Patent #
High charge capacity focal plane array readout cell	5,128,534
Two quadrants high speed multiplying DAC	5,128,674
Efficient high speed N-word comparator	5,130,578
Microelectronic field emission device with air bridge anode	5,136,205
Digital equalization method and apparatus	5,164,959
Low cost AGC function for multiple approximation A/D converters	5,206,647
Multiple use digital transmitter/transceiver with time multiplexing	5,220,557
Single-ended and differential transistor amplifier circuits with full signal modulation compensation techniques which are technology independent	5,250,911
Efficient digital frequency division multiplexed signal receiver	5,251,218
Receiver automatic gain control (AGC)	5,267,272
Distortion suppression using thresholding techniques	5,271,038
Multiple user digital receiver apparatus and method with combined multiple frequency channels	5,278,837
Technology independent integrated circuit mask artwork generator	5,303,161

Title/Description	Patent #
Divider synchronization circuit for phase-locked loop frequency synthesizer	5,304,951
Sample and hold circuit with full signal modulation compensation using bipolar transistors of single conductivity type	5,313,113
Power-efficient sample and hold circuit using bipolar transistors of single conductivity type	5,315,169
Symmetrical bipolar bias current source with high power supply rejection ratio (PSRR)	5,315,231
Single-ended and differential transistor amplifier circuits with full signal modulation compensation techniques which are technology independent	5,343,163
Sample and hold circuit with push-pull output charging current	5,350,952
Single-ended and differential amplifiers with high feedback input impedance and low distortion	5,410,274
Transistor current switch array for digital-to-analog converter (DAC) including bias current compensation for individual transistor current gain and thermally induced base-emitter voltage drop variation	5,483,150
Technique to detect angle of arrival with low ambiguity	5,572,220
Variable gain amplifier circuit	5,581,213
Analog waveform communications reduced instruction set processor	5,684,435
Interference canceling receiver	5,729,576
I.C. power supply terminal protection clamp	5,774,318

Title/Description	Patent #
Digital synthesized wideband noise-like waveform	5,848,160
Mixer structures with enhanced conversion gain and reduced spurious signals	5,859,559
Communication signal processors and methods	5,960,040
Multiple user digital receiver apparatus and method with time division multiplexing	5,870,402
Self calibration circuitry and algorithm for multipass analog to digital converter interstage gain correction	5,926,123
Monolithic class AB shunt-shunt feedback CMOS low noise amplifier having self bias	5,963,094
Test circuit and method of trimming a unary digital-to-analog converter (DAC) in a subranging analog-to-digital converter (ADC)	5,973,631
Monolithic circuit and method for adding a randomized dither signal to the fine quantizer element of a subranging analog-to digital converter (ADC)	5,990,815
Rapid time and frequency acquisition of spread spectrum waveforms via ambiguity transform	5,995,535
Differential pair gain control stage	6,040,731
Self-calibrating, self-correcting transceivers and methods	6,118,811
High speed pin driver integrated circuit architecture for commercial automatic test equipment applications	6,157,224
Low noise, low distortion RF amplifier topology	6,400,229

Title/Description	Patent #
Low noise, low distortion, complementary IF amplifier	6,535,062
Unit cell with fan-out for large focal plane arrays with small detector pitch	6,552,343
High resolution ADC based on an oversampled subranging ADC	6,580,383
RF transciever with low power chirp acquisition mode	6,683,904
Wideband fast-hopping receiver front-end and mixing method	6,693,980
Monolithic I-Load architecture for automatic test equipment	6,717,450
High-performance track and hold circuit	6,825,697
Monolithic payload IF switch	6,891,424
Low noise, low distortion, muxable Gilbert mixer signal processing system and method with AGC functionality	6,931,083
On-chip multilayer metal shielded transmission line	6,975,189
High speed, high resolution amplifier topology	7,071,781
Sample and hold circuit and bootstrapping circuits therefor	7,088,148
Digitally trimmed DAC cell	7,095,347
High speed switch	7,098,684
Low power output driver	7,098,700
DNL/INL trim techniques for comparator based analog to digital converters	7,154,421
Low distortion amplifier	7,253,689

Title/Description	Patent #
HIGH/SPEED DIGITAL DATA COMMUNICATION SYSTEM	4,941,153
HIGH SPEED PROGRAMMABLE DIVIDER	4,975,931
EFFICIENT DIGITAL FREQUENCY DIVISION MULTIPLEXED SIGNAL RECEIVER	5,058,107
MULTIPLE USER DIGITAL RECEIVER APPARATUS AND METHOD WITH TIME DIVISION MULTIPLEXING	5,621,730
DIFFERENTIAL LOGIC LEVEL TRANSLATOR CIRCUIT WITH DUAL OUTPUT LOGIC LEVELS SELECTABLE BY POWER CONNECTOR OPTIONS	5,428,305
VEHICLE POSITION TRACKING TECHNIQUE	5,592,181
OVERDRIVE PROTECTION CLAMP SCHEME FOR FEEDBACK AMPLIFIERS	5,856,760
LOW VOLTAGE ANALOG FRONT END	5,859,558
TEMPERATURE COMPENSATED AMPLIFIER	5,859,568
CURRENT FEEDBACK DIFFERENTIAL AMPLIFIER CLAMP	5,859,569
WIDEBAND IF IMAGE REJECTING RECEIVER	6,636,730
SPLIT CELL BOWTIE DIGITAL TO ANALOG CONVERTER AND METHOD	6,879,276

Title/Description	Patent #
RESISTIVE LADDER, SUMMING NODE CIRCUIT, AND TRIMMING METHOD FOR A SUBRANGING ANALOG TO DIGITAL CONVERTER	6,882,294

U.S. Application Publication Nos.

Title/Description	APN
Trickle current-cascode DAC	20040257125
Clamped comparator	20050035788
Subtraction circuit with a dummy digital to analog converter	20050038846
Resolution enhanced folding amplifier	20050083223
Digital pre-distortion technique using nonlinear filters	20060078065
System and method for crest factor reduction	20060120479
Digital pre-distortion technique using nonlinear filters	20080095265
System and method for dynamic drain voltage adjustment to control linearity, output power, and efficiency in RF power amplifiers	20080211583
Dynamic crest factor reduction system	20080247487
Dynamic digital pre-distortion system	20080260066