

PATENT ASSIGNMENT

Electronic Version v1.1
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| SUBMISSION TYPE: | NEW ASSIGNMENT |
| NATURE OF CONVEYANCE: | ASSIGNMENT |
| CONVEYING PARTY DATA | |
| Name | Execution Date |
| Integrated Device Technology, Inc. | 07/17/2009 |
| RECEIVING PARTY DATA | |
| Name: | Netlogic Microsystems, Inc. |
| Street Address: | 1875 Charleston Rd. |
| City: | Mountain View |
| State/Country: | CALIFORNIA |
| Postal Code: | 94043 |
| PROPERTY NUMBERS Total: 1 | |
| Property Type | Number |
| Application Number: | 12405154 |
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| NAME OF SUBMITTER: | Peter C. Mei |
| Total Attachments: 16 source=072309_Assign-Non Publ'd App -28#page1.tif source=072309_Assign-Non Publ'd App -28#page2.tif source=072309_Assign-Non Publ'd App -28#page3.tif source=072309_Assign-Non Publ'd App -28#page4.tif source=072309_Assign-Non Publ'd App -28#page5.tif source=072309_Assign-Non Publ'd App -28#page6.tif | |

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PATENT ASSIGNMENT

This Patent Assignment (the "Assignment") is made and entered into as of July 17, 2009 (the "Effective Date"), by and between **INTEGRATED DEVICE TECHNOLOGY, INC.**, a Delaware corporation ("Assignor") and **NETLOGIC MICROSYSTEMS, INC.**, a Delaware corporation ("Assignee"). Defined terms not specifically defined herein shall have the meanings ascribed to them in the Asset Purchase Agreement, dated April 30, 2009 (the "Purchase Agreement"), by and between the parties.

WHEREAS, pursuant to the Purchase Agreement, Assignor has agreed to assign to Assignee certain patents, patent applications and invention disclosures as listed on Exhibit A to this Assignment (the "Patents");

WHEREAS, to effect the transfer of the Patents as contemplated in the Purchase Agreement, Assignor and Assignee desire to enter into this Assignment;

NOW, THEREFORE, in consideration of the mutual promises of the parties, and for good and valuable consideration, the receipt, adequacy and legal sufficiency of which are hereby acknowledged, the parties hereby agree as follows:

1. Assignment. Assignor does hereby sell, assign, transfer, convey and deliver to Assignee, all of the right, title, and interest of Assignor in and to the Patents, not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for the subject matter of the Patents, and to any continuation, continuation-in-part, division, renewal, substitute, re-examination or reissue thereof or any legal equivalent in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under any international conventions and treaties, together with all claims for damages and other remedies by reason of past infringements of the Patents, whether arising prior to or subsequent to the date of this Assignment, along with the right to sue for and collect such damages and other remedies for the use and benefit of Assignee and its successors, assigns and other legal representatives. The Patents are conveyed subject to any and all licenses, permissions, consents or other rights that may have been granted by Assignor or its predecessors-in-interest with respect thereto prior to the Effective Date.

2. Authorization. Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States, and, in the case of any patent applications filed with any office of any country or countries foreign to the United States, any officer of such country whose duty it is to issue patents or other evidence or forms of intellectual property protection or applications as aforesaid, to issue the same to Assignee and its successors, assigns and other legal representatives in accordance with the terms of this Assignment.

3. Conflicts. Notwithstanding any other provisions of this Assignment to the contrary, Assignor acknowledges and agrees that the representations, warranties, covenants, agreements, conditions, indemnities, rights and remedies contained in the Purchase Agreement shall not be superseded, modified, replaced, amended, changed, rescinded, or in any way affected hereby, but shall remain in full force and effect to the full extent provided in the

Purchase Agreement. This Assignment is subject to and controlled by the terms of the Purchase Agreement, and in the event of any conflict or inconsistency between the terms of the Purchase Agreement and the terms hereof, the terms of the Purchase Agreement shall govern.

4. Further Actions. Each of the parties hereto covenants and agrees, at its own expense, to execute and deliver, at the request of the other party hereto, such further instruments of transfer and assignment and to take such other action as such other party may reasonably request to more effectively consummate the assignments and assumptions contemplated by this Assignment.

5. Governing Law. This Assignment and any disputes hereunder shall be governed by and construed in accordance with the domestic laws of the State of California, without giving effect to any choice of law or conflict of law provision or rule (whether of the State of California or any other jurisdiction) that would cause the application of laws of any jurisdiction other than those of the State of California.

6. Notices. All notices and other communications hereunder shall be in writing and shall be deemed to have been duly given when delivered in person, by telecopy with answer back, by express or overnight mail delivered by a nationally recognized air courier (delivery charges prepaid), by registered or certified mail (postage prepaid, return receipt requested) or by e-mail with receipt confirmed by return e-mail to the respective parties as set forth below, or such other address as may be designated in writing hereafter, in the same manner, by such party:

If to Assignor:

Integrated Device Technology, Inc.
6024 Silver Creek Valley Road
San Jose, CA 95138
Attention: General Counsel

with a copy (which shall not constitute notice) to:

Latham & Watkins
140 Scott Drive
Menlo Park CA 94025
Attn: Mark V. Roeder, Esq.
Facsimile No.: (650) 463-2600

If to Assignee:

NetLogic Microsystems, Inc.
1875 Charleston Rd.
Mountain View, CA 94043
Attention: General Counsel

with a copy (which shall not constitute notice) to:

Bingham McCutchen, LLP
1900 University Avenue, 4th Fl.
East Palo Alto, CA 94303
Facsimile No.: (650) 849-4609

Any notice or communication delivered in person shall be deemed effective on delivery. Any notice or communication sent by e-mail, telecopy or by air courier shall be deemed effective on the first business day following the day on which such notice or communication was sent. Any notice or communication sent by registered or certified mail shall be deemed effective on the third business day following the day on which such notice or communication was mailed.

7. Relationship Between Parties. Assignee and Assignor shall at all times and for all purposes be deemed to be independent contractors and neither party, nor either party's employees, representatives, subcontractors or agents, shall have the right or power to bind the other party. This Assignment shall not itself create or be deemed to create a joint venture, partnership or similar association between Assignee and Assignor or either party's employees, representatives, subcontractors or agents.

8. Third Party Beneficiaries. The terms and provisions of this Assignment are intended solely for the benefit of Assignee and its affiliates (as defined in the Purchase Agreement), on the one hand, and Assignor and its affiliates, on the other hand. It is not the intention of the parties to confer third-party beneficiary rights upon any other person or entity, and this Assignment does not (shall not be construed to) confer any right or cause of action in, upon or on behalf of any other person or entity, and no person or entity (including any of employee or former employee of any of the parties) other than Assignee or its affiliates and Assignor or its affiliates shall be entitled to rely on any provision of this Assignment in any action proceeding, hearing or other forum.

9. Severability. In the event that any clause, sub-clause or other provision contained in this Assignment shall be determined by any competent authority to be invalid, unlawful or unenforceable to any extent, such clause, sub-clause or other provision shall to that extent be severed from the remaining clauses and provisions, or the remaining part of the clause in question, which shall continue to be valid and enforceable to the fullest extent permitted by law.

10. No Waiver; Remedies Cumulative. Failure or neglect by a party to enforce at any time any of the provisions hereof shall not be construed nor shall be deemed to be a waiver of such party's rights hereunder nor in any way affect the validity of the whole or any part of this Assignment nor prejudice such party's rights to take subsequent action. All rights and remedies conferred under this Assignment or by any other instrument or law shall be cumulative and may be exercised singularly or concurrently.

11. Amendment. Any term of this Assignment may be amended, modified, rescinded, canceled or waived, in whole or in part, only by a written instrument signed by each of the parties' authorized representatives or their respective permitted successors and assigns.

Any amendment or waiver effected in accordance with this Section shall be binding upon the parties and their respective successors and assigns.

12. Counterparts. This Assignment may be executed in two or more counterparts, all of which, taken together, shall be considered to be one and the same instrument.


13. Headings; Construction. The headings to the clauses, sub-clauses and parts of this Assignment are inserted for convenience of reference only and are not intended to be part of or to affect the meaning or interpretation of this Assignment. The terms "this Assignment," "hereof," "hereunder" and any similar expressions refer to this Assignment and not to any particular Section or other portion hereof. The parties hereto agree that any rule of construction to the effect that ambiguities are to be resolved against the drafting party will not be applied in the construction or interpretation of this Assignment. As used in this Assignment, the words "include" and "including," and variations thereof, will be deemed to be followed by the words "without limitation" and "discretion" means sole discretion.

14. Entire Assignment. With the exception of the Purchase Agreement and the Ancillary Agreements, this Assignment supersedes any arrangements, understandings, promises or agreements made or existing between the parties hereto prior to or simultaneously with this Assignment and, together with the Purchase Agreement and the Ancillary Agreements, constitutes the entire understanding between the parties hereto.

[SIGNATURE PAGE FOLLOWS]

IN WITNESS WHEREOF, Assignor has caused this Assignment to be executed as of the Effective Date.

INTEGRATED DEVICE TECHNOLOGY, INC.

By: 
Name: Ted Tewksbury
Title: President and Chief Executive Officer

Signature Page to Patent Assignment

PATENT
REEL: 022997 FRAME: 0441

Exhibit A
Assigned Patents, Applications, and Disclosures

- Issued Patents

| Title | Juris. | Patent No. | Issue Date | Application # | Filing Date |
|--|--------|------------|------------|---------------|-------------|
| Six Transistor Content Addressable Memory Cell | US | US6101116 | 8/8/2000 | 09/345,224 | 6/30/1999 |
| Content Addressable Memory (CAM) Arrays And Cells Having Low Power Requirements | US | USRE39227 | 8/8/2006 | 10/403,581 | 3/31/2003 |
| Low-Power Content Addressable Memory Cell | US | US6128207 | 10/3/2000 | 09/185,057 | 11/2/1998 |
| Increasing Priority Encoder Speed Using The Most Significant Bit Of A Priority Address | US | US6505271 | 1/7/2003 | 09/439,968 | 11/12/1999 |
| Pipelining A Content Addressable Memory Cell Array For Low-Power Operation | US | US6470418 | 10/22/2002 | 09/232,413 | 1/15/1999 |
| Content Addressable Memory With Longest Match Detect | US | US6370613 | 4/9/2002 | 09/361,680 | 7/27/1999 |
| Priority Encoder For A Content Addressable Memory System | US | US5964857 | 10/12/1999 | 08/865,819 | 5/30/1997 |
| Network Translation Circuit and Method Using A Segmentable Content Addressable Memory | US | US6732227 | 5/4/2004 | 09/655,019 | 9/5/2000 |
| Content Addressable Memory Multiple Match Detection Circuit | US | US5852569 | 12/22/1998 | 08/858,997 | 5/20/1997 |
| Content Addressable Memory And Random Access Memory Partition Circuit | US | US5706224 | 1/6/1998 | 08/729,626 | 10/10/1996 |
| CAM Array With Minimum Cell Size | US | US6256216 | 7/3/2001 | 09/574744 | 5/18/2000 |
| Ternary CAM Array | US | US6262907 | 7/17/2001 | 09/574,747 | 5/18/2000 |
| Quad CAM Cell With Minimum Cell Size | US | US6373739 | 4/16/2002 | 09/731,160 | 12/6/2000 |
| CAM Array With Minimum Cell Size | US | US6266263 | 7/24/2001 | 09/678,502 | 10/2/2000 |
| Low Power Priority Encoder | US | US6307767 | 10/23/2001 | 09/829,679 | 4/9/2001 |
| Sense Amplifier For Content Addressable Memory | US | US6442054 | 8/27/2002 | 09/866,056 | 5/24/2001 |
| Ternary CAM Cell With Dram Mask Circuit | US | US6400593 | 6/4/2002 | 09/780,714 | 2/8/2001 |
| Dram-Based CAM Cell Using 3T Or 4T Dram Cells | US | US6421265 | 7/16/2002 | 09/816,742 | 3/22/2001 |
| Content Addressable Memory Array Having Flexible Priority Support | US | US6996662 | 2/7/2006 | 09/884,797 | 6/18/2001 |
| Content Addressable Memory (CAM) Devices That Can Identify Highest Priority Matches In Non-Sectored Cam Arrays And Methods Of Operating Same | US | US6665202 | 12/16/2003 | 09/962,737 | 9/25/2001 |
| Compact Ternary Content Addressable Memory Cell | US | US6496399 | 12/17/2002 | 09/941,372 | 8/28/2001 |
| Content Addressable Memory (CAM) Devices Having Dedicated Mask Cell Sub-Arrays Therein And Methods Of Operating Same | US | US6839256 | 1/4/2005 | 10/386,400 | 3/11/2003 |

| Title | Juris. | Patent No. | Issue Date | Application # | Filing Date |
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| Content Addressable Memory (CAM) Devices That Support Power Saving Longest Prefix Match Operations And Methods Of Operating Same | US | US7050317 | 5/23/2006 | 10/927,453 | 8/26/2004 |
| Content Addressable Memory (CAM) Devices Having Priority Class Detectors Therein That Perform Local Encoding Of Match Line Signals | US | US7095641 | 8/22/2006 | 11/393,336 | 3/30/2006 |
| Content Addressable Memory (CAM) Devices Having Bidirectional Interface Circuit Therein That Support Passing Word Line And Match Signals On Global Word Lines | US | US7301850 | 11/27/2007 | 11/393,493 | 3/30/2006 |
| Content Addressable Memory (CAM) Devices That Utilize Priority Class Detectors To Identify Highest Priority Matches In Multiple CAM Arrays And Methods Of Operating Same | US | US7092311 | 8/15/2006 | 10/386,399 | 3/11/2003 |
| Content Addressable Memory (CAM) Devices Having CAM Array Blocks Therein That Conserve Bit Line Power During Staged Compare Operations | US | US6804134 | 10/12/2004 | 10/410,569 | 4/9/2003 |
| Content Addressable And Random Access Memory Devices Having High Speed Sense Amplifiers Therein With Low Power Consumption Requirements | US | US6879532 | 4/12/2005 | 10/934,209 | 9/3/2004 |
| Multi-Bank Content Addressable Memory (CAM) Devices Having Segment-Based Priority Resolution Circuits Therein And Methods Of Operating Same | US | US6937491 | 8/30/2005 | 10/263,223 | 10/2/2002 |
| Multi-Bank Content Addressable Memory (CAM) Devices Having Staged Segment-To-Segment Soft And Hard Priority Resolution Circuits Therein And Methods Of Operating Same | US | US7069378 | 6/27/2006 | 10/263,258 | 10/2/2002 |
| Content Addressable Memories Having Entries Stored Therein With Independently Searchable Weight Fields And Methods Of Operating Same | US | US6745280 | 6/1/2004 | 10/109,328 | 3/28/2002 |
| Content Addressable Memory With Programmable Priority Weighting And Low Cost Match Detection | US | US6577520 | 6/10/2003 | 10/274,659 | 10/21/2002 |
| Priority Encoder For A Content Addressable Memory System | Taiwan | 440872 | 6/16/2001 | 087108450 | 5/29/1998 |
| Content Addressable Memory (CAM) Devices That Utilize Dual-Capture Match Line Signal Repeaters To Achieve Desired Speed/Power Tradeoff And Methods Of Operating Same | US | US6965519 | 11/15/2005 | 10/464,598 | 6/18/2003 |
| Dram-Based CAM Cell With Shared Bitlines | US | US7016211 | 3/21/2006 | 10/921760 | 8/18/2004 |
| Low Power Content Addressable Memory Array (CAM) and Method of Operating Same | US | US7486531 | 2/3/2009 | 11/203,058 | 8/12/2005 |

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| Content Addressable Memory (CAM) Devices Having Reliable Column Redundancy Characteristics And Methods Of Operating Same | US | US6657878 | 12/2/2003 | 10/084,842 | 2/27/2002 |
| CAM Circuit With Radiation Resistance | US | US6560156 | 5/6/2003 | 10/099,913 | 3/14/2002 |
| CAM Circuit With Radiation Resistance | US | US6924995 | 8/2/2005 | 10/845,654 | 5/13/2004 |
| CAM Circuit With Radiation Resistance | US | US6754093 | 6/22/2004 | 10/165,506 | 6/6/2002 |
| CAM Circuit With Separate Memory And Logic Operating Voltages | US | US6661687 | 12/9/2003 | 10/350,991 | 1/23/2003 |
| CAM Circuit With Separate Memory And Logic Operating Voltages | US | US6512685 | 1/28/2003 | 10/164,981 | 6/6/2002 |
| Hardware Hashing Of An Input Of A Content Addressable Memory (CAM) To Emulate A Wider CAM | US | US7136960 | 11/14/2006 | 10/173516 | 6/14/2002 |
| Use Of Hashed Content Addressable Memory (CAM) To Accelerate Content-Aware Searches | US | US7171439 | 1/30/2007 | 10/173,206 | 6/14/2002 |
| Fast Collision Detection For A Hashed Content Addressable Memory (CAM) Using A Random Access Memory | US | US7290084 | 10/30/2007 | 10/980,858 | 11/2/2004 |
| CAM Circuit With Error Correction | US | US6700827 | 3/2/2004 | 10/226,512 | 8/23/2002 |
| Content Addressable Memory (CAM) Devices Having Error Detection And Correction Control Circuits Therein And Methods Of Operating Same | US | US6879504 | 4/12/2005 | 10/619,638 | 7/15/2003 |
| Content Addressable Memory (CAM) Devices That Support Distributed CAM Control And Methods Of Operating Same | US | US7058757 | 6/6/2006 | 10/620161 | 7/15/2003 |
| Content Addressable Memory (CAM) Devices That Utilize Multi-Port CAM Cells And Control Logic To Support Multiple Overlapping Search Cycles That Are Asynchronously Timed Relative To Each Other | US | US6781857 | 8/24/2004 | 10/306,799 | 11/27/2002 |
| Multiple Match Detection Logic And Gates For Content Addressable Memory (CAM) Devices | US | US6859378 | 2/22/2005 | 10/869,387 | 6/16/2004 |
| Content Addressable Memory (CAM) Devices Having Scalable Multiple Match Detection Circuits Therein | US | US6924994 | 8/2/2005 | 10/385,155 | 3/10/2003 |
| Content Addressable Memory (CAM) Devices Having CAM Array Blocks Therein That Perform Pipelined And Interleaved Search, Write And Read Operations And Methods Of Operating Same | US | US6829153 | 12/7/2004 | 10/622,396 | 7/18/2003 |
| Content Addressable Memory (CAM) Devices Having Adjustable Match Line Precharge Circuits Therein | US | US6775168 | 8/10/2004 | 10/622,408 | 7/18/2003 |
| Content Addressable Memory (CAM) Devices Having Speed Adjustable Match Line Signal Repeaters Therein | US | US6760242 | 7/6/2004 | 10/323,236 | 12/18/2002 |

| Title | Country | Patent No. | Issue Date | Application# | Filing Date |
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| Content Addressable Memory (CAM) Devices That Utilize Segmented Match Lines And Word Lines To Support Pipelined Search And Write Operations And Methods Of Operating Same | US | US6967856 | 11/22/2005 | 10/701,048 | 11/4/2003 |
| Content Addressable Memory Devices With Virtual Partitioning And Methods Of Operating Same | US | US6867991 | 3/15/2005 | 10/613,245 | 7/3/2003 |
| Content Addressable Memory (CAM) Arrays Having Memory Cells Therein With Different Susceptibilities To Soft Errors | US | US7193876 | 3/20/2007 | 11/181534 | 7/14/2005 |
| Content Addressable Memory (CAM) Devices Having Multi-Block Error Detection Logic And Entry Selective Error Correction Logic Therein | US | US6987684 | 1/17/2006 | 10/738,264 | 12/17/2003 |
| Content Addressable Memory (CAM) Devices With Dual-Function Check Bit Cells That Support Column Redundancy And Check Bit Cells With Reduced Susceptibility To Soft Errors | US | US6870749 | 3/22/2005 | 10/619,635 | 7/15/2003 |
| Ternary Content Addressable Memory (Tcam) Cells With Small Footprint Size And Efficient Layout Aspect Ratio | Taiwan | I266319 (200514083) | 11/11/2006 | 093118317 | 6/24/2004 |
| Ternary Content Addressable Memory (Tcam) Cells With Small Footprint Size And Efficient Layout Aspect Ratio | US | US6900999 | 5/31/2005 | 10/609,756 | 6/30/2003 |
| Content Addressable Memory (CAM) Devices With Block Select And Pipelined Virtual Sector Look-Up Control And Methods Of Operating Same | US | US6972978 | 12/6/2005 | 10/663,860 | 9/16/2003 |
| CAM-Based Search Engines That Support Pipelined Multi-Database Search Operations Using Encoded Multi-Database Identifiers | US | US7260675 | 8/21/2007 | 11/532,746 | 9/18/2006 |
| CAM-Based Search Engines That Support Pipelined Multi-Database Search Operations Using Replacement Search Key Segments | US | US7120731 | 10/10/2006 | 10/688,353 | 10/17/2003 |
| Content Addressable Memories (CAM) Having Low Power Dynamic Match Line Sensing Circuits Therein | US | US7471537 | 12/30/2008 | 11/751,900 | 5/22/2007 |
| High Speed Nand-Type Content Addressable Memory (CAM) | US | US7110275 | 9/19/2006 | 11/137,163 | 5/25/2005 |
| CAM Based Search Engines And Packet Co-Processors Having Results Status Signaling For Completed Contexts | US | US7082493 | 7/25/2006 | 10/698,246 | 10/31/2003 |
| CAM-Based Search Engine Devices Having Advanced Search And Learn Instruction Handling | US | US7194573 | 3/20/2007 | 10/721,036 | 11/21/2003 |
| CAM-Based Search Engines Having Per Entry Age Reporting Capability | US | US7120733 | 10/10/2006 | 10/714,680 | 11/14/2003 |
| Binary And Ternary Non-Volatile CAM | US | US7499303 | 3/3/2009 | 10/950,186 | 9/24/2004 |

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| CAM-Based Search Engine Devices Having Index Translation Capability | US | US7185172 | 2/27/2007 | 10/743,597 | 12/22/2003 |
| Method And Apparatus For CAM With Reduced Cross-Coupling Interference | US | US7522438 | 4/21/2009 | 11/931,573 | 10/31/2007 |
| Method And Apparatus For CAM With Reduced Cross-Coupling Interference | US | US7248492 | 7/24/2007 | 11/393,985 | 3/30/2006 |
| Method And Apparatus For CAM With Reduced Cross-Coupling Interference | US | US7187571 | 3/6/2007 | 10/821,601 | 4/9/2004 |
| Content Addressable Memory (CAM) Devices That Support Background Bist And Bist Operations And Methods Of Operating Same | US | US7304875 | 12/4/2007 | 11/184,414 | 7/19/2005 |
| Content Addressable Memory (CAM) Devices Having Nand-Type Compare Circuits | US | US7355890 | 4/8/2008 | 11/553,202 | 10/26/2006 |
| Complementary Data Line Driver Circuits With Conditional Charge Recycling Capability That May Be Used In Random Access And Content Addressable Memory Devices And Method Of Operating Same | US | US6549042 | 4/15/2003 | 10/004,456 | 10/19/2001 |
| Switching circuit implementing variable string matching | US | US7353332 | 4/1/2008 | 11/248,901 | 10/11/2005 |
| Method and Apparatus for Source Synchronous Testing | US | US7548105 | 6/16/2009 | 11/395,079 | 3/31/2006 |

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| Content Addressable Memory (CAM) Array Capable of Implementing Read or Write Operations During Search Operations | US | 12/405,154 | 3/16/2009 | Filed |
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