PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE:		NEW ASSIGNMENT		
NATURE OF CONVEYANCE:		ASSIGNMENT		
CONVEYING PARTY DATA				
	1	lame	Execution Date	
Industrial Technology Resea	rch Institute		07/01/2009	
RECEIVING PARTY DATA				
Name: Trans	Pacific IP I Lte	d.		
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City: Taipe	Taipei City			
State/Country: TAIW	TAIWAN			
Postal Code: 105	105			
Property Type Patent Number: 6		Number 989298		
PROPERTY NUMBERS Tota	ıl: 2			
Patent Number: 7180		990		
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NAME OF SUBMITTER:		Justin King		
Total Attachments: 2 source=SKMBT_5000908031 source=SKMBT_5000908031				

ASSIGNMENT OF PATENT

Atty Dkt No.:

WHEREAS, Industrial Technology Research Institute, of Hsinchu, Taiwan, whose address is No. 195, Sec. 4, Chung Hsing Rd., Chutung, Hsinchu, Taiwan, 310, R.O.C., is the owner of the entire right, title and interest of the following U.S. Patent(s):

Patent No. Title

SEE ATTACHED APPENDIX A (1 page indicating 2 separate U.S. Patents)

WHEREAS, **TransPacific IP I Ltd.**, of Taipei, Taiwan, whose address is Room 1402, 14F, No. 205, Dunhua N. Road, Songshan District, Taipei City 105, Taiwan, R.O.C., hereinafter referred to as "assignee", is desirous of acquiring the entire right, title and interest in the same;

NOW, THEREFORE, for good and valuable consideration, the receipt whereof is acknowledged, we, the patent owners, by these presents do sell, assign and transfer unto said assignee the entire right, title and interest in and to the aforementioned Patents; the same to be held and enjoyed by the said assignee for his own use and behoof, and for his legal representatives and assigns, to the full end of the term for which said Patents is granted, as fully and entirely as the same would have been held by me had this assignment and sale not been made.

Executed this 1st day of July, 2009, at Hsinchu.

gnature)

<u>General Director</u> (Title)

ASSIGNMENT OF PATENT APPENDIX A		Atty Dkt No.: BHT/	
PATENT NOS.	TITLES		
6,989,298	Method of forming thin-film transistor devices with electro-static discharge protection		
7,180,090	Method of forming thin-film transistor devices with electro-static discharge protection		

57-14