

**PATENT ASSIGNMENT**

Electronic Version v1.1  
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
Name	Execution Date
Micron Technology, Inc.	09/26/2008
<b>RECEIVING PARTY DATA</b>	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman Limited)
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	KY1-1205
<b>PROPERTY NUMBERS Total: 1</b>	
Property Type	Number
Application Number:	09483362
<b>CORRESPONDENCE DATA</b>	
Fax Number:	(202)420-2201
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	202-420-2200
Email:	delopezc@dicksteinshapiro.com
Correspondent Name:	Gianni Minutoli
Address Line 1:	Dickstein Shapiro LLP
Address Line 2:	1825 Eye Street, NW
Address Line 4:	WASHINGTON, DISTRICT OF COLUMBIA 20006
ATTORNEY DOCKET NUMBER:	M4065.0793/P793
NAME OF SUBMITTER:	Gianni Minutoli
Total Attachments: 7 source=assignmentp793#page1.tif source=assignmentp793#page2.tif	

OP \$40.00 09483362

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**PATENT  
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**PATENT ASSIGNMENT AGREEMENT**

**1. Definitions**

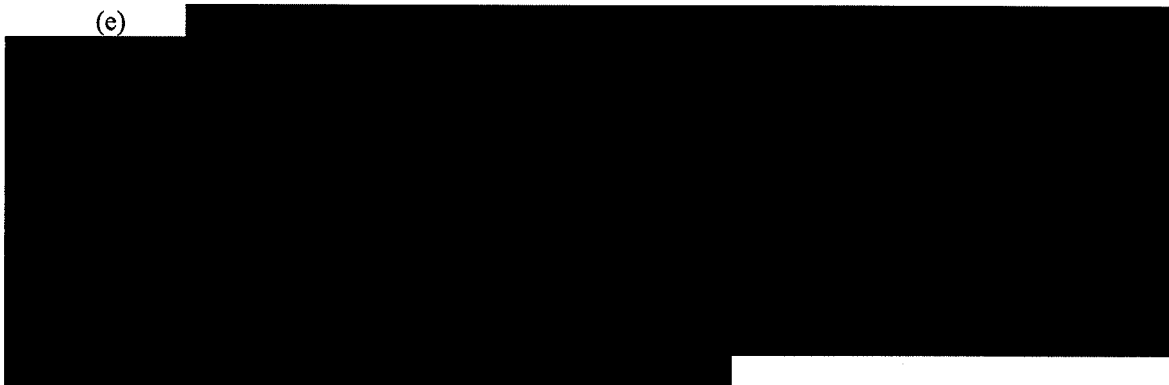
(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k)

2.

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c)

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3 [REDACTED]

[REDACTED]

4. [REDACTED]

[REDACTED]

5. [REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

**Micron Technology, Inc.**

Signed: *[Signature]*

Date: 9/20/08, 2008

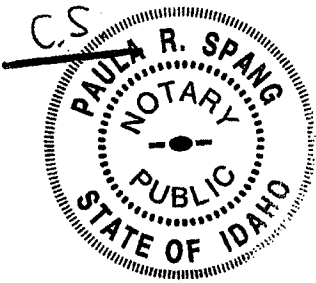
**Notarization**  
State of Idaho  
County of Ada

On 9/26/08, before me, *Paula R. Spang*, personally appeared the above-named *Steven R. Appleton*

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: *[Signature]*  
My commission expires: 8/12/11

REVIEWED  
MTI LEGAL



Agreed to by:

**Aptina Imaging Corporation**

Signed: *Thomas L. Lewis, Jr.*

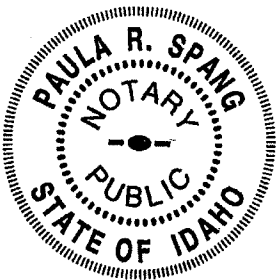
Date: 9/26/08, 2008

**Notarization**  
State of Idaho  
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On 9/26/08, before me, *Paula R. Spang*, personally appeared the above-named *Thomas L. Lewis, Jr.*

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: *[Signature]*  
My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"

IMAGING PATENTS



## Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
		OTHER CIRCUITS		
6384460	AVGO-0188.00/US	A SELF-ALIGNED METAL ELECTRODE STRUCTURE FOR ELEVATED SENSORS	United States of America	May 7, 2002
6388241	PBIT-0016.00/US	ACTIVE PIXEL COLOR LINEAR SENSOR WITH LINE-PACKED PIXEL READOUT	United States of America	May 14, 2002
6388242	PBIT-0054.00/US	MICRO POWER MICRO-SIZED CMOS ACTIVE PIXEL	United States of America	May 14, 2002
6388593	PBIT-0021.01/US	DIFFERENTIAL NON-LINEARITY CORRECTION SCHEME	United States of America	May 14, 2002
6392423	AVGO-0170.01/US	METHOD FOR TESTING INTEGRATED CIRCUIT DEVICES	United States of America	May 21, 2002
6396118	AVGO-0174.00/US	CONDUCTIVE MESH BIAS CONNECTION FOR AN ARRAY OF ELEVATED ACTIVE PIXEL SENSORS	United States of America	May 28, 2002
6407440	1998-0272.00/US	PIXEL CELL WITH HIGH STORAGE CAPACITANCE FOR A CMOS IMAGER	United States of America	Jun 18, 2002
6414342	1997-1465.00/US	PHOTOGATE WITH IMPROVED SHORT WAVELENGTH RESPONSE FOR A CMOS IMAGER	United States of America	Jul 2, 2002
6417074	AVGO-0064.01/US	METHOD OF MANUFACTURING A STRUCTURE FOR REDUCING LEAKAGE CURRENTS BY PROVIDING ISOLATION BETWEEN ADJACENT REGIONS OF AN INTEGRATED CIRCUIT	United States of America	Jul 9, 2002
6424375	AVGO-0086.00/US	Low Noise Active Reset Readout For Image Sensors	United States of America	Jul 23, 2002
6429036	PBIT-0067.00/US	Backside Illumination of CMOS Image Sensors	United States of America	Aug 6, 2002
6429470	1998-0019.01/US	CMOS IMAGER WITH STORAGE CAPACITOR	United States of America	Aug 6, 2002
6435682	AVGO-0028.00/US	SOCKETABLE FLEXIBLE CIRCUIT BASED ELECTRONIC DEVICE MODULE AND A SOCKET FOR THE SAME	United States of America	Aug 20, 2002
6437379	AVGO-0064.02/US	INTEGRATED CIRCUIT DEVICE PROVIDING ISOLATION BETWEEN ADJACENT REGIONS	United States of America	Aug 20, 2002