

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	EXECUTIVE ORDER 9424, CONFIRMATORY LICENSE
CONVEYING PARTY DATA	
Name	Execution Date
Iowa State University of Science and Tech	06/02/2008
RECEIVING PARTY DATA	
Name:	National Science Foundation
Street Address:	4201 Wilson Blvd
Internal Address:	Room 1265
City:	Arlington
State/Country:	VIRGINIA
Postal Code:	22230
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12107415
CORRESPONDENCE DATA	
Fax Number:	(703)292-9041
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Email:	nsfpatents@nsf.gov
Correspondent Name:	National Science Foundation
Address Line 1:	4201 Wilson Blvd
Address Line 4:	Arlington, VIRGINIA 22230
NAME OF SUBMITTER:	Robin Clay Fritsch
Total Attachments: 1 source=20090811150142976#page1.tif	

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Sign and Fax this to (301) 480-0272

Invention Title: Utilizing Timing Error Detection and Recovery to Dynamically Improve Superscalar Processor Performance

Inventor(s): Mikel Bezdek, Arun K. Somani

U.S. Filing/Issue Date: 4-22-2008

Patent or Application Serial No.: 12/107,415

Grant/Contract Number(s): CCF0311061

Foreign Applications filed/intended in (countries): _____

The invention identified above is a Subject Invention under 35 U.S.C. 200, et seq., and the Standard Patent Rights clause at 37 CFR 401.14, FAR 52.227-11 or FAR 52.227-12 (if applicable) which are included among the terms of the above identified grant or contract award from the United State Government. This document is confirmatory of:

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Signed this 2nd day of June, 2008.

By Kenneth Kirkland
(Name of Grantee/Contractor Official)

K. Kirkland
(Signature)

Title Executive Director

For IOWA STATE UNIVERSITY OF SCIENCE & TECH
(Grantee/Contractor Organization)

At Iowa State University Research Foundation, Inc. 310 Lab of Mechanics Ames, IA 50011 US
(Business Address)

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