

PATENT ASSIGNMENT

Electronic Version v1.1
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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Infinera Acquisition Corporation	11/30/2005
RECEIVING PARTY DATA	
Name:	Vitesse Semiconductor Corporation
Street Address:	741 Calle Plano
City:	Camarillo
State/Country:	CALIFORNIA
Postal Code:	93012
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	11943569
CORRESPONDENCE DATA	
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ATTORNEY DOCKET NUMBER:	1161-111-CON-DC
NAME OF SUBMITTER:	Daniel M. Cavanagh
Total Attachments: 5 source=1161-111-CON-DC--AssgnInfineraToVitesse#page1.tif source=1161-111-CON-DC--AssgnInfineraToVitesse#page2.tif source=1161-111-CON-DC--AssgnInfineraToVitesse#page3.tif	

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ASSIGNMENT OF WORLDWIDE PATENTS AND PATENT APPLICATIONS

For good and valuable consideration, the receipt of which is hereby acknowledged, **Infinera Acquisition Corporation**, a corporation formed under the laws of the state of Delaware and a wholly owned subsidiary of Infinera Corporation, having a place of business at 1322 Bordeaux Drive, Sunnyvale, CA 94089-1005, assignor herein, hereby sells, assigns and transfers to **Vitesse Semiconductor Corporation**, a Delaware corporation with its principal place of business located at 741 Calle Plano, Camarillo, California 93012, assignee herein, its successors, assigns, and legal representatives, the full and exclusive right, title and interest it has in and to the patents and patent applications set forth in the attached Exhibit A (consisting of two pages setting forth one patent and six pending patent applications) and Exhibit B (consisting of one page setting forth three pending PCT patent applications) as well as all its right, title and interest in and to any and all inventions described in such patents and patent applications for the United States, its territorial possessions and all foreign countries; and in and to any and all continuations-in-part, continuations, divisions, substitutes, reissues, extensions thereof, and all other applications for all patents relating thereto which have been or shall be filed in the United States, its territorial possessions and/or any foreign countries, and all rights, together with all priority rights, under any of the international conventions, unions, agreements, acts, and treaties, including all future conventions, unions, agreements, acts, and treaties; and all rights to recover and retain damages for infringement, including past infringement, if any, of any patent or patents based upon or corresponding to the patents.

Infinera Acquisition Corporation hereby agrees to sign all necessary papers and do all lawful acts reasonably requisite in connection with the prosecution, assignment, enforcement and disclaimer of each and every patent application based upon the patents, without further compensation, but at the expense of the assignee or its successors and assigns; and agrees that, upon request it will, at any time without charging a fee to assignee but at assignee's expense for any reasonable costs incurred, furnish all necessary documentation relating to or supporting chain of title and to confirm assignee's ownership of all right, title, and interest in and to the patents, provide testimony at any time in connection with any proceedings affecting the right, title, interest or benefit of assignee in, to or under the patents and sign and deliver all papers, take all rightful oaths, and do all acts which may be reasonably necessary for vesting title to the patents in assignee, its successors, assigns and legal representatives or nominees; and in the event it fails to execute such documentation after a reasonable amount of time, hereby appoints assignee with full and complete authority and power of attorney to act in its stead and to execute and record as its attorney-in-fact such transfer documentation.

Infinera Acquisition Corporation certifies that it is the assignee of record of the entire right, title and interest in the patents and patent applications of Exhibit A and Exhibit B by virtue of written assignments from Big Bear Networks, Inc. ("BBN"); and that BBN had acquired the entire right, title and interest in all such patents and patent applications by virtue of written assignments from the named inventors set forth in such patents and patent applications, the USPTO recording date of said inventor assignments as well as designated reel and frame numbers also set forth in Exhibit A in connection with each such United States patent and patent application.

The undersigned (whose title is supplied below) is authorized to act on behalf of the original assignee.

[Signature Page Follows]

(301) 229-5314

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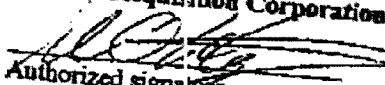
IN TESTIMONY WHEREOF, Infionera Acquisition Corporation has executed this Assignment
this 20th day of November, 2005.


Attesting signature

David F. Welch
Name (Print)

Chief Strategy Officer
Title

Infionera Acquisition Corporation


Authorized signature

Michael C. McCarthy
Name (Print)

Vice President
Title

EXHIBIT A Schedule of U.S. Patents and Patent Applications Assigned From Infinera Acquisition Corporation to Vitesse Semiconductor Corporation			
BBN Docket No.	Patents and Applications With Issued and Filing Dates	Title And Named Inventors	U.S. Patent Office Reel/Frame Recording of Assignment from Named Inventors and Date Recorded
BBNP019	6,545,567 B1 Issued 04/08/2003	PROGRAMMABLE ANALOG TAPPED DELAY LINE FILTER HAVING CASCADED DIFFERENTIAL DELAY CELLS Shanthi Pavan, Sudeep Bhoja and John S. Wang	Reel/Frame: 016769/0040 Recorded: 11/11/2005
BNNP020	09/955,278 Filed 09/11/2001 RCE Filed on 10/11/2005	METHOD AND APPARATUS FOR IMPROVED HIGH-SPEED ADAPTIVE EQUALIZATION John S. Wang, Sudeep Bhoja and Shanthi Pavan	Confirmation No.: 8686 Reel/Frame: 012181/0994 Recorded: 09/12/2001
BNNP029	10/677,123 Filed 09/30/2003 A CIP of 09/955,278 09/11/2001 (P020)	METHOD AND APPARATUS FOR IMPROVED HIGH-SPEED ADAPTIVE EQUALIZATION John S. Wang, Sudeep Bhoja, Shanthi Pavan and Hai Tao	Confirmation No.: 3909 Reel/Frame: 015135/0383 Recorded: 03/25/2004
BBNP031	10/810,271 Filed 03/26/2004	METHOD AND APPARATUS FOR IMPROVED HIGH-SPEED FEC ADAPTIVE EQUALIZATION John S. Wang, Jeff Rahan and Sudeep Bhoja	Confirmation No.: 8535 Reel/Frame: 015135/0189 Recorded: 03/26/2004

BBN Docket No.	Patents and Applications With Issued and Filing Dates	Title And Named Inventors	U.S. Patent Office Reel/Frame Recording of Assignment from Named Inventors and Date Recorded
BNNP032	10/872,108 06/17/2004	POWER AND AREA EFFICIENT ADAPTIVE EQUALIZATION Shanthi Pavan Madras, India	Confirmation No.: 2477 Reel/Frame: 015498/0203 Recorded: 06/17/2004
BNNP033	10/872,307 06/17/2004	IMPROVED INTEGRATED CIRCUIT IMPLEMENTATION FOR POWER AND AREA EFFICIENT ADAPTIVE EQUALIZATION Shanthi Pavan Madras, India	Confirmation No.: 2277 Reel/Frame: 015501/0397 Recorded: 06/17/2004
BNNP035	11/029,297 01/04/2005	ADAPTIVE EQUALIZATION WITH GROUP DELAY Sudeep Bhoja, John S. Wang and Hai Tao	Confirmation No.: 8969 Reel/Frame: 015498/0203 Recorded: 06/17/2004

EXHIBIT B Schedule of Foreign Patent Applications Assigned From Infinera Acquisition Corporation to Vitesse Semiconductor Corporation		
BBN Docket No.	PCT Patent Applications With International Filing dates	Title and Named Inventors
BBNP029.PCT	International Application No. PCT/US2004/032014, filed on 09/28/2004 Priority is 09/30/2003, based from Serial No. 10/677,123 Publications: WO 2005/034370 A2 04/14/2005 WO 2005/034370 A3	METHOD AND APPARATUS FOR IMPROVED HIGH-SPEED ADAPTIVE EQUALIZATION John S. Wang, Sudeep Bhoja, Shanthi Pavan and Hai Tao
BNNP032.PCT	International Application No. PCT/US2005/020710 Filed 06/09/2005 Based from Serial No. 10/872,108, filed 06/17/2004	POWER AND AREA EFFICIENT ADAPTIVE EQUALIZATION Shanthi Pavan
BNNP033.PCT	International Application No. PCT/US2005/020709 Filed 06/09/2004 Based from Serial No. 10/872,307, filed 06/17/2004	IMPROVED INTEGRATED CIRCUIT IMPLEMENTATION FOR POWER AND AREA EFFICIENT ADAPTIVE EQUALIZATION Shanthi Pavan