

PATENT ASSIGNMENT

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SUBMISSION TYPE:

NEW ASSIGNMENT

NATURE OF CONVEYANCE:

ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Infineon Technologies North America Corp.	09/01/2006

RECEIVING PARTY DATA

Name:	Infineon Technolgoes AG
Street Address:	St.-Marten-Strasse 53,
City:	Munchen
State/Country:	GERMANY
Postal Code:	81669

PROPERTY NUMBERS Total: 10

Property Type	Number
Patent Number:	5210699
Patent Number:	5311407
Patent Number:	5344794
Patent Number:	5357674
Patent Number:	5446295
Patent Number:	5506152
Patent Number:	5506425
Patent Number:	5723363
Patent Number:	5855924
Patent Number:	6100858

CORRESPONDENCE DATA

Fax Number: (613)591-8148

*Correspondence will be sent via US Mail when the fax attempt is unsuccessful.*

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Email: ipadmin@mosaid.com

CH \$400.00 5210699

500993643

PATENT  
REEL: 023401 FRAME: 0082

Correspondent Name:	Mosaid Technologies Incorporated
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ATTORNEY DOCKET NUMBER:	INFINEON ASSIGNMENTS
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NAME OF SUBMITTER:	Victoria Donnelly
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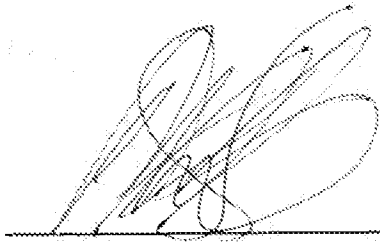
**Total Attachments: 4**

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# CERIFICATION OF TRUE COPY

I, **Phillip Shaer**, a Notary Public in and for the Province of Ontario by Royal authority duly appointing, residing in the City of Ottawa in the Province of Ontario, Canada, certify that I have compared the attached Assignment from Infineon Technologies North America Corp. to Infineon Technologies AG consisting of 3 pages with the original in this office, that I am the record custodian, and that the attached is a true, exact, complete, and unaltered copy.

In testimony whereof I have hereto subscribed my name and affixed my seal of Office at Ottawa, Ontario, Canada this 26<sup>th</sup> day of January, 2007.



Phillip Shaer

Notary Public in and for the Province of Ontario, CANADA

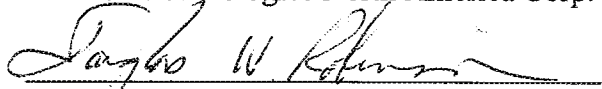
## ASSIGNMENT

Infineon Technologies North America Corp., a Delaware Corporation, hereinafter referred to as Assignor, hereby assigns and transfers to Infineon Technologies AG, a corporation organized and existing under the laws of Germany, having a principal place of business at St.-Martin-Strasse 53, 81669 Muenchen, Germany, hereinafter referred to as Assignee, the entire right, title and interest in and to any and all inventions disclosed and/or claimed in the United States, Japanese, German, French and Korean patents identified on Schedule A to this assignment ("the Patents"). All rights held by the Assignee as a result of this assignment have been and are granted, extended and transferred to Assignee as fully and entirely as they would have been held and enjoyed by the Assignor if this assignment had not been made.

Assignor represents and covenants that no assignment, grant, mortgage, license or other agreement affecting the rights and property herein conveyed has been made to any other party by Assignee.

Assignor:

Infineon Technologies North America Corp.



Douglas W. Robinson

Assistant Secretary

Dated: 9/1/06

Assignee:

Infineon Technologies AG



Dr. Dieter Joseph  
Senior Principal



Joe Villella  
Vice President

Date: Munich, 5th September 2006

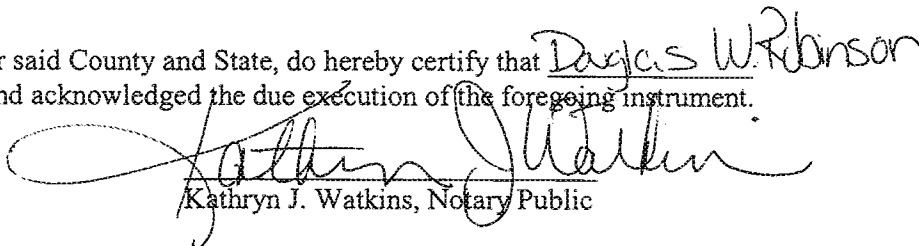
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State of North Carolina

County Wake

I, Kathryn J. Watkins, a Notary Public for said County and State, do hereby certify that Douglas W. Robinson personally appeared before me this day and acknowledged the due execution of the foregoing instrument.

(Official Seal)



Kathryn J. Watkins, Notary Public

My commission expires December 24, 2007

**PATENT**  
**REEL: 023401 FRAME: 0085**

## Schedule A

### US Patents:

Patent Number	Date of Grant	Filing No	Filing Date	Title
US 5210699	11.05.1993	07/759,190	10.09.1991	Process for Extracting Logic From Transistor and Resistor Data Representations of Circuits
US 5311407	10.05.1994	07/876,640	30.04.1992	Printed Circuit Board For Mounting Semiconductors And Other Electronic Components
US 5344794	06.09.1994	08/128,424	31.03.1993	A Pedestal Lead Frame for Supporting A Semiconductor Chip
US 5357674	25.10.1994	08/101,637	04.08.1993	Method Of Manufacturing A Printed Circuit Board
US 5446295	29.08.1995	08/110,608	23.08.1993	Silicon Controlled Rectifier with A Variable Base-Shunte Resistance
US 5506152	09.04.1996	08/299,717	23.08.1993	Method Of Making Silicon Controlled Rectifier With A Variable Baseshunt Resistance
US 5506425	09.04.1996	08/355,452	13.12.1994	Semiconductor Device And Lead Frame Combination
US 5723363	03.03.1998	08/537,062	29.09.1995	A Method Of Manufacturing A Semiconductor Device
US 5855924	05.01.1999	08/579,535	27.12.1995	Closed-Mold For LED Alphanumeric Displays
US 6100858	08.08.2000	08/940,703	30.09.1997	Alphanumeric Display With 21-Dot Matrix Format

### DE Patent:

Patent Number	Date of Grant	Filing No	Filing Date	Title
DE 69320090	05.08.1998	93105688.1	06.04.1993	Printed Circuit Board For Mounting Semiconductors And Other Electronic Components

### FR Patent:

Patent Number	Date of Grant	Filing No	Filing Date	Title
FR 567814	05.08.1998	93105688.1	06.04.1993	Printed Circuit Board For Mounting Semiconductors And Other Electronic Components

**JP Patent:**

Patent Number	Date of Grant	Filing No	Filing Date	Title
JP 3095783	04.08.2000	09-524347	17.12.1996	Closed-Mold For LED Alphanumeric Displays

**KR Patent:**

Patent Number	Date of Grant	Filing No	Filing Date	Title
KR 326487	18.02.2002	704208/95	31.03.1994	A Pedestal Lead Frame for Supporting A Semiconductor Chip