

PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
NSC CO., LTD	11/09/2009
RECEIVING PARTY DATA	
Name:	NS Semicon Co., LTD.
Street Address:	2-10-12, Shiba Daimon
City:	Minato-ku, Tokyo
State/Country:	JAPAN
Postal Code:	105-0012
PROPERTY NUMBERS Total: 8	
Property Type	Number
Patent Number:	6735608
Patent Number:	6763407
Patent Number:	6411238
Patent Number:	6486814
Patent Number:	6486813
Patent Number:	6489910
Patent Number:	6486815
Patent Number:	6448918
CORRESPONDENCE DATA	
Fax Number:	(503)224-7017
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	503-224-0115
Email:	ip@patenttm.us
Correspondent Name:	James H. Walters; patenttm.us
Address Line 1:	P.O. Box 82788
Address Line 4:	Portland, OREGON 97282-0788

OP \$320.00 6735608

501019475

PATENT
REEL: 023538 FRAME: 0240

ATTORNEY DOCKET NUMBER:	A-284 NSC TO NSSEMI 1118
NAME OF SUBMITTER:	James H. Walters
Total Attachments: 2 source=a284-nsc-nsseimcon110909ExecutedAssignment#page1.tif source=a284-nsc-nsseimcon110909ExecutedAssignment#page2.tif	

ASSIGNMENT

WHEREAS, NSC CO., LTD, a corporation organized under the laws of JAPAN, having a place of business at Shin Kojimachi Bldg. 8F, 3-3, Kojimachi 4-chome, Chiyoda-ku, Tokyo 102-0083 Japan (hereinafter also referred to as "Assignor") of the entire right, title and interest in the following U.S. Patents and U.S. Patent Applications:

U.S. Patent No. 6735608 issued on May 11, 2004, for
DATA INTERPOLATION METHOD (Attorney docket A-386
FLP0062PUS)

U.S. Patent No. 6763407 issued on July 13, 2004, for
DIGITAL-TO-ANALOG CONVERTER (Attorney docket A-387
FLP0064PUS)

U.S. Patent No. 6411238 issued on June 25, 2002 for
DIGITAL TO ANALOG CONVERTER WITH STEP VOLTAGE
GENERATOR FOR SMOOTHING ANALOG OUTPUT (Attorney docket
A-390 FLP0066PUS)

U.S. Patent No. 6486814 issued on November 26, 2002
for DIGITAL-TO-ANALOG CONVERTER USING DIFFERENT
MULTIPLICATORS BETWEEN FIRST AND SECOND PORTIONS OF A
DATA HOLDING PERIOD (Attorney docket A-402 FLP0081PUS)

U.S. Patent No. 6486813 issued on November 26, 2002
for OVERSAMPLING CIRCUIT AND DIGITAL/ANALOG CONVERTER
(Attorney docket A-403 FLP0082PUS)

U.S. Patent No. 6489910 issued on December 3, 2002 for
OVERSAMPLING CIRCUIT AND DIGITAL/ANALOG CONVERTER
(Attorney docket A-404 FLP0083PUS)

U.S. Patent No. 6486815 issued on November 26, 2002
for OVERSAMPLING CIRCUIT AND DIGITAL/ANALOG CONVERTER
(Attorney docket A-405 FLP0084PUS)



U.S. Patent No. 6448918 issued on September 10, 2002
for DIGITAL/ANALOG CONVERTER (Attorney docket A-406
FLP0085PUS)

Whereas **NS Semicon Co., LTD., of 2-10-12, Shiba Daimon,
Minato-ku, Tokyo, 105-0012, Japan** (hereinafter "Assignee") desires
to acquire the entire right, title and interest therein;

For good and valuable consideration, the receipt and
sufficiency of which are acknowledged, Assignor hereby assigns
and transfers to Assignee, its entire right, title and interest
in and to said patents and applications including any
substitute, renewal or reissue of said patents, and including
claims for damages by reason of past infringement of said
patents.

This assignment is binding upon Assignor, their successors,
legal representatives and assigns, and shall inure to the benefit
of Assignee, its successors, assigns and nominees.

The Commissioner of Patents is hereby authorized and
requested to recognize Assignee, their successors, legal
representatives and assigns, as the Assignee of 100 percent of
the right, title and interest therein, in accordance with the
terms of this Assignment.

In testimony whereof, the Assignor hereto has executed this
Assignment on this date November 9, 2009.

NSC CO., LTD

By Hajime Momose

Name: Hajime Momose

Title: President