

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Get2Chip, Inc.	12/07/2009
RECEIVING PARTY DATA	
Name:	Cadence Design Systems, Inc.
Street Address:	2655 Seely Avenue
City:	San Jose
State/Country:	CALIFORNIA
Postal Code:	95134
PROPERTY NUMBERS Total: 2	
Property Type	Number
Application Number:	09574572
Application Number:	09574693
CORRESPONDENCE DATA	
Fax Number:	(408)944-0215
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	408-914-6070
Email:	sunnyo@cadence.com
Correspondent Name:	Cadence Design Systems, Inc.
Address Line 1:	2655 Seely Avenue
Address Line 4:	San Jose, CALIFORNIA 95134
ATTORNEY DOCKET NUMBER:	GET2CHIP
NAME OF SUBMITTER:	Sunny Oh
Total Attachments: 4 source=Get2Chip_Assignment#page1.tif source=Get2Chip_Assignment#page2.tif source=Get2Chip_Assignment#page3.tif	

OP \$80.00 09574572

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PATENT
REEL: 023634 FRAME: 0402

PATENT ASSIGNMENT

This Patent Assignment (the "Assignment") is made and entered into by and between **GET2CHIP, Inc.**, a corporation organized and existing under and by virtue of the laws of the state of Delaware, (hereinafter referred to as "ASSIGNOR") and **CADENCE DESIGN SYSTEMS, Inc.**, a corporation organized and existing under and by virtue of the laws of the state of Delaware and having its principal place of business at 2655 Seely Avenue, San Jose, California 95134 (hereinafter referred to as "ASSIGNEE").

NOW, THEREFORE, for good and valuable consideration, the receipt of which is hereby acknowledged, the parties hereto agree as follows:

- 1) the ASSIGNOR hereby sells, assigns, transfers and sets over unto the said ASSIGNEE (i) all right, title and interest in and to the patents and applications listed on EXHIBIT A ("Assigned patents"), with the right to file, and all right title and interest in, divisionals, continuations, continuations-in-part, substitute, continued prosecution, request for continued examination, renewals, reexaminations and reissues of the Assigned Patents, including but not limited to the right to file, prosecute and issue patent applications and patents claiming priority to the Assigned Patents; and (ii) all past, present and future claims against any person with respect to the Assigned Patents, and all rights to recover past, present and future damages and remedies for the infringement or misappropriation of the Assigned Patents ("Assigned Claims"), with all of the foregoing hereinafter collectively referred to as "Assigned Patent rights".
- 2) The foregoing assignment of rights, title, and interest is not only in the United States and its territorial possessions, but in all countries foreign thereto to be obtained for the

Assigned Patents, and to any continuation, division, renewal, substitute or reissue thereof or any legal equivalent thereof in the United States or a foreign country for the full term or terms for which the same may be granted, including all priority rights under any international conventions or treaties; and ASSIGNOR hereby authorizes and request the United States Commissioner of Patents and Trademarks, and any officials of foreign countries whose duty it is to issue patents or any legal equivalent thereof, to issue the Assigned Patents to ASSIGNEE, its successors and assigns, in accordance with this Assignment.

- 3) The ASSIGNOR hereby covenants that no assignment, sale, agreement or encumbrance has been or will be made or entered into which would conflict with the Agreement; the ASSIGNOR further covenants that ASSIGNEE will, upon its request, be provided promptly with all pertinent facts and documents relating to said patents as may be known and accessible to ASSIGNOR and will testify as to the same in any interference or litigation related thereto and will promptly execute and deliver to ASSIGNEE or its legal representative any and all papers, instruments or affidavits required to apply for, obtain, maintain, issue and enforce said application, said patents and said equivalents in the United States or in any foreign country, which may be necessary or desirable to carry out the purposes thereof.

4) This Assignment shall be governed by the laws of the State of California.

“ASSIGNOR”

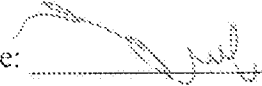
“ASSIGNEE”

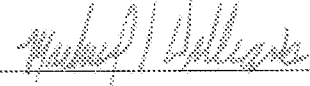
Get2Chip, Inc.

Cadence Design Systems, Inc.

By: Sharon Segev

By: Michael J. Williams

Signature:  _____

Signature:  _____

Title: Assistant Secretary

Title: Vice President & Associate General Counsel

Date: 12-4-09

Date: 12-4-09

EXHIBIT A
Assigned Patents

Title:	Juris.	Appl. No.:	Filing Date:	Reel/Frame:
Method for timing analysis during automatic scheduling of operations in the high-level synthesis of digital systems	US	09/574,572	May 17, 2000	N/A
Method for delay-optimizing technology mapping of digital logic	US	09/574,693	May 17, 2000	011051/0417