

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Cypress Microsystems	12/16/2009
RECEIVING PARTY DATA	
Name:	Cypress Semiconductor Corporation
Street Address:	198 Champion Court
City:	San Jose
State/Country:	CALIFORNIA
Postal Code:	95134
PROPERTY NUMBERS Total: 3	
Property Type	Number
Patent Number:	6727765
Patent Number:	6738834
Patent Number:	7308608
CORRESPONDENCE DATA	
Fax Number:	(408)545-6911
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
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Correspondent Name:	Cypress Semiconductor Corporation
Address Line 1:	198 Champion Court
Address Line 4:	San Jose, CALIFORNIA 95134
NAME OF SUBMITTER:	Andrew J. Bateman
Total Attachments: 3 source=CypressMicrosystems_CorporateAssignment#page1.tif source=CypressMicrosystems_CorporateAssignment#page2.tif source=CypressMicrosystems_CorporateAssignment#page3.tif	

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CORPORATE ASSIGNMENT

CYPRESS MICROSYSTEMS a corporation duly organized under and pursuant to the laws of **DELAWARE** and having its principal place of business at **2700 162ND STREET SW, BUILDING D, LYNWOOD, WASHINGTON 98087** (hereafter referred to as the "Assignor"), is the owner by respective assignment of the Issued Patents set forth in Schedule A, attached hereto (hereafter referred to as the "Patents").

CYPRESS SEMICONDUCTOR CORPORATION, a corporation duly organized under and pursuant to the laws of **DELAWARE** and having its principal place of business at **198 CHAMPION COURT, SAN JOSE, CALIFORNIA 95134** (hereafter referred to as the "Assignee"), desires to acquire the entire right, title, and interest in and to the Patents.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) and other good and sufficient consideration, the receipt of which is hereby acknowledged, the Assignor hereby sells, assigns, transfers, and sets over to the Assignee, its successors, legal representatives and assigns the entire right, title and interest in and to the Patents and all inventions described and claimed therein, the right to file applications on the inventions, and the entire right, title and interest in and to any applications for Letters Patent of the United States or other countries claiming priority to the Patents, including divisionals, continuations, and continuations-in-part of the Patents, and reissues, reexaminations, renewals and extensions of the Patents or Letters Patents, and any and all Letters Patent or Patents of the United States of America and all foreign countries that may be granted therefor and thereon, and all rights under the International Convention for the Protection of Industrial Property, the same to be held and enjoyed by the Assignee, for its own use and behalf and the use and behalf of its successors, legal representatives, and assigns, to the full end of the term or terms for which the Patents have been granted, and for which Letters Patent or Patents may be granted, as fully and entirely as the same would have been held and enjoyed by the Assignor had the present sale and assignment not been made.

By its undersigned representative, the Assignor agrees:

- a. to execute all papers necessary in connection with the Patents and any continuations, continuations-in-part, divisionals, reissues, reexaminations or corresponding applications thereof in any country, and also to execute separate assignments in connection with such application as the Assignee may deem necessary or expedient;
- b. to execute all papers necessary in connection with any interference that may be declared concerning the Patents or any continuations, continuations-in-part, divisionals,

reissues or reexaminations thereof, and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and

c. to perform all affirmative acts and take all lawful oaths that may be necessary or required to obtain a grant of a valid patent to the Assignee on the Patents and on any continuations, continuations-in-part, divisionals, reissues or reexaminations of the Patents in any country, and for the procurement, maintenance, enforcement, and defense of Letters Patent or Patents for the inventions described and claimed therein, without charge to the Assignee, its successors, legal representatives, and assigns, but at the cost and expense of the Assignee, its successors, legal representatives, and assigns.

The Assignor hereby covenants that, at the time of execution and delivery of the present assignment, the Assignor is the sole and lawful owner of the entire right, title, and interest in and to the inventions set forth in the Patents identified above, and has the full and complete right, title, and interest to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

The undersigned has reviewed the documents in the Patents identified above, and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this assignment on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the Patents, corresponding applications or any patents issuing thereon.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the undersigned's name.

CYPRESS MICROSYSTEMS

By:  **ANDB**
Neil Weiss

Title: Director

Date: 12/16/09

ASSIGNMENT

SCHEDULE A

CASE NO.	PATENT NO.	ISSUE DATE	FILING DATE	TITLE	INVENTORS (last names)
CD01248M	6,727,765	4/27/2004	6/28/2002	Stochastic Pulse Generator Device and Method of Same	VAN ESS
CD02067M	6,738,834	5/18/2004	7/15/2002	System for Reconfiguring a Peripheral Device Using Configuration Residing On the Peripheral Device by Electronically Simulating a Physical Disconnection and Reconnection to a Host Device	WILLIAMS and HASTINGS
CD01197M	7,308,608	12/11/2007	5/1/2002	Reconfigurable Testing System and Method	PLEIS, SULLAM and LESHER