PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
Cypress Semiconductor Corporation	12/15/2009

RECEIVING PARTY DATA

Name:	Formus Two B.V., L.L.C.
Street Address:	160 Greentree Drive
Internal Address:	Suite 101
City:	Dover
State/Country:	DELAWARE
Postal Code:	19904

PROPERTY NUMBERS Total: 13

Property Type	Number
Patent Number:	5805794
Patent Number:	5996091
Patent Number:	5835503
Patent Number:	5815510
Patent Number:	5768288
Patent Number:	6272646
Patent Number:	6912601
Patent Number:	6678861
Application Number:	09881493
Application Number:	09881367
Application Number:	60351639
Patent Number:	6954891
Patent Number:	7047479

CORRESPONDENCE DATA

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NAME OF SUBMITTER:	Michael K. Colby

Total Attachments: 7
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ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, Cypress Semiconductor Corporation, a Delaware corporation, with an office at 198 Champion Court, San Jose, CA 95134-1709 (*"Assignor"*), does hereby sell, assign, transfer, and convey unto Formus Two B.V., L.L.C., a Delaware limited liability company, having an address at 160 Greentree Drive, Suite 101, Dover, DE 19904 (*"Assignee"*), or its designees, all right, title, and interest that exist today and may exist in the future in and to any and all of the following (collectively, the *"Patent Rights"*):

(a)   the provisional patent applications, patent applications and patents listed in the table below (the *"Patents"*);

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
5805794	US	03/28/1996	Cpld serial programming with extra read register Jones, Christopher W.
5996091	US	8/19/1997	Cpld serial programming with extra read register Jones, Christopher W.
6954891	US	12/7/2001	Technique for payload-independent frame delineation engine for optical data transport Jha, Pankaj K.
TW220615	TW	12/25/2001	Programmable protocol processing engine for network packet devices Jha, Pankaj K.
5835503	US	3/28/1996	Method and apparatus for serially programming a programmable logic device Jones, Christopher W.

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
5815510	US	3/28/1996	Serial programming of instruction codes in different numbers of clock cycles Jones, Christopher W.
5768288	US	3/28/1996	Method and apparatus for programming a programmable logic device having verify logic for comparing verify data read from a memory location with program data Jones, Christopher W.
6272646	US	9/4/1996	Programmable logic device having an integrated phase lock loop Rangasayee, Krishna
6912601	US	6/28/2000	Method of programming plds using a wireless link Moore, Michael T.
6678861	US	8/21/2000	Fifo with crc in a pld Jones, Christopher W.
7047479	US	2/4/2002	Parallel crc formulation Hagh-Panah, Morteza

(b)   all patents and patent applications (i) to which any of the Patents directly or indirectly claims priority, (ii) for which any of the Patents directly or indirectly forms a basis for priority;

(c)   all reissues, reexaminations, extensions, continuations, continuations in part, continuing prosecution applications, requests for continuing examinations, divisionals, registrations of any item in any of the foregoing categories (a) and (b);

(d) all foreign patents, foreign patent applications, and foreign counterparts relating to any item in any of the foregoing categories (a) through (c), including, without limitation, certificates of invention, utility models, industrial design protection, design patent protection, and other governmental grants or issuances;

(e) all items in any of the foregoing in categories (b) through (d), whether or not expressly listed as Patents below and whether or not claims in any of the foregoing have been rejected, withdrawn, cancelled, or the like;

(f) inventions, invention disclosures, and discoveries described in any of the Patents and/or any item in the foregoing categories (b) through (e) that (i) are included in any claim in the Patents and/or any item in the foregoing categories (b) through (e), (ii) are subject matter capable of being reduced to a patent claim in a reissue or reexamination proceeding brought on any of the Patents and/or any item in the foregoing categories (b) through (e), and/or (iii) could have been included as a claim in any of the Patents and/or any item in the foregoing categories (b) through (e), except subject matter incorporated by reference in any of the Patents;

(g) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (f), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding;

(h) all causes of action (whether known or unknown or whether currently pending, filed, or otherwise) and other enforcement rights under, or on account of, any of the Patents and/or any item in any of the foregoing categories (b) through (g), including, without limitation, all causes of action and other enforcement rights for

(1) damages,
(2) injunctive relief, and
(3) any other remedies of any kind

for past, current, and future infringement; and

(i) all rights to collect royalties and other payments under or on account of any of the Patents and/or any item in any of the foregoing categories (b) through (h).

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Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

Assignor will, at the reasonable request of Assignee, do all things necessary, proper, or advisable, including without limitation, the execution, acknowledgment, and recordation of specific assignments, oaths, declarations, and other documents on a country-by-country basis, to assist Assignee in obtaining, perfecting, sustaining, and/or enforcing the Patent Rights. The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at San Jose, California on December 15, 2009.

ASSIGNOR:

Cypress Semiconductor Corporation

By: _____

Name: _____ Brad W. Buss _____

Title: Chief Financial Officer & Executive Vice President of Finance and Administration

(Signature MUST be attested)

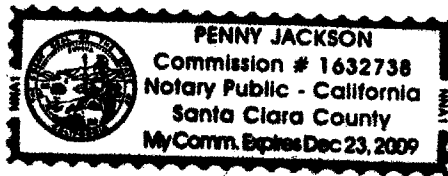## ATTESTATION OF SIGNATURE PURSUANT TO 28 U.S.C. § 1746

The undersigned witnessed the signature of <u>Brad W. Buss</u> to the above Assignment of Patent Rights on behalf of Cypress Semiconductor Corporation and makes the following statements:

1.   I am over the age of 18 and competent to testify as to the facts in this Attestation block if called upon to do so.

2.   <u>Brad W. Buss</u> is personally known to me (or proved to me on the basis of satisfactory evidence) and appeared before me on <u>December 15, 2009</u> to execute the above Assignment of Patent Rights on behalf of Cypress Semiconductor Corporation.

3.   <u>Brad W. Buss</u> subscribed to the above Assignment of Patent Rights on behalf of Cypress Semiconductor Corporation.

I declare under penalty of perjury under the laws of the United States of America that the statements made in the three (3) numbered paragraphs immediately above are true and correct.

EXECUTED on _12/15/09_____(date)

_____

Print Name:_____Penny JACKSON_____

PATENT
REEL: 023750 FRAME: 0024

Exhibit C

ASSIGNMENT OF RIGHTS IN CERTAIN ASSETS

For good and valuable consideration, the receipt of which is hereby acknowledged, Cypress Semiconductor Corporation, a Delaware corporation, with an office at 198 Champion Court, San Jose, CA 95134-1709 (*"Assignor"*), does hereby sell, assign, transfer, and convey unto Formus Two B.V., L.L.C., a Delaware limited liability company, having an address at 160 Greentree Drive, Suite 101, Dover, DE 19904 (*"Assignee"*), or its designees, the right, title, and interest in and to any and all of the following provisional patent applications, patent applications, patents, and other governmental grants or issuances of any kind (the *"Certain Assets"*):

Patent or Application No.	Country	Filing Date	Title of Patent and First Named Inventor
60/351639	US	10/29/2001	Technique for payload-independent frame delineation engine for optical data transport Jha, Pankaj K.
09/881493	US	06/14/2001	Programmable protocol processing engine for network packet devices Jha, Pankaj K.
CA2360711	CA	10/31/2001	Programmable protocol processing engine for network packet devices Jha, Pankaj K.
EP01127402.4	EP	11/23/2001	Programmable protocol processing engine for network packet devices Jha, Pankaj K.
JP2001-372455	JP	12/06/2001	Programmable protocol processing engine for network packet devices Jha, Pankaj K.
09/881367	US	06/14/2001	Programmable protocol processing engine for network packet devices Jha, Pankaj K.

Assignor assigns to Assignee all rights to the inventions, invention disclosures, and discoveries in the assets listed above, together, with the rights, if any, to revive prosecution of claims under such assets and to sue or otherwise enforce any claims under such assets for past, present or future infringement.

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to make available to Assignee all records regarding the Certain Assets.

The terms and conditions of this Assignment of Rights in Certain Assets will inure to the benefit of Assignee, its successors, assigns, and other legal representatives and will be binding upon Assignor, its successors, assigns, and other legal representatives.

DATED this 15th day of December 2009.

ASSIGNOR:

Cypress Semiconductor Corporation

By: _____

Name: _____ Brad W. Buss _____

Title: Chief Financial Officer & Executive Vice President of Finance and Administration