

PATENT ASSIGNMENT

Electronic Version v1.1

Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Micron Technology, Inc.	10/03/2008
RECEIVING PARTY DATA	
Name:	Aptina Imaging Corporation
Street Address:	c/o Citco Trustees (Cayman) Limited
Internal Address:	Regatta Office Park, West Bay Road
City:	Grand Cayman
State/Country:	CAYMAN ISLANDS
Postal Code:	Y1-1205
PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	6724426
CORRESPONDENCE DATA	
Fax Number:	(202)420-2201
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	202-420-2200
Email:	bakerm@dsmo.com
Correspondent Name:	Dickstein Shapiro LLP
Address Line 1:	1825 Eye Street, NW
Address Line 4:	Washington, DISTRICT OF COLUMBIA 20006
ATTORNEY DOCKET NUMBER:	M4065.0786/P786
NAME OF SUBMITTER:	Madeline E. Baker
<p>Total Attachments: 7</p> <p>source=Document#page1.tif</p> <p>source=Document#page2.tif</p> <p>source=Document#page3.tif</p>	

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PATENT ASSIGNMENT AGREEMENT

1. Definitions

(a) "Agreement" shall mean this Patent Assignment Agreement.

(b) "Assignee" shall mean **Aptina Imaging Corporation**, a Cayman Islands corporation with offices at c/o Citco Trustees (Cayman) Limited, Regatta Office Park, West Bay Road, Grand Cayman, Y1-1205, Cayman Islands.

(c) "Assignor" shall mean **Micron Technology, Inc.**, a Delaware corporation with offices at 8000 South Federal Way, Boise, ID 83707.

(d) "Effective Date" shall mean October 3, 2008.

(e)



(f)



(g) "Imaging Patents" shall mean those patents identified in ATTACHMENT "A" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(h) "Imaging Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "B" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(i) "Semiconductor Patents" shall mean those patents identified in ATTACHMENT "D" hereto, including, without limitation, all divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts thereof, and which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(j) "Semiconductor Patent Applications" shall mean those filed patent applications identified in ATTACHMENT "E" hereto, including, without limitation, all patents, divisions, continuations, continuations-in-part, reissues, reexaminations, and all foreign counterparts which may issue thereon or in connection therewith after the Effective Date of this Agreement.

(k) [REDACTED]

2. [REDACTED]

(a) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Imaging Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Imaging Patents to Assignee and to record assignment of the Imaging Patents to Assignee.

(b) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Imaging Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Imaging Patent Applications to Assignee.

(c) [REDACTED]

(d) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, and subject further to the restrictions on the Semiconductor Patents set out below, Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Semiconductor Patents, including all rights to causes of action and remedies related thereto (including the right to sue for past, present or future infringement of rights related to the foregoing and the right to collect damages therefor). Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to issue the Semiconductor Patents to Assignee and to record assignment of the Semiconductor Patents to Assignee.

(e) Subject to the terms and conditions of this Agreement, and subject to the rights of others existing as of the Effective Date of this Agreement if any, [REDACTED] Assignor, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, hereby sells, assigns, and transfers to Assignee its entire right, title and interest in and to the Intellectual Property rights in the Semiconductor Patent Applications. Assignor hereby authorizes and requests the Commissioner of Patents of the United States, and any other official of the United States and any country foreign to the United States whose duty it is to issue or record patents, to record assignment of the Semiconductor Patent Applications to Assignee.

(f)

[REDACTED]

[REDACTED]

[REDACTED]

3

[REDACTED]

[REDACTED]

4.

[REDACTED]

[REDACTED]

5.

[REDACTED]

[REDACTED]

[REDACTED]

6. [REDACTED]

[REDACTED]

7. General

This Agreement shall be effective as of the Effective Date hereof and shall be binding on the respective heirs, assigns, representatives, and successors of Assignor and of Assignee.

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Agreed to by:

Micron Technology, Inc.

Signed: [Signature]

Date: 9/26/08, 2008

Notarization

State of Idaho

County of Ada

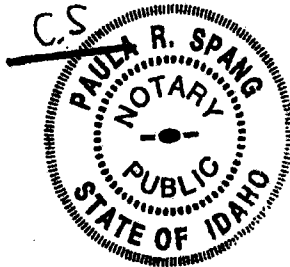
On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Steven R. Appleton

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11

REVIEWED
MTI LEGAL



Agreed to by:

Aptina Imaging Corporation

Signed: Thomas L. Lous, Jr.

Date: 9/26/08, 2008

Notarization

State of Idaho

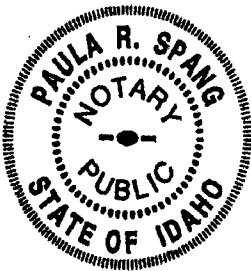
County of Ada

On 9/26/08, before me, Paula R. Spang, personally appeared the above-named Thomas L. Lous, Jr.

who executed this Assignment in my presence and acknowledged to me that he did so of his own free will and in his authorized capacity for the purposes set forth herein.

Signed: [Signature]

My commission expires: 8/12/11



[Signature page to the Patent Assignment Agreement]

ATTACHMENT "A"

IMAGING PATENTS

Aptina Issued

PATENT NUMBER	FILE NUMBER	TITLE	COUNTRY NAME	ISSUE DATE
		DYNAMIC COMPARATOR HAVING REDUCED THRESHOLD VOLTAGE SENSITIVITY		
6707937	AVGO-0010.00/US	INTERPOLATION OF EDGE PORTIONS OF A DIGITAL IMAGE	United States of America	Mar 16, 2004
6723584	1998-0272.01/US	PIXEL CELL WITH HIGH STORAGE CAPACITANCE FOR A CMOS IMAGER	United States of America	Apr 20, 2004
6724426	PBIT-0073.00/US	Multi Junction APS with Dual Simultaneous Integration	United States of America	Apr 20, 2004
6730980	1997-1490.02/US	MULTI-FRENCH REGION FOR ACCUMULATION OF PHOTO GENERATED CHARGE IN A CMOS IMAGER	United States of America	May 4, 2004
6734897	AVGO-0179.00/US	DIGITAL IMAGING CIRCUIT AND METHOD	United States of America	May 11, 2004
6738171	PBIT-0106.00/US	COLOR FILTER ARRAY AND MICROLENS ARRAY HAVING HOLOGRAPHIC OPTICAL ELEMENTS	United States of America	May 18, 2004
6740870	PBIT-0059.00/US	CLEAR PLASTIC PACKAGING IN A CMOS ACTIVE PIXEL IMAGE SENSOR	United States of America	May 25, 2004
6744084	2002-0111.00/US	TWO TRANSISTOR PIXEL WITH BURIED RESET CHANNEL AND METHOD OF FORMATION	United States of America	Jun 1, 2004
6744109	AVGO-0054.00/US	GLASS ATTACHMENT OVER MICRO-LENS ARRAYS	United States of America	Jun 1, 2004
6750490	PBIT-0047.02/US	PINNED FLOATING PHOTORECEPTOR WITH ACTIVE PIXEL SENSOR	United States of America	Jun 15, 2004
6753801	2002-0371.00/US	FULLY DIFFERENTIAL REFERENCE DRIVER FOR PIPELINE ANALOG TO DIGITAL CONVERTER	United States of America	Jun 22, 2004
6756576	1999-0112.00/US	IMAGING SYSTEM HAVING REDUNDANT PIXEL GROUPINGS	United States of America	Jun 29, 2004
6756616	1998-1080.00/US	CMOS IMAGER AND METHOD OF FORMATION	United States of America	Jun 29, 2004
6759724	AVGO-0157.01/US	ISOLATION OF ALPHA SILICON DIODE SENSORS THROUGH ION IMPLANTATION	United States of America	Jul 6, 2004
6765813	PBIT-0040.00/US	LOW LIGHT SENSOR SIGNAL TO	United States of America	Jul 20, 2004