orm PTO-1595 (Rev. 01-09)	U.S. DEPARTMENT OF COMMERCE United States Patent and Trademark Office
MB No. 0851-0027 (exp. 02/28/2009) RECORDATION FOR	
PATENTS	S ONLY
To the Director of the U.S. Patent and Trademark Office: Please	e record the attached documents or the new address(es) below.
Name of conveying party(ies):	2. Name and address of receiving party(les)
Teradyne, INC.	Name: AMPHENOL CORPORATION
_	Internal Address:
Additional name(s) of conveying party(iea) attached? Yes X No	Street Address:
3. Nature of conveyance/Execution Date(s):	\
Execution Date(s): 11/30/2005	358 Hall Avenue
X Assignment Merger Change of Name	1
Security Agreement Joint Research Agreement	City: Wallingford
Government Interest Assignment	State: Connecticut
Executive Order 9424, Confirmatory License	Country: United States of America Zip: 06492
Other	Additional name(s) & address(es) Yes X No attached?
Application or patent number(s):	This document is being filed together with a new application.
A. Patent Application No.(\$)	B. Patent No.(s)
11/785,130	
Additional numbers attache	ed? Yes X No
Name and address to whom correspondence concerning document should be mailed:	6. Total number of applications and patents involved:
Name: Peter S. Weissman BLANK ROME LLP	7. Total fee (37 CFR 1.21(h) & 3.41) \$ 40.00
internal Address: Atty. Dkt.: 124315.0491 Street Address: 600 New Hampshire Ave., NW	X Authorized to be charged to deposit account
The state of the s	Enclosed
	None required (government interest not affecting to
	Noue rednited (Angelmient uitelest not successful a
City: Washington	8. Payment Information
State: DC Zip: 20037	_
Phone Number: (202) 772-5800	— Deposit Account Number 23-2185
Fax Number: (202) 572-1405	Deposit Account Number 23-2185 Authorized User Name Peter S. Weissman
Email Address: Weissman@blankrome.com	
9. Signature:	February 12, 2010
Signature	
Peter S, Weissman - 40,220	Total number of pages including cover 36
Name of Person Signing	sheet, attachments, and documents:

INTELLECTUAL PROPERTY ASSIGNMENT AGREEMENT AND LICENSE AGREEMENT

This Intellectual Property Assignment Agreement and License Agreement (this "Agreement") is made this 30th day of November, 2005 (the "Effective Date"), by and between Teradyne, Inc., a Massachusetts corporation ("Assignor") and Amphenol Corporation, a Delaware corporation ("Buyer").

RECITALS

WHEREAS, Buyer and Teradyne, Inc. have entered into an Asset and Stock Purchase Agreement dated October 10, 2005 (the "Purchase Agreement"), pursuant to which Buyer shall purchase certain assets related to the Business (capitalized terms used herein and not otherwise defined shall have the respective meanings set forth in the Purchase Agreement);

WHEREAS, pursuant to the Purchase Agreement, Assignor desires to transfer and assign to Buyer, and Buyer desires to accept the transfer and assignment of all of Assignors' right, title and interest in and to the Purchased Intellectual Property including that Intellectual Property listed on Schedule A attached hereto, and to license to Buyer certain trademark rights for the use of those marks listed on Schedule C subject to certain conditions as identified herein;

WHEREAS, Buyer is willing to grant a limited license to Assignor with respect to certain Intellectual Property set forth on <u>Schedule B</u> transferred pursuant to the Purchase Agreement for internal use and for use in connection with its Products;

NOW, THEREFORE, for valuable consideration, the sufficiency of which is hereby mutually acknowledged, the parties agree as follows:

I. THE ASSIGNMENT OF INTELLECTUAL PROPERTY RIGHTS BY ASSIGNOR TO BUYER

Pursuant and subject to the Purchase Agreement, Assignor does hereby transfer, assign, convey and deliver to Buyer, and its Affiliates, successors and assigns, and Buyer hereby accepts the transfer and assignment of all of Assignor's right, title and interest in, to and under the Purchased Intellectual Property including the Intellectual Property listed on Schedule A attached hereto, and including, to the extent included therein: (i) all of the goodwill associated therewith; (ii) all rights to recover for infringement associated therewith whether arising prior to or subsequent to the date of this Assignment; (iii) all inventions disclosed therein and all divisionals, continuations, continuations-in-part, substitutes, renewals, reissues, reexaminations or other applications which have been or may be filed in the United States or any foreign country on any of said inventions, (iv) all patents which may be granted therefore throughout the world, including the right to file applications and obtain patent grants, utility models, industrial models and designs for any of said inventions in its own name throughout the world, and (v) all rights of priority, all rights to publish cautionary notices reserving ownership of said inventions, and all rights to register said inventions in appropriate registries.

-1-

To the extent that this Agreement is not acceptable for purposes of recording in a necessary office to indicate transfer to Buyer of any of the Purchased Intellectual Property, including the Intellectual Property listed on Schedule A, then in addition to this Agreement and without limiting its obligations under Section 4, for a period of one hundred eighty (180) days after the Closing or such other time period reasonably necessitated or required by local jurisdiction in question to achieve the transfer, Assignor and its Affiliates agree to execute individual assignments of all of the Purchased Intellectual Property including the Intellectual Property listed on Schedule A attached hereto, with respect to those individual countries where this Agreement is not acceptable for recording, which assignments shall be executed, and, where required, notarized and/or legalized apostille, on behalf of Assignor. The form of assignment or transfer shall be that used customarily in the jurisdiction in question to achieve the transfer on the terms set forth above. Buyer and Assignor shall each be responsible for fifty percent (50%) of the costs of filing or recording the assignments of all of the Purchased Intellectual Property in a necessary office to indicate transfer to Buyer.

2. THE LICENSE OF INTELLECTUAL PROPERTY RIGHTS BY BUYER TO ASSIGNOR

Buyer hereby grants to Assignor and its Affiliates, and their permitted successors and assigns, a limited, non-exclusive, perpetual, irrevocable, worldwide, royalty-free, non-sublicensable license under the Intellectual Property Rights listed in <u>Schedule B</u>, to design, have designed, use, have used, manufacture, have manufactured, market, sell, offer to sell, distribute (directly and indirectly), import and export:

- Assignor's and its Affiliates', current and future end products and/or subassemblies which contain a printed circuit board, backpanel, backplane, or equivalent structure;
- (ii) printed circuit boards, backpanels, backplanes, or equivalent structures, for use only in Assignor's and its Affiliates' current and future products, sold or provided as spare, repair or replacement parts;
- (iii) products used internally by Assignor and its Affiliates, or used internally by subcontractors, to manufacture products for Assignor and its Affiliates; and
- (iv) in the case of DesignLink software and its intellectual property identified in <u>Schedule</u>
 B, to be used internally by Assignor and its Affiliates or externally by their customers,

which, except for the limited license granted herein, would infringe a right conferred by the Intellectual Property Rights identified on <u>Schedule B</u>.

The rights granted hereunder are not intended to nor shall be they be construed to minimize or limit the noncompetition restrictions contained in Section 5.05(a) of the Purchase Agreement.

Assignor agrees that Buyer has no obligation to maintain or continue the prosecution of any provisional or pending patent applications listed in <u>Schedule B</u>; provided that, at least ninety (90) days before allowing any such provisional or pending applications to lapse, Buyer will provide notice to Assignor, whereupon Assignor will have the option to maintain or continue prosecution of such provisional or pending applications at Assignor's expense. In such an event,

Assignor will assume all ownership rights in the provisional or pending applications and Buyer agrees to assign such ownership rights to Assignor; provided that, in such event Assignor shall grant Buyer a license under such Patent substantially similar to the license granted to Assignor in this Section 2.

Assignor acknowledges that a breach of the license granted by Buyer in this Section 2 may cause the Buyer irreparable damage, for which the award of damages alone may not be adequate compensation and the Buyer is therefore entitled to seek injunctive relief to enjoin the Assignor from violation of those provisions. Such injunctive relief remedy shall be cumulative and not exclusive and the Buyer is entitled to seek any other relief available to it at law or in equity.

3. THE LICENSE OF CERTAIN TRADEMARK RIGHTS BY ASSIGNOR TO BUYER

For a term of not more than two (2) years from the Effective Date, Assignor hereby grants to Buyer, and its Affiliates, successors and assigns, a non-exclusive, worldwide, royalty-free, non-transferable right and license to the rights Assignor has to the mark set forth on Schedule C (hereinafter "Licensed Mark") only on products formerly offered by Assignor's TCS division and only in conjunction with the word "Amphenol" (for example "an Amphenol TCS product" or "an Amphenol product formerly manufactured by TCS"). Buyer may continue to use Assignor's existing inventory of stationary, business cards, marketing literature, preprinted documents, and similar materials that are branded only with the Licensed Mark but not any other trademark of Assignor (for example, excluding any materials branded with the word "Teradyne") until such existing supply is exhausted. This license does not extend to the use by Buyer of the Licensed Mark on a stand-alone basis.

Buyer agrees that its use of the Licensed Mark will be in accordance with applicable law and Assignor's reasonable policies regarding advertising and trademark usage as published from time to time by Assignor. Buyer agrees to use the Licensed Mark in a manner reasonably consistent with the established standards of quality and goodwill associated with the Licensed Mark prior to the Effective Date. If, in Assignor's sole discretion, Assignor reasonably determines that Buyer's use of the Licensed Mark harms, tarnishes, dilutes or damages the goodwill associated with the Licensed Mark, Assignor shall notify Buyer in writing and Buyer shall correct any such use within ten (10) business days of receipt of such notice or Assignor may unilaterally terminate this license in the Licensed Mark.

In order to verify compliance with this Section 3 of this Agreement, Assignor may from time to time require that Buyer provide samples of Buyer's marketing materials that bear the Licensed Mark.

4. FURTHER ASSURANCES

Assignor and Buyer hereby further agree, each for itself and its successors, assigns, and legal representatives, to execute upon request any other lawful documents and likewise to perform any other lawful acts that are necessary to secure fully the aforesaid rights, titles, and interests in and to said Purchased Intellectual Property.

5. EXECUTION OF COUNTERPARTS

This Assignment may be executed in one or more counterparts, each of which will be deemed to be an original copy of this Assignment and all of which, when taken together, will be deemed to constitute one and the same agreement. The exchange of copies of this Assignment and of signature pages by facsimile transmission shall constitute effective execution and delivery of this Assignment as to the parties and may be used in lieu of the original Assignment for all purposes. Signatures of the parties transmitted by facsimile shall be deemed to be their original signatures for all purposes.

6. ENTIRE AGREEMENT; AMENDMENT

Together with the Purchase Agreement and the Ancillary Agreements (as defined therein), this document contains the entire agreement relating to the subject matter contained herein and supersedes all prior or contemporaneous agreements, written or oral, between the parties. This Agreement may not be modified except by written document signed by an authorized representative of each party.

7. PRECEDENCE

This Agreement is executed and delivered pursuant to, and is subject to, the Purchase Agreement. In the event of any conflict between the terms of the Purchase Agreement and the terms of this Agreement, the terms of the Purchase Agreement shall prevail.

8 SEVERABILITY

Any provision of this Agreement which is invalid, illegal or unenforceable in any jurisdiction shall, as to that jurisdiction, be ineffective to the extent of such invalidity, illegality or unenforceability, without affecting in any way the remaining provisions hereof in such jurisdiction or rendering that or any other provision of this Agreement invalid, illegal or unenforceable in any other jurisdiction.

GOVERNING LAW

This Agreement shall be construed in accordance with and governed by the laws of the State of New York, without regard to the conflicts of law rules of such state.

10. ASSIGNMENT

This Agreement and the rights and licenses hereunder granted are not assignable by Assignor without the written consent of Buyer except, with reasonable notice to Buyer: (i) to an Affiliate of Assignor; (ii) in connection with an assignment of all or substantially all of the assets of Assignor or a business division of Assignor.

[REMAINDER OF PAGE LEFT INTENTIONALLY BLANK]

IN WITNESS WHEREON executed and delivered as of Nov	F, the parties have caused this ASSIGNMENT to be duly ember, 2005.
ASSIGNOR: TERADYNE, INC. By:	State of Mass Achazelts) ss.: County of Suffe & On this 30 day of NO3. , 2005, before me, personally appeared Gracory Recent of West Fo of Teradyne Inc. personally known to me (or proved to me on the basis of satisfactory evidence consisting of
BUYER: AMPHENOL CORPORATION By: Name: Title:	State of

SIGNATURE PAGE TO INTELLECTUAL PROPERTY ASSIGNMENT

IN WITNESS WHEREOF, executed and delivered as of	the parties have caused this ASSIGNMENT to be duly, 2005.
ASSIGNOR:	
TERADYNE, INC.	(State of) ss.: (County of)
By: Name: Title:	On this
	Witness my hand and official seal
	Notary Public
BUYER:	
AMPHENOL CORPORATION	(State of <u>Connecticut</u>) ss.: Wallingford (County of <u>New Haven</u>)
By: / Joinothy & Lohane. Name: Timothy F. Coha Title: Executive V.P.	On this day of, 2005, before me, personally appeared Twother E CHANE, of AMPHENCI ORFORATION personally known to me (or proved to me on the basis of satisfactory newidence consisting of) to be the person whose name is subscribed to the within instrument and acknowledged to me that he executed the same in his authorized capacity and that by his signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.
	Witness my hand and official seal New Commission Expires: September 30, 2008

[SIGNATURE PAGE TO INTELLECTUAL PROPERTY ASSIGNMENT]

SCHEDULE A

Intellectual Property Assigned by Assignor to Buyer

Patents:

0007 CA 1274887 02-Oct-1990 07-Jan-1987 Granted Title: DAUGHTER BOARD BACKPLANE ASSEMBLY 10-Jan-1987 Granted Title: DAUGHTER BOARD BACKPLANE ASSEMBLY 08-Jan-1986 Granted Title: DAUGHTER BOARD BACKPLANE ASSEMBLY 08-Jan-1986 Granted	Çase#	Country	Patent #	Issue Date	Filing Date	Status	
Title: DAUGHTER BOARD BACKPLANE ASSEMBLY 0007 JP 1587364 10-Jan-1987 Granted Title: DAUGHTER BOARD BACKPLANE ASSEMBLY 0007 US 4686607 11-Aug-1987 08-Jan-1986 Granted	0007	CA CA			07-Jan-1987	Granted	
0007 US 468607 11-Aug-1987 08-Jan-1980 Grancu	Title:	DAUGHTER IP	1587364		10-Jan-1987	Granted	
	0007	US	4686607	11-Aug-198/	08-Jan-1986	Granted	

0008 Title:	PRINTED CIRCUIT BO. DE 40 PRINTED CIRCUIT BO.	ARDS WITH IMPROVED ELECTRICAL CURRENT CONTROL 12:146 29-Dec-1992 22-Sep-1989 Granted 13:12146 29-Dec-1992 22-Sep-1989 Granted 14:12146 29-Dec-1992 ELECTRICAL CURRENT CONTROL 15:12146 29-Dec-1992 Granted 16:12146 29-Dec-1992 Granted 16:12146 29-Dec-1992 Granted 16:12146 29-Dec-1992 Granted 16:12146 29-Dec-1992 Granted
	PRINTED CIRCUIT BO	229042 26-Jan-1990 Granted ARDS WITH IMPROVED ELECTRICAL CURRENT CONTROL 038252 06-Aug-1991 26-Jan-1989 Granted OARDS WITH IMPROVED ELECTRICAL CURRENT CONTROL

Γ	0009		18-Nov-1986	Granted
	Title:		(A-140A-120)	Granted
	Title:	BACKPLANE-DAUGHTER BOARD CONNECTOR		
ı				

	0010 US 4871321 03-Oct-1989 22-Mar-1988 Granted Title: ELECTRICAL CONNECTOR
1	

0012	DE	4411187	05-Oct-2000	30-Mar-1994	Granted
Title: 1	POWER CONNECT FR	2703518	20-Sep-1996	31-Mar-1994	Granted
0012	POWER CONNECT: JP	2706619	09-Oct-1997	31-Mar-1994	Granted
0012	POWER CONNECT US POWER CONNECT	5360349	01-Nov-1994	31-Mar-1993	Granted

0016	US	4909743	20-Mar-1990	14-Oct-1988	Granted	
Title:	ELECTRICAL (•				
		1346777	29-Nov-1988	16-Aug-1985	Granted	
	CA	1245733	73-1404-1300	10-74¢Ë-1303	<u></u>	
Title:	BACKPLANE (ONNECTOR_				
0067	CA	2119500	24-Nov-1998	21-Mar-1994	Granted	
0057	CALLI DED EL.	ECTRICAL CONN				
0057	DE	4410047D	28-Sep-2000	23-Mar-1994	Granted	
Tielos	SHIELDED EL	ECTRICAL CONN				
0057	FR	2706223	04-Jul-1997	29-Mar-1994	Granted	
UV)/ Title-		ECTRICAL CONN				
0057	FR	2707045	23-Oct-1998	01-Jul-1994	Granted	
Titles	SHIELDED EL	ECTRICAL CON				
11tie:	GB	2276989	29-Jan-1997	25-Mar-1994	Granted	
7141	CRIEI VEV EL	ECTRICAL CON				
		2739922	23-Jan-1998	04-Apr-1994	Granted	
0057	JP	ECTRICAL CON		~ · · · •		
		5403206	04-Apr-1995	02-Apr-1993	Granted	
0057	US CAUSE DED EL			02 (1)		
		ECTRICAL CONI 5605476	25-Feb-1997	07-Jun-1995	Granted	
0057	US			•,		
		ECTRICAL CON 5484310	16-Jan-1996	06-Mar-1995	Granted	
0057	US			AA 11701 1555		
		ECTRICAL CON	04-Маг-1997	17-Jun-1996	Granted	
0057	US	5607326		11 *****	•	
Title:	SHIELDED EI	ECTRICAL CON	NECTOR			
<u>,</u>		***********	06 1am 1000	17-Apr-1995	Granted	
1009	US	5704793 HIGH DENSITY	90-Jan-1770 CONNIECTAD EC	ነው ፎ፤ ድርፕድርንህ፤ 17-ሚኮ-1422	SIGNALS	
	HIGH SPEED,	HIGH DENSITY	13-Oct-1998	21-May-1997	Granted	
1009	US	5820397	CONDIDECTOR EC	NO ELECTRONIO	SIGNALS	
Title:	HIGH SPEED	HIGH DENSITY	CONNECTORFO	AT TITLE I WOLLE		
		(DEIDOSC)	21-Jul-1999	04-Dec-1995	Granted	
1011	DE	69510956.1		0-1 5-40 1334		
l		CUIT BOARD CO	21-Jul-1999	04-Dec-1995	Granted	
1011	EP	00803136		0.1 5.00 1332		
1		CUIT BOARD CO	21-Jul-1999	04-Dec-1995	Granted	
1011	FR	00803136	_	04-10-04-1330		
Title:		CUIT BOARD CO	77 Y 1 1000	04-Dec-1995	Granted	
1011	GB	00803136	21-Jul-1999	V4-1760-1773	CHOUNT	
Title	: PRINTED CII	CUIT BOARD CO	DNNECTORS	19 1, . 1005	Granted	
1011	UŠ	5595490	21-Jan-1997	13-Jan-1995	Granteu	
Title	: PRINTED CI	CUIT BOARD C	ONNECTORS			
				04 34 1004	Granted	
1016	DE	0829117	05-Jul-2000	04-Mar-1996	/1) Stillien	
Title	: SURFACE M	OUNTED ELECT	RICAL CONNEC	1UK	Granted	
1016	GR	0829117	05-Jul-2000	04-iviar-1990	Cranicu	
Title		OUNTED ELECT	RICAL CONNEC	1OK 31-May-1993	5 Granted	
1016	118	6152742	28-Nov-2000	21-initia-122	, Ciaille	
1 Tiffe	e SURFACE M	OUNTED ELECT	RIÇAL ÇONNEC	IUK		

 "	Comptent	Patent #	Issue Date	Filing Date	Status	
Case #	Country US	6042386	28-Mar-2000		Granted	
Title:	SURFACE MOU	INTED ELECTRI	CAL CONNECTO	R		
1041	US	5672064	30-Sep-1997	21-Dec-1995	Granted	
Title:	STIFFENER FO	R ELECTRICAL	CONNECTOR			
1054	US	5690504	25-Nov-1997	13-May-1996	Granted	
Title:	PLASTIC GUII	E PIN WITH ST	EEL CORE			
1056	DE	69722392.2	2 28-May-2003	06-Feb-1997	Granted	
Title:	Electrical Conne	ector Assembled F	rom Wafers 28-May-2003	06-Feb-1997	Granted	
1056 Title:	EP • Electrical Conn	0890201 ector Assembled F				
1056	FR	0890201	28-May-2003	06-Feb-1997	Granted	
Title: 1056	: Electrical Conn GB	ector Assembled F 0890201	28-May-2003	06-Feb-1997	Granted	
Title	: Electrical Conn	ector Assembled F	rom Wafers	06-Feb-1997	Granted	
1056	IT Floatsical Conn	25969BE/0 ector Assembled I)3 25-Aug-2003 From Wafers	00-L60-122/	Cianted	
1056	KR	319306	18-Dec-2001	06-Feb-1997	Granted	
		ector Assembled I 5702258	From Waters 30-Dec-1997	28-Mar-1996	Granted	
1056 Title	US :: Electrical Conf	ector Assembled)	From Wafers		C	
1056	US I	5860816 lector Assembled I	19-Jan-1999	24-Nov-1997	Granted	
Title	: Electrical Conf	lectot Waseimolen i	Tion waters			
			23-Mar-1999	28-May-1996	Granted	
1085	US • Electrical Cont	5885095 nector Assembly V	Vith Mounting Hard	ware And Protec	tive Cover	
1111	27 Dicott tent Aam					
	Y-1-1	<u> </u>	D 02-May-2003	15-Jan-1998	Granted	
1091 Title	DE e- High Speed, H	igh Density Electr	ical Connector		- '	
1001	EP	1021854	02-May-2003	15-Jan-1998	Granted	
	e: High Speed, H FR	igh Density Electr 1021854	02-May-2003	15-Jan-1998	Granted	
1091 Titl	e: High Speed, fi	ligh Density Electr	ical Connector		(T+)	
1091	GΒ	1021854	02-May-2003	15-Jan-1998	Granted	
1091	MΧ	ligh Density Electr 212198	17-Dec-2002	15-Jan-1998	Granted	
Titl	le: High Speed,	High Density Ele	ctrical Connector 30-Nov-1999	07-Feb-1997	Granted	
1091 Titl	US le: High Speed, l	5993259 Jigh Density Elect	rical Connector		_	
3091	US	6238245	29-May-2001	26-Aug-1999	Granted	
Tit!	le: High Speed, l	ligh Density Elect	Licai Connector			

Çase#	Country	Patent #	Issue Date	Filing Date	Status
1103	DE	69805426	15-May-2002	15-Jan-1998	Granted
Title	HIGH SPEED.	HIGH DENSITY EI	LECTRICAL CON	INECTOR	
1107	ED	1021855	15-May-2002	13-393-1220	Granted
Title	HIGH SPEED	HIGH DENSITY E	LECTRICAL CON	NECTOR	_
1 1102	ĊĎ	1071855	15-May-2004	13-1911-1530	Granted
Title	WIGH SPEED	, HIGH DENSITY E	LECTRICAL CON	NECTOR	
1107	CP	1021855	15-May-2002	12-1911-1220	Granted
Titler	HIGH SPEED	, HIGH DENSITY E	LECTRICAL CO	VNECTOR	
1302	1E	1021855	[5-May-2002	12-3411-1220	Granted
Titles	nich zbeed	, HIGH DENSITY E	LECTRICAL COL	NNECTOR	
1107	እለፕ	212197	17-L7ec-2002	12-39U-1220	Granted
Titles	MIGH SPEED	, HIGH DENSITY E	LECTRICAL CO	NNECTOR	
3 103	NII	1021855	15-May-2002	12-1911-1220	Granted
Title	HIGH SPEED	HIGH DENSITY E	LECTRICAL CO	NNECTOR	
11107	CE	1071855	15-Mav-4004	13-1011-1220	Granted
Title	HIGH SPEED	, HIGH DENSITY E	LECTRICAL CO	NNECTOR	_
1102	211	5980321	09-MOA-1333	0/-reo-1997	Granted
Title	HIGH SPEED	, HIGH DENSITY E	LECTRICAL CO	NNECTOR	
1103	LIC	62 99 483	09-Oct-2001	20-Aug-1777	Granted
Title	HIGH SPEEL	, HIGH DENSITY E	LECTRICAL CO	NNECTOR	
1151	CN	99813967.>	₹ 27-Apr-2005	01-Jun-2001	Granted

151	CN	91	9813967 X	27-Apr-2005	01-Jun-2001	Granted
Title:	Printed (Circuit Board an	d Method f	or Fabricating	Such Board	
161	MX	2	23480] 3-Qct-2004	01-1011-2001	Granted
Title:	Printed (Circuit Board an	d Method f	or Fabricating	Such Board	
151	HS	6	181219	30-Jan-2001	02-1366-1336	Granted
Title:	Printed	Circuit Board an	d Method f	or Fabricating	Such Board	
1151	ED	1	138180	24-Mar-2004	1 02-060-1999	Granted
Title:	Printed	Circuit Board an	d Method f	or Fabricating	Such Board	Granted
1151	FR	j	138180	TBD	02-060-1333	Cianica
Title:	Printed	Circuit Board ar	id Method i	for Fabricating	02-Dec-1999	Granted
1151	NE	_ 1	138180	TBD		Citamon
Title:	Printed	Circuit Board ar	id Method 1	or Pabricating	02-Dec-1999	Granted
1151	DE		9915874.5	IBU C Eshalantina		QI WITTO
Title:	Printed	Circuit Board ar	id Method	or rapricating	Sacit Dagle	

1154 CN	1127780	12-Nov-2003	24-May-2001	Granted
Title: ELEC	CTRICAL CONNECTOR 1133812	31-Mar-2004	22-Nov-1999	Granted
1154 DE		31-Mar-2004	22-Nov-1999	Granted
1154 FR		31-Mar-2004	22-Nov-1999	Granted
1154 GE		31-Mar-2004	22-Nov-1999	Granted
1154 US		11-Mar-2003	24-Nov-1998	Granted
1154 US	CTRICAL CONNECTOR 6537087 CTRICAL CONNECTOR	25-Mar-2003	25-Jan-2002	Granted

Case #	Country	Patent #	Issue Date	Filing Date	Status
1220	US	6379188	30-Apr-2002	24-Nov-1998	Granted
1220	Differential Signal US DI	6503103	0/-Jan-2003	22-Jun-2000	Granted
1220	Differential Signal US D2	655 464 7	29-Apr-2003	22-Jun-2000	Granted
1220	Differential Signal US D3 Differential Signal	6607402	19-Aug-2003	08-Арг-2002	Granted
			-		
1221	US	6565387	20-May-2003	30-Jun-1999	Granted
Title:	Modular Electrical MX Modular Electrica	228505	12-1 nu- 5003	19-Dec-2001	Granted
				22-Nov-1999	Granted
1227	CN Electrical Connec	1 139151 for with Impedat	-Feb-2004 ace Balancing Secti	ons	_
1227	EĎ	1145386	24-M2r-2004	27-140 4-1322	Granted
1227	Electrical Connec US	6394822	28-May-2002 lance Balancing Section	24-1101-1770	Granted
Title:	Electrical Colin	octor treatment			
1228 Title	US : Electrical Conne	6152747 ctor with End of	28-Nov-2000 Row Contacts to E	24-Nov-1998 qualize Performa	
		6388208	14-May-2002	23-Jul-1999	Granted
1284 Title:	US Multi-Connecti	on VIA with Ele	ctrically Isolated S	-	
11114				23-Jul-1999	Granted
1285 Title	US SPLIT VIA SUR	6137064 FACE MOUNT	24-Oct-2000 CONNECTOR AN	VD RELATED T	
1302	US	6593535	15-Jul-2003	26-Jun-2001	Granted
Title:	DIRECT INNI	R LAYER INT	ERCONNECT FOI	R A HIGH SPEE	D PRINTED CIRCUIT BOARD
1325 Title:	US Differential Si	6293827 gnal Electrical C	25-Sep-2001 Connector	03-Feb-2000	Granted
Title.					Granted
1326 Title:	US HIGH SPEED F	6517360 RESSURE MO	11-Feb-2003 UNT CONNECTO	[[-Jun-2001 R	Graned
3207	DE	1256145	24-Nov-2004	30-Jan-2001	Granted
1277	le: CONNECTOR	WITH EGG-CR 1256145	ATE SHIELDING 24-Nov-2004	30-Jan-2001	Granted
Tit	le: CONNECTOR FR	1256145	Z4-NOV-ZUU4	30-Jan-2001	Granted
Tit	le: CONNECTOR	156867	26-36 p- 2004	18-May-200] Granted
1327	le: CONNECTOR US	6506076	14-Jan-2003 CRATE SHIELDIN	71-1411 2001	Granted
Title:	201112010				

Case #	Country	Patent #	Issue Date	Filing Date	Status
1380	US	6639154	28-Oct-2003	10-Oct-2000	Granted
Title:	Methods and At	paratus For Form	ing A Connection	ı between A Circi	uit Board And A Connector
4 44444					
1383	US	6352436	05-Mar-2002	29-Jun-2000	Granted
1202		ssure Connection			
1388	US	6520686	18-Feb-2003	09-Nov-2000	Granted
Titla-	Method and Ann:	aratus for Forming	a Fiber Optic Co	nnection	
111101	mono and repr				
1388	US	6616342	09-Sep-2003	01-Oct-2002	Granted
Title:	Method and Appa	aratus for Forming	a Fiber Optic Co	nnection	
	Tradition III				
1393	US	6452379	17-Sep-2002	20-Oct-2000	Granted
Title:	Methods and An	paratus for Conne	ctine to A Signal	Launch	
1393	US I	6717398	06-Apr-2004	22-Jul-2002	Granted
Title:	Methods and A	pparatus for Con		il Launch	
I RIC.	INICHIOGS MINI I	pparatas 151 551	<u>.</u>		
		6511229	28-Jan-2003	21-Dec-2000	Granted
1394	US	0011249			-
Title:	Methods and A	pparatus for Cont	totting Access to	All Options inters	
				10.0-+ 0000	Granted
1395	US	6516105	04-Feb-2003	10-Oct-2000	Granies
Title:	Optical Backpl	ane Assembly and	Method of Maki	ing Same	
					and the second s
1396	US	6619854	16-Sep-2003	31-Jan-2001	Granted
Title:	Techniques for	Cleaning An Opt	cal Interface of A	n Optical Connec	tion System
1405	US	6588943	08-Jul-2003	07-Aug-2000	Granted
Title:		Connector Module	-	-	
inc.	Dicogo Opera				
1110	EP	1360533	23-Mar-2005	30-Jan-2002	Granted
1410	Taskaitanaa for i	Selectively Expos	ing and Protecting		face Using Film
	DE	60203372	23-Mar-2005	30-Jan-2002	Chamed
1410	DE Tachniques fo	- Salentively Fyn	sine and Protecti	ng An Optical Int	erface Using Film
Title:	ED	1360533	23-Mar-2005	30-Jan-2002	Grantea
1410	Tachniques fo	- Selectively Exp	sing and Protecti	ng An Optical Int	erface Using Film
Title:	CD	1360533	23-Mar-2005	50-Jan-2002	Qi anteo
1410 Title:	Techniques fo	r Selectively Exp	osing and Protecti	ng An Optical Int	erface Using Film
1410	110	654744 4	15-Anr-2003	3[-Jan-200]	/21 Stifen
Title:	Techniques fo	r Selectively Exp	osing and Protecti	ing An Optical Int	terface Using Film
11the:	Tooming and to	1 Controlling			
	TALL	205664	26-Oct-2004	15-Jan-2002	Granted
1437	TW Mak	ding Method and	20-001-2007 Chielded Waferize		
		nng Method and 6409543	25-Jun-2002	25-Jan-2001	Granted
1437	US Commenter Male	وموروب : ding Method and	25-3011-2002 Shielded Waferiza	ed Connector Mad	
Title:		ecopos Anticipos ano	o5-Aug-2003	24-Apr-2002	Granted
1437	US	6602095 olding Method and	cous-gum-co irabelli bablaidh b	zed Connector M	ade Therefrom
Title:	Connector M	olding Memod an	TOTHER WATER	SPU TOTAL	
			ni i roce	26-Jun-1997	Granted
1447	ÜŚ	5945838	31-Aug-1999	₹0-1π 0-133 7	Giantou
Title:	Apparatus for	Testing Circuit E	soards		
					5 C
1448	US	6334784	01-Jan-2002	07-Aug-200	0 Granted
Title:	7-Axis Press	ure Mount Connec	ctor Fixture		

Case #	Country	Patent #	Issue Date	Filing Date	Status
477 Title:	US Optical Connector Misalignments by	6736546 Ferrule Designed Incorporating Ex	18-May-2004 I to Minimize Man act Constraint Pri	16-Jul-2002 rufacturing Imper nciples	Granted fections and Mating
48 Title:	US Electrical Solder B	6641410 all Contact	04-Nov-2003	07-Jun-2001	Granted
1482 Title:	US Waferized Power (6592381 Connector	15-Jul-2003	25-Jan-2001	Granted
1485	ĖP	1358697	04-May-2005	01-Feb-2002	Granted
1485	Matrix Connector US Matrix Connector	6769935	03-Aug-2004	01-Feb-2002	Granted
1486 Title	US : High-Density Fibe	6547445 er Optic Backplar	15-Apr-2003 ne	06-Feb-2001	Granted
1489 Title	US : High Speed Multi	6541712 -Layer Printed C	01-Apr-2003 ircuit Board VIA	04-Dec-2001	Granted
1524 Title	US : Waferized Fiber (6769814 Optic Connector	03-Aug-2004	16-Jul-2002	Granted
1615 Title	US :: SELF-ALIGNIN	6739918 G ELECTRICAI	25-May-2004 CONNECTOR	01-Feb-2002	Granted
1617 Titl e	US e: Methods and App	6839935 paratus for Clean	11-Jan-2005 ing Optical Conne	29-May-2002 ctors	Granted
1636 Titl	US e: Matrix Connecto	6764349 r with Integrated	20-Jul-2004 Power Contacts	29-Mar-2002	Granted
I 684 Titl	US e: Electrical Conne	6709294 ctor with Conduc	23-Mar-2004 ctive Plastic Featur	17-Dec-2002 res	Granted
1718 Titl	US le: Techniques for I	6832858 orming Fiber Op	21-Dec-2004 otic Connectors In	13-Sep-2002 A Modularized N	Granted Manner

	Country	Patent #	Issue Date	Filing Date	Status	
719 Title:	US Techniques for Apparatus	6762941 Connecting A Set o	13-Jul-2004 f Connecting Elem	15-Jul-2002 tents Using An In	Granted nproved Latching	
1734	DE	3751977.8	10-Sep-1987	Granted	uit Boards and	
Title:	Reinforced Plas	tic Laminates for U	ise in the Production Br	vynete Vii Of Littiten Oue	dit Dogida tira	
	Process for Mai	cing Such Laminate 0326577	10-Sep-1987	Granted		
1734	FR	stic Laminates for U	tee in the Productiv		uit Boards and	
I itie:	Remnorceu Flas	king Such Laminate	s and Resulting P	oducts		
1734	CD	0326577	10-Sep-1987	Granted		
Title:	Reinforced Plas	stic Laminates for U	ise in the Producti	on of Printed Circ	uit Boards and	
E Street	Process for Ma	king Such Laminate	es and Resulting P	roducts		
1734	110	5037691	06-A KP-1991	23-AU2+1707	Granted	
Title:	Reinforced Plan	stic Laminates for U	Jse in the Producti	on of Printed Cite	epit Boards and	
	Process for Ma	king Such Laminat	es and Resulting P	roducts		
			10.0 1007	10-Sep-1987	Granted	
1735	JP	2129158	10-Sep-1997	rv-ocp-176/ Resin Laminates		
Title:	: Printed Circuit	Board Made of Re	iniorced Symmetic	ACGINI Danning		
1736	JP	2138554	04-Oct-1995	10-Sep-1987	Granted	
1/30 Title	ur ∙ Method for M:	aking Reinforced Pl	astic Laminates fo	r Use in Producti	on of Circuit Boards	
4 1716	. 14100100 101 111	767777) 4 A DT_ WW (1114761141767	Otatica	
	T T1	2637378	Co tape +			
	T T1	2037376 aking Reinforced Pi	lastic Laminates fo	r Use în Producti	on of Circuit Boards	
1736 Title	JP : Method for Ma	aking Reinforced P	lastic Laminates fo	r Use în Productî 15-Sep-1986	on of Circuit Boards Granted	
1736 Title	JP : Method for Ma	aking Reinforced P	lastic Laminates fo	r Use în Productî 15-Sep-1986	on of Circuit Boards Granted on of Circuit Boards	
1736 Title 1736 Title	JP : Method for Ma US : Method for Ma	aking Reinforced P 4943334 aking Reinforced P	lastic Laminates fo 24-Jul-1990 lastic Laminates fo	r Use in Producti 15-Sep-1986 Ir Use in Producti	on of Circuit Boards	
1736 Title 1736 Title	JP : Method for Ma US : Method for Ma	aking Reinforced P 4943334 aking Reinforced P	lastic Laminates fo 24-Jul-1990 lastic Laminates fo 26-Dec-1995	r Use in Producti 15-Sep-1986 or Use in Producti 28-Sep-1993	Oldition.	
1736 Title 1736 Title	JP : Method for Ma US : Method for Ma	aking Reinforced P 4943334 aking Reinforced P	lastic Laminates fo 24-Jul-1990 lastic Laminates fo 26-Dec-1995	r Use in Producti 15-Sep-1986 or Use in Producti 28-Sep-1993	on of Circuit Boards	
1736 Title 1736 Title 1740 Title	JP : Method for Ma US : Method for Ma US : Method for M	aking Reinforced P: 4943334 aking Reinforced P 5478421 aking Composite S:	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004	r Use in Producti 15-Sep-1986 or Use in Producti 28-Sep-1993 ent Winding	on of Circuit Boards Granted Granted	
1736 Title 1736 Title 1740 Title	JP : Method for Ma US : Method for Ma US : Method for M	aking Reinforced P 4943334 aking Reinforced P	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004	r Use in Producti 15-Sep-1986 or Use in Producti 28-Sep-1993 ent Winding	on of Circuit Boards Granted Granted	
1736 Title 1736 Title 1740 Title	JP : Method for Ma US : Method for Ma US : Method for M	aking Reinforced P 4943334 aking Reinforced P 5478421 aking Composite S 6786771 on System With Imp	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004 proved High Frequency	r Use in Producti 15-Sep-1986 or Use in Producti 28-Sep-1993 ent Winding 20-Dec-2002 ency Performanc	on of Circuit Boards Granted Granted	
1736 Title 1736 Title 1740 Title	JP : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US	aking Reinforced P: 4943334 aking Reinforced P 5478421 aking Composite S: 6786771 an System With Imp	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004 proved High Freque 17-Aug-2004	r Use in Producti 15-Sep-1986 or Use in Producti 28-Sep-1993 ent Winding 20-Dec-2002 ency Performanc	on of Circuit Boards Granted Granted	
1736 Title 1736 Title 1740 Title	JP : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US	aking Reinforced P 4943334 aking Reinforced P 5478421 aking Composite S 6786771 on System With Imp	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004 proved High Freque 17-Aug-2004	r Use in Producti 15-Sep-1986 or Use in Producti 28-Sep-1993 ent Winding 20-Dec-2002 ency Performanc	on of Circuit Boards Granted Granted	
1736 Title 1736 Title 1740 Title 1757 Title	JP : Method for Market US :: Method for Market US :: Method for Market US :: Interconnection US :: LATCH AND	aking Reinforced P: 4943334 aking Reinforced P 5478421 aking Composite S: 6786771 on System With Imp 6776645 CRELEASE SYST	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004 proved High Freque 17-Aug-2004 EM FOR A CONN	r Use in Producti 15-Sep-1986 or Use in Producti 28-Sep-1993 ent Winding 20-Dec-2002 ency Performanc	on of Circuit Boards Granted Granted	
1736 Title 1736 Title 1740 Title 1757 Title 1766 Title	JP : Method for Market US : Method for Market US : Method for Market US : Interconnection US : LATCH AND	aking Reinforced P: 4943334 aking Reinforced P 5478421 aking Composite S: 6786771 an System With Imp	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004 proved High Freque 17-Aug-2004 EM FOR A CONN 17-Aug-2004	r Use in Producti 15-Sep-1986 r Use in Producti 28-Sep-1993 ent Winding 20-Dec-2002 ency Performanc 20-Dec-2002 IECTOR	on of Circuit Boards Granted Granted Granted	
1736 Title 1736 Title 1740 Title 1757 Title 1766 Title	JP : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Interconnection US : LATCH ANE US : High Speed, 1	aking Reinforced P: 4943334 aking Reinforced P 5478421 aking Composite S: 6786771 an System With Imp 6776645 a RELEASE SYST 6776659 High Density Electronic	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004 proved High Freque 17-Aug-2004 EM FOR A CONN 17-Aug-2004 rical Connector	r Use in Producti 15-Sep-1986 r Use in Producti 28-Sep-1993 ent Winding 20-Dec-2002 ency Performanc 20-Dec-2002 IECTOR	Granted Granted Granted Granted	
1736 Title 1736 Title 1740 Title 1757 Title 1766 Title	JP : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Method for Ma US : Interconnection US e: LATCH AND US e: High Speed, 1	aking Reinforced P: 4943334 aking Reinforced P 5478421 aking Composite S: 6786771 an System With Imp 6776645 a RELEASE SYST	lastic Laminates for 24-Jul-1990 lastic Laminates for 26-Dec-1995 tructures by Filame 07-Sep-2004 proved High Freque 17-Aug-2004 EM FOR A CONN 17-Aug-2004 rical Connector 07-Dec-2004	r Use in Producti 15-Sep-1986 r Use in Producti 28-Sep-1993 ent Winding 20-Dec-2002 ency Performanc 20-Dec-2002 IECTOR	Granted Granted Granted Granted	

Case #	Country	Patent #	Issue Date	Filing Date	Status	
1824	US	6814619	09-Nov-2004	26-Jun-2003	Granted	
Title:	High Speed, Hig	h Density Electrica	al Connector And	Connector Assen	ably	
1825 Title:	US High Speed, Hig	6780059 th Density Electric	24-Aug-2004 al Connector	26-Jun-2003	Granted	
i 849	US	6872085	29-Mar-2005	29-Sep-2003	Granted	
Title:	HIGH SPEED,	HIGH DENSITY E	LECTRICAL CO	INNECTOR ASS	EMBLY	

3996376v2

Patent Applications:

Case # Country

Filing Date Status

05-2021 US

60/691,423 17-Jun-2005 Pending

Title:

A COMPUTER SYSTEM FOR MODELING LOSS ACROSS A BACKPLANE AND

AN ASSOCIATED METHOD OF USE

1011 JΡ 521651/96 04-Dec-1995 Published

Title:

PRINTED

CIRCUIT BOARD CONNECTORS

1056 JР

97/534383 06-Feb-1997 Published

05-Aug-1999 Pending

Title:

Electrical Connector Assembled From Wafers

2280173 1091 ĊA

High

Title:

Speed, High Density Electrical Connector

High

534683/98 09-Aug-1999 Published

Title:

Speed, High Density Electrical Connector

1999-7007149 06-Aug-1999Granted (Pat #

517158)

Title:

High

Speed, High Density Electrical Connector

CA 1103

2280174 15-Jan-1998 Pending

HIGH

Title:

SPEED, HIGH DENSITY ELECTRICAL CONNECTOR

JΡ

KR

CA

534510/98 09-Aug-1999 Published

1103

HIGH

Title:

SPEED, HIGH DENSITY ELECTRICAL CONNECTOR

06-Aug-1999 Published

1103

1999-7007150

HIGH

Title:

SPEED, HIGH DENSITY ELECTRICAL CONNECTOR

1151

30-May-2001 Pending 2352820

Printed

Title:

Circuit Board and Method for Fabricating Such Board

99962969.4 02-Dec-1999 Granted (Pat # 1138180)

1151 Title:

Printed

3996376v2

Circuit Board and Method for Fabricating Such Board
1151 JP 2000-586144 04-Jun-2001 Published
Title:

Printed

Circuit Board and Method for Fabricating Such Board

1154 EP	04000082.0 06-Jan-2004 Published
Title:	
ELECTRICAL CONNECTOR	
1154 JP	2000-584560 24-May-2001 Published
Title:	
ELECTRICAL CONNECTOR	

Case # Co	ountry		Filing Date	Status		
1220 Title:	CA	2392322	22-May-2002 Differential	Pending Signal	Electrical	Connectors
1220 Title:	CA	2461037	24-Nov-1999 Differential	Pending Signal	Electrical	Connectors
1220 Title:	CN	99817015.1	23-May-2002 Differential	Publish Signal	ed Electrical	Connectors
1220 Title:	C74	0410033454	.5 Differential	08-Apr Signal	2004 Publis Electrical	hed Connectors
1220 Title:	EÞ	99962856.3	3 24-Nov-1999 Differential	Publish Signal	ned Electrical	Connectors
1220 Title:	EP	04002034-	24-Nov-1999 Differential	Publis L Signal	ed Electrical	Connectors
1220 Title:	EP	04028406.	9 01-Dec-2004 Differentia	Publish L Signal	hed L Electrical	. Connectors
1220 Title:	JP	2001-5408	92 23-May-2002 Differentia	Publis 1 Signa	hed l Electrical	Connectors
1220 Title:	JP	2004-5619	o 01-Mar-2004 Differentia	Pendin l Signa	g l Electrica:	l Connectors
1220 Title:	KR	2002-7006	695 Differentia	24-May l Signa	-2002 Publi l Electrica	shed l Connectors
1220 Title:	KR	2004-7008	353 Differentia	31-May Signa	-2004 Pend l Electrica	ing l Connectors

1220 Title:	мх	2002/005163 24-Nov-1999 Pending Differential Signal Electrical Connectors
1220 Title:	мх	2004/003001 24-Nov-1999 Pending Differential Signal Electrical Connectors

1221 Title:	CA	2377396 21-Jun-2000 Pending Modular Electrical Connector and Connector System
1221 11/30/05) Title:	EP	00943009.1 21-Jun-2000 Published (Will grant on Modular Electrical Connector and Connector System
Title:		MODICIAL STOCKLES COMME

1227 JF	2000-584561 24-May-2001 Published
Title:	Electrical Connector with Impedance Balancing Sections

1325 J Title:	P 2001-557119 05-Aug-2002 Pending Differential Signal Electrical Connector

1326 Title:	EP	03029590.1 23-Dec-2003 Published HIGH SPEED PRESSURE MOUNT CONNECTOR
1326 Title:	JÞ	2001-557118 05-Aug-2002 Pending HIGH SPEED PRESSURE MOUNT CONNECTOR

Case #	Country	Filing Date	Status

1327 Title:	CA	2399960	31-Jul-2002 Pending CONNECTOR WITH EGG-CRATE SHIELDING
1327 Title:	CN	01804535.9	05-Aug-2002 Published CONNECTOR WITH EGG-CRATE SHIELDING
1327 Title:	IL	151055	01-Aug-2002 Pending CONNECTOR WITH EGG-CRATE SHIELDING

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1327 Title:	JP	2001-557116 05-Aug	1-2002 Pending CONNECTOR WITH EGG-CRATE SHIELDING
1327 Title:	KR	2002-7010001	02-Aug-2002 Published CONNECTOR WITH EGG-CRATE SHIELDING
1327 Title:	мх	2002/007546 05-Aug	g-2002 Pending CONNECTOR WITH EGG-CRATE SHIELDING

1394	CA		2431346	16-Jun-2003 Pending	Methods
Title:				_	Mermods
and Appa	ratus	for	Controlling Access	to An Optical Interface	
1394	CN		01821104.6	19-Dec-2001 Published	Methods
Title:					Mectiogs
and Appa	ratus	for	Controlling Access	to An Optical Interface	
1394	EP		00991358.1	19-Dec-2001 Pending	Methods
Title:					Wettoos
and Appa	ratus	for	Controlling Access	to An Optical Interface	
1394	J₽		2002-551629	9 17-Jun-2003 Published	
Title:					Methods
and Appa	ratus	for	Controlling Access	to An Optical Interface	
1394	MX		2003/005541	l 19-Dec-2001 Published	_
Title:			-		Methods
and Appa	ratus	for	Controlling Access	to An Optical Interface	
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			01813871.3 08-Feb-2003 Published
1405	ÇN		010130/1.1
Title:			W. 55 a
Electro	-Optic	Connector	MODULE
1405	Eb		01952858.7 20-Jul-2001 Published
Title:			•
Electro	-Optic	Connector	Module
1405	JP		2002-517268 07-Feb-2003 Published
Title:			
	-Optic	Connector	Module
Di-Court	. .		<u></u>

1410	JР	2002-561992 30-Jan-2002 Published
Title: Techni	ques for	Selectively Exposing and Protecting An Optical Interface
Using F	ilm MX	2003/006844 31-Jul-2003 Published
	iques for	Selectively Exposing and Protecting An Optical Interface
Using F	7ilm	

1437	CA	2435759	23-Jan-2002 Pending

Title: Connector Molding Method and Shielded Waferized Connector Made Therefrom 02804171.2 25-Jan-2002 Published ÇN 1437 Title: Connector Molding Method and Shielded Waferized Connector Made Therefrom 02720835.4 23-Jan-2002 Published 1437 Title: Connector Molding Method and Shielded Waferized Connector Made Therefrom 2002-560236 23-Jan-2002 Published 1437 Title: Connector Molding Method and Shielded Waferized Connector Made Therefrom 2003/006690 25-Jul-2003 Published Title: Connector Molding Method and Shielded Waferized Connector Made Therefrom

Case #	Country	Filing Date Status	
1477 Titl	CA	2469301 29-Aug-2002 Pending Optica	il.
Connec	ctor Ferrule De Mating Misalie CN	esigned to Minimize Manufacturing Imperfections and gnments by Incorporating Exact Constraint Principles 02821606.7 29-Aug-2002 Pending Optice	a.]
Connec	ctor Ferrule D Mating Misali EP	esigned to Minimize Manufacturing Imperfections and gnments by Incorporating Exact Constraint Principles 02766158.6 29-Aug-2002 Published Optice	al
1477	ctor Ferrule D Mating Misali JP	esigned to Minimize Manufacturing Imperfections and gnments by Incorporating Exact Constraint Principles 2003-525350 01-Mar-2004 Pending Optics	al
Conne	star Parrula D	Designed to Minimize Manufacturing Imperfections and ignments by Incorporating Exact Constraint Principles	

1482 CN	02804169.0 23-Jan-2002 Pending
Title:	
Waferized Power	02705906.2 23-Jan-2002 Pending
1482 EP	02/05906.2 25-dan-4002 10
Waferized Power	Connector
1482 JP	2002-560238 23-Jan-2002 Published
Title:	
Waferized Power	Connector
1482 MX	2003/006691 25-Jul-2003 Published
Title:	
Waferized Power	Connector

1485 CN Title:	02805724.4 03	l-Feb-2002 Published	Matrix
Connector			

1485 JP Title:	2002-561325 01-Feb-2002 Published	Matrix
Connector 1485 MX Title:	2003/006895 01-Feb-2002 Pending	Matrix
Connector		
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1486 Title:	CN	· • · · · · · · · · · · · · · · · · · ·	02804622.6 05-Feb-2002 Pending	High-
Density 1486 Title:	Fiber EP	Optic	Backplane 02702147.6 05-Feb-2002 Pending	нigh-
Density 1486 Title:	Fiber JP	Optic	Backplane 2002-563051 05-Feb-2002 Published	High-
Density	Fiber	Optic	Backplane	

1489 TW	91135020 03-Dec-2002 Published	High
Title: speed Multi-Layer Printed	l Circuit Board VIA	_

1524	CA		2455903	29-Aug-2002 Pending
Title: Waferi: 1524	zed Fiber CN	Optic	Connector 02816889.5	27-Feb-2004 Published
Title: Waferi: 1524	zed Fiber EP	Optic	Connector 02768 7 56.5	29-Aug-2002 Published
Title: Waferi	zed Fiber	Optic	Connector	

Case #	Country		Filing Date	Status	
1524	ਦੁਹ		2003-525351 27-Feb-200	4 Published	
1524	ized Fiber KR	Optic	Connector 2004-7002967	27-Feb-2004	Pending
Title Wafer	e: rized Fiber	Optic	Connector		

1618 Title:	EP	03814958-9 24-Dec-2003 Published System and Method for Inspecting an Optical Interface
1618 Title:	US	10/329881 26-Dec-2002 Published System and Method for Inspecting an Optical Interface
1		

1636	CN		03800362.7 01-Dec-2003 Published	Matrix
1636	with EP	Integrated	Power Contacts 03716816.8 25-Mar-2003 Published	Matrix
1636	with JP	Integrated	Power Contacts 2003-582862 29-Sep-2004 Pending	Matrix
Title: Connector	with	Integrated	Power Contacts	

1684 EP	03814022.4 17-Dec-2003 Published
Title: Electrical	Connector with Conductive Plastic Features

1693 Title:	EP	03814856.5 18-Dec-2003 Published Systems and Methods for Inspection An Optical Interface
1693 Title:	US	10/329881 26-Dec-2002 Pending Systems and Methods for Inspection An Optical Interface

1	03821632.9 11-Mar-2005 Published Uniques for Connecting A Set of Connecting Elements Using Ar
Improved Latching App 1719 EP Title: Tec Improved	paratus 03764595.9 15-Jul-2003 Published Thniques for Connecting A Set of Connecting Elements Using Ar
Latching App	2004-521766 11-Mar-2005 Pending chniques for Connecting A Set of Connecting Elements Using A

1721	CA	2459290	29-Aug-2002 Pending	Modular
Title Fiber (tion System		¥
	_		<u></u>	

Case #	Country
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Filing Date Status

					"
1721 Title	CN 	02819348.2	31-Mar-2004	Published	Modular
	= :	Cuetom			
1	Optic Connection	placem	29-Aug-2002	Published	
1721	EP	02/9///9.2	29-Aug-2004	I Character	Modular
Title					
Fiber	Optic Connection	System			
1721	JP	2003-525349	01-Mar-2004	Pending	14 + A - 7 mag
Titl	e;				Modular
Fiber	Optic Connection	System			
1721	US	10/195960	16-Jul-2002	Published	
Titl		·			Modular
	Optic Connection	System			
	- 	•			
1					
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ſ	1742	EP	03800022.0 20-Dec-2003 Pending	
ŀ	Title:	FERRULE	E ASSEMBLY AND METHODS THEREFOR	
ı	1742	ŲS	10/324816 20-Dec-2002 Published	
ı	Ti	tle: FF	ERRULE ASSEMBLY AND METHODS THEREFOR	

1757 EP	03814231.1 20-Dec-2003 Pending	
Title: Interconnection Syst	em With Improved High Frequency Performance .	

1769	US	10/952103	28-Sep-2004 Pending
	~~		Star Routed Backplane
Title:			- · · · · · · · · · · · · · · · · · · ·
1769	WO	US05/34494	26-Sep-2005 Pending
1103	· · · ·		
Title:	Star Routed B	ackplane	

1772 EP 03783463.7 13-Nov-2003 Published Title: CONNECTOR AND PRINTED CIRCUIT BOARD FOR REDUCING CROSS-TALK		-	1
THE POST OF THE PO			ŀ
Title: CONNECTOR AND PRIMIAD CIRCUIT DOLLE FOR THE TOTAL CONNECTOR AND PRIMIAD CIRCUIT	NAME OF TAKE OF THE PROPERTY BOARD FOR REDUCING CROSS-TALK		ŀ
	NAMECTOR AND PRINTED CIRCUIT DOTAIN TOTAL	_	٠,

									"
1774	បទ		10/422515	24-Apr	-2003 Pendin	g			Printed
			Undesirable	Signal	Reflections	in	a Via	and	I 4 111000
M∈	thods WO	Therefor	US04/012704	23-Apr	-2004 Publis	hed			Printed
			Undesirable	Signal	Reflections	s in	a Via	and	
Me	thods	Therefor							

1783 Title:	US	10/954865	30-Sep-2004 Pending Perpendicular Switch Card Architecture
1783	WO		28-Sep-2005 Pending
Title:	Perpendicular		Architecture

PATENT

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REEL: 023944 FRAME: 0878

1788	US 10/67277	9 26-Sep-2003 Published
Title:	Protective Covers for Fi	ber Optic Connector

1811	US	10/675647	03-Sep-2003 Published	High
Title	•			uran
Speed,	High Density	Electrical Connec	tor	
1811	₩O	US04/028956	03-Sep-2004 Published	High
Title				**** 311
Speed,	High Density	Electrical Connec	tor	

Case # Country

Filing Date Status

1815 Title:	US	Discharging	60/679 Energy	5217 During	27-Ar Hot	r-2005 Pe Plugging	for	Power	Using	Conductive
Plastic										

1819	US	60/639064	23-Dec-2004 Pending		
Title:	00	22, 444		BARE DIE	SOCKET
1819	US	11/053448	08-Feb-2005 Pending	•	
Title:		·		BARE DIE	SOCKET

1821 Title:	US	60/639041	23-Dec-2004 Pending ENVIRONMENTALLY	SEALED	CHIP	SOCKET
1821 T it	us le: Environmen	11/059492 TALLY SEALED C	08-Feb-2005 Pending HIP SOCKET			

824 WO	US04/020170 23-Jun-2004 Published
Title: High Speed	A, High Density Electrical Connector And Connector
Assembly	US04/020109 23-Jun-2004 Published
BR25 WO	ed, High Density Electrical Connector

1826	US	10/603048 24-Jun-2003 Published	Printed
Title:	Roard Fo	or High Speed, High Density Electrical Connector W	ith
I	mproved (Pross-Talk Minimization, Attenuation And Impedance	Mismatch
Charact 1826	erístics WO	US04/19846 23-Jun-2004 Published	
Titles		·	Printed
1	Ιt	or High Speed, High Density Electrical Connector W mproved Cross-Talk Minimization, Attenuation And D	mpedance
Mismatc	h Charac	teristics	

1.840	US	10/744328	23-Dec-2003 Pending
		100.00	

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Title:

SPEED CONNECTOR ASSEMBLY

1840

US04/039063 19-Nov-2004 Pending

Title:

HIGH SPEED CONNECTOR ASSEMBLY

TIS. 1855

10/741542 23-Dec-2003 Pending

Title:

Modular Rackmount Chiller

HIGH

1855

WO

US04/041964 13-Dec-2004 Pending

Title: Modular Rackmount Chiller

1856

US

10/774050 23-Dec-2003 Pending

Title:

Modular Fiber Optic Connection System

1857

US

US

US

10/745475 23-Dec-2003 Pending

Fiber Optic Adapter Designed To Accommodate Different Style Ferrules Title:

Case # Country

Filing Date

Status

1874 Title:

07-Jul-2004 Pending 10/886449

Atmospheric 2-Phase Electronics Cooling System

1882

11/115874

27-Apr-2005 Pending

Layer count reduction in backplane PCB

1886

Title:

10/882598 US

01-Jul-2004 Pending

Orthogonal optical edge connector

Title: WO 1886

US05/22487 24-Jun-2005 Pending

Orthogonal optical edge connector Title:

10/874837 23-Jun-2004 Pending

Title:

1891

Electrical Connector Incorporating Passive Circuit Elements

US05/22423 23-Jun-2005 Pending

Title:

US

Electrical Connector Incorporating Passive Circuit Elements

1892

ÜS

10/874669 23-Jun-2004 Pending

Methods

Title:

of Manufacturing An Electrical Connector Incorporating Passive Circuit

Elements

1898 US 10/959652 06-Oct-2004 Pending
Title: GbX 1.85mm x 1.85 mid-plane x-connect
1898 WO US05/ 06-Oct-2005 Pending
Title: GbX 1.85mm x 1.85 mid-plane x-connect

1902 US 10/881248 30-Jun-2004 Pending
Title: Connector System for Optical Wave Guides

1902 WO TBD 24-Jun-2005 Pending
Title: Connector System for Optical Wave Guides

1903 US 10/897638 23-Jul-2004 Pending
Title: Microwave Quality Reflect Standard To Improve In-Fixture Calibration

1903 WO US05/026075 22-Jul-2005 Pending
Title: Microwave Quality Reflect Standard To Improve In-Fixture Calibration

1913 US 60/638973 24-Dec-2004 Pending

Title:

MIDPLANE ESPECIALLY APPLICABLE TO AN ORTHOGONAL

ARCHITECTURE ELECTRONIC SYSTEM

1913 US 11/173927 01-Jul-2005 Pending

Title:

MIDPLANE ESPECIALLY APPLICABLE TO AN ORTHOGONAL ARCHITECTURE ELECTRONIC SYSTEM

Case # Country

Filing Date Status

1915 US 10/954441 30-Sep-2004 Pending
Title:
Cooling Shelf
1915 WO US05/34596 28-Sep-2005 Pending
Title: Modular Cooling Shelf

1918 US 10/955571 30-Sep-2004 Pending
Title: High Speed, High Density Electrical Connector

1918 WO US05/34605 28-Sep-2005 Pending
Title: High Speed, High Density Electrical Connector

1943 US 60/638971 24-Dec-2004 Pending Title:

DIFFERENTIAL ELECTRICAL CONNECTOR ASSEMBLY

US 1943

11/173926 01-Jul-2005 Pending

Title:

DIFFERENTIAL ELECTRICAL CONNECTOR ASSEMBLY

1952 Title: Archited	us tures	11/011838 1 Front-To-Back Air C	14-Dec-2004 Pending Cooling Architecture For Orth	ogonal Board
Í				

1973 Title:	US	11/220382 06-Sep-2005 Pending Bi-Directional Electrical Path for Contact End
05-2019 Title:	ຫຣ	60/695705 30-Jun-2005 Pending TRIPOLI
05-2019 Title:	ປຣ	11/183564 18-Jul-2005 Pending TRIPOLI

05-2028 Title:	VS	60/695264 30-Jun-2005 Pending CONDUCTIVE I	NSERTS

ļ	05-2029	US				5 Pending			l
Ì	Title:		CONNECTOR	MITH	OFFSET	DIFFERENTIAL	SIGNAL	CONDUCTORS	l
									J

Patent Disclosures:

Case No	Statu	s Title
05-2036	Open	EXTRACTION OF PHYSICALLY CONSISTENT, FREQUENCY-DEPENDENT DIELECTRIC PROPERTIES OF PCB TRANSMISSION LINES
05-2037	Open	LOSSY MATERIAL DISPOSED BETWEEN DIFFERENTIAL PAIRS OF SIGNAL CONDUCTORS IN AN INTERCONNECTION DEVICE
05-2040	Open	THICK FILM RESISTORS AND OTHER COMPONENTS INTEGRATED INTO A CONNECTOR
1919	Open	MATCHED IMPEDANCE PLATED THROUGH HOLE FOR THE INTERCONNECTION OF TWO CONNECTORS
1976	Open	STIFFENER-ORGANIZER FOR CONNECTOR TO IMPROVE WAFER POSITIONING & ATTACHMENT REFLOW
05-2002	Aut	horized SMT PIN WITH NOTCH/DIMPLE FOR SOLDERING AND ADHESIVE ATTACHMENT [Note: Awaiting Inventor signatures - will be filed as US provisional application on November 30, 2005 if inventors have not responded]
05-2003		Authorized FOLDED-DISK PIN FOR SMT SOLDER AND/OR COMPOUND ATTACHMENT [Note: Awaiting Inventor signatures - will be filed as US provisional application on November 30, 2005 if inventors have not responded]

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Patent Disclosures:

Case No	Status	Title
05-2036	Open	EXTRACTION OF PHYSICALLY CONSISTENT, FREQUENCY-DEPENDENT DIELECTRIC PROPERTIES OF PCB TRANSMISSION LINES
05- 2037	Open	LOSSY MATERIAL DISPOSED BETWEEN DIFFERENTIAL PAIRS OF SIGNAL CONDUCTORS IN AN INTERCONNECTION DEVICE
05-2040	Open	THICK FILM RESISTORS AND OTHER COMPONENTS INTEGRATED INTO A CONNECTOR
1919	Open	MATCHED IMPEDANCE PLATED THROUGH HOLE FOR THE INTERCONNECTION OF TWO CONNECTORS.
1976	Open	STIFFENER-ORGANIZER FOR CONNECTOR TO IMPROVE WAFER POSITIONING & ATTACHMENT REFLOW
05-2002	Authorized	SMT PIN WITH NOTCH/DIMPLE FOR SOLDERING AND ADHESIVE ATTACHMENT
05-2003	Authorized	FOLDED-DISK PIN FOR SMT SOLDER AND/OR COMPOUND ATTACHMENT

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Trademarks:

Mark	Class	Serial No.	Filing Date	Registration Number.	Registration Date	Status
CROSSBOW	IC 009	78682550	August 1, 2005	N/A	N/A	Pending
VENTURA	IC 009	78418881	May 14, 2004	N/A	<u>N/A</u>	Pending
APTERA	IC 009	78514820	November 10, 2004	N/A	N/A	Pending
DIFFERENTIAL CLEARANCE	IC 009	78297381	September 8, 2003	N/A	N/A	Pending
TECHNOLOGY	IC 009	78107484	February 7, 2002	2669292	December 31, 2002	Registered
NEXLEV GBX	IC 009	76474185	December 12, 2002	2779372	November 4, 2003	Registered
BRINGING BANDWIDTH TO THE GLOBAL	IC 009	75862605	December 2, 1999	2426683	February 6, 2001	Registered
NETWORK	IC 009	75383873	November 3, 1997	2423664	January 23, 2001	Registered
HDM PLUS	IC 009	74350016	January 21, 1993	1894556	May 16, 1995	Registered
· · · · · · · · · · · · · · · · · · ·	1C 009	74353156	January 21, 1993	1886552	March 28, 1995	Registered
HDM HIGH DENSITY PLUS (HD+)	IC 009	73688734	October 9, 1987	1494014	June 28, 1988	Registered

Service Marks:

Mark	Class	Serial No.	Filing Date	Registration Number	Registration Date	Status
DESIGNLINK	IC 042	78470448	August 19, 2004	N/A	N/A	Pending

SCHEDULE B

Intellectual Property Licensed from Buyer to Assignor

Granted Patents Re: DCT Technology

	Case #	Country	Patent #	Issue Date	Filing Date	Status	
1	1220	US D3	6607402	19-Aug-2003	08-Apr-2002	Granted	ļ
		Differential	Signal Electrical Conne	ctors			

Patent Applications Re: DCT Technology

Case #	Country		Filing Date	Status	
1220	CA	2392322	22-May-2002	Pending	ļ
Title:	Differential Signal Electrical CN	Connectors 0410033454.5	08-Apr-2004	Published	
Title:	Differential Signal Electrical EP	O4028406.9	01-Dec-2004	Published	
	Differential Signal Electrical	Connectors 2004-56190	01-Mar-2004	Pending	
Title:	Differential Signal Electrica KR	1 Connectors 2004-7008353	31-May-2004	Pending	
Title:	Differential Signal Electrica MX	2004/003001	24-Nov-1999	Pending	
Title:	Differential Signal Electrica	l Connectors			

Patent Applications Re: DESIGNLINK

	Case #	Country	Filing Date Status	٠ ٦
Ī	05 2021	HS	60/691423 17-Jun-2005 Pending	ľ
l	Title:	A COMPU	FER SYSTEM FOR MODELING LOSS ACROSS A BACKPLANE AND	ŀ
İ		AN ASSOC	TATED METHOD OF USE	ل

PATENT

REEL: 023944 FRAME: 0886

SCHEDULE C

Trademarks Licensed from Assignor to Buver Subject to Certain Conditions

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FAX TRANSMISSION
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Patent Number Inventor: Richard F. ROTH et al.
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FAX NUMBER: (571) 273-0140
FROM: BLANK ROME LLP
Minh-Quan K. Pham
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