

PATENT ASSIGNMENT

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NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
Brian Matthew Henderson	01/25/2010
Arvind Chandrasekaran	01/25/2010
RECEIVING PARTY DATA	
Name:	QUALCOMM Incorporated
Street Address:	5775 Morehouse Drive
Internal Address:	Patent Department
City:	San Diego
State/Country:	CALIFORNIA
Postal Code:	92121-1714
PROPERTY NUMBERS Total: 1	
Property Type	Number
Application Number:	12710586
CORRESPONDENCE DATA	
Fax Number:	(858)658-2502
<i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i>	
Phone:	858-658-5787
Email:	Patent.Docketing.US@qualcomm.com
Correspondent Name:	QUALCOMM Incorporated
Address Line 1:	5775 Morehouse Drive
Address Line 2:	Patent Department
Address Line 4:	San Diego, CALIFORNIA 92121-1714
ATTORNEY DOCKET NUMBER:	092084
NAME OF SUBMITTER:	Sam Talpalatsky
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PATENT
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ASSIGNMENT

Reducing Susceptibility to Electrostatic Discharge Damage during Die-To-Die Bonding for 3-D Packaged Integrated Circuits

TO WHOM IT MAY CONCERN:

For the sum of One Dollar and other valuable consideration to us in hand paid, receipt of which is hereby acknowledged, be it known that We/I,

1. Brian Matthew Henderson, having a mailing address of 5775 Morehouse Drive, San Diego, CA 92121; and a resident of Escondido, California.
2. Arvind Chandrasekaran, a citizen of India having a mailing address of 5775 Morehouse Drive, San Diego, CA 92121; and a resident of San Diego, California.

have sold, assigned and transferred and by these presents do sell, assign, transfer and set over unto QUALCOMM Incorporated, a Delaware corporation, having a place of business at 5775 Morehouse Drive, San Diego, California 92121-1714, its successors, legal representatives, or assigns, the entire right, title and interest in and to a certain invention relating to **Reducing Susceptibility to Electrostatic Discharge Damage during Die-To-Die Bonding for 3-D Packaged Integrated Circuits** by me devised and the application for United States Patent therefore filed in the United States Patent and Trademark Office on February 23, 2010, and assigned Serial No. 12/710,586, (and I hereby authorize and instruct **QUALCOMM Incorporated** to hereafter complete this Assignment by inserting the serial number and date in the space provided above when known), including, without limitation, all provisional patent applications, and all original and reissue patents granted thereof, and all divisions, continuations and continuations-in-part thereof, including the subject matter of any and all claims which may be obtained in every such patent, and all foreign rights to

ASSIGNMENT

Reducing Susceptibility to Electrostatic Discharge Damage during Die-To-Die Bonding for 3-D Packaged Integrated Circuits

said invention, and covenant that I have full right to do so, and agree that I will communicate to said corporation or its representatives all facts known to me respecting said invention, whenever requested, and testify in any legal proceedings, sign all lawful papers, make all rightful oaths and generally do everything possible to aid said corporation, its successors, assigns and nominees, to obtain and enforce proper patent protection for said invention in all countries.

The Commissioner of Patents and Trademarks is requested to issue the Letters Patent which may be granted for said invention or any part thereof unto said corporation in keeping with this Assignment.

Done at San Diego, California this 25 day of Jan, 2010.

Brian Matthew Henderson

Done at San Diego, California this 25 day of Jan, 2010.

Arvind Chandrasekaran