

# PATENT ASSIGNMENT

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<b>SUBMISSION TYPE:</b>	NEW ASSIGNMENT
<b>NATURE OF CONVEYANCE:</b>	ASSIGNMENT
<b>CONVEYING PARTY DATA</b>	
<b>Name</b>	<b>Execution Date</b>
Marvell Semiconductor, Inc.	03/03/2010
<b>RECEIVING PARTY DATA</b>	
<b>Name:</b>	Marvell International Ltd.
<b>Street Address:</b>	Argyle House, 41A Cedar Avenue
<b>City:</b>	Hamilton, Hm12
<b>State/Country:</b>	BERMUDA
<b>PROPERTY NUMBERS Total: 1</b>	
<b>Property Type</b>	<b>Number</b>
<b>Application Number:</b>	11736561
<b>CORRESPONDENCE DATA</b>	
<p><b>Fax Number:</b> (408)222-2755</p> <p><i>Correspondence will be sent via US Mail when the fax attempt is unsuccessful.</i></p> <p><b>Phone:</b> 408-222-2500</p> <p><b>Email:</b> hng@marvell.com</p> <p><b>Correspondent Name:</b> Horace Ng</p> <p><b>Address Line 1:</b> 5488 Marvell Lane</p> <p><b>Address Line 4:</b> Santa Clara, CALIFORNIA 95054</p>	
<b>ATTORNEY DOCKET NUMBER:</b>	MP1087
<b>NAME OF SUBMITTER:</b>	Horace Ng
<p><b>Total Attachments: 3</b></p> <p>source=MP1087_Assgn MSI-MIL (executed)#page1.tif</p> <p>source=MP1087_Assgn MSI-MIL (executed)#page2.tif</p> <p>source=MP1087_Assgn MSI-MIL (executed)#page3.tif</p>	

OP \$40.00 11736561

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**PATENT**  
**REEL: 024068 FRAME: 0803**

## ASSIGNMENT

### Corporate

Marvell Semiconductor, Inc., a California corporation, having a principal place of business at 5488 Marvell Lane, Santa Clara, CA 95054 (hereafter the "Assignor"), is the owner by respective Assignment of United States Patent Application(s) identified below (hereafter sometimes called the "Patent Application(s)").

Marvell International Ltd., a corporation of Bermuda, having a place of business at Argyle House, 41A Cedar Avenue, Hamilton, Hm12, Bermuda (hereafter the "Assignee"), desires to acquire all of the rights to the Patent Application(s) and all inventions described and claimed therein or entitled to the benefit thereof.

THEREFORE, in consideration of the sum of One Dollar (\$1.00) or equivalent and other good and valuable consideration paid to it, Assignor hereby sells and assigns to the Assignee the entire right, title and interest in and to the Patent Application(s) identified below, and all inventions described and claimed therein or entitled to the benefit thereof, in any and all Letters Patent therefor, and in any and all reissues, extensions, renewals, reexaminations, divisions and continuations of such applications or Letters Patent to the full end of the term or terms for which such Letters Patent issue, such entire right, title and interest to be held and enjoyed by the above-named Assignee the same as they would have been held and enjoyed by the Assignor had this Assignment and sale not been made.

<u>Marvell Semiconductor, Inc. Case No.</u>	<u>Application No.</u>	<u>Filing Date</u>	<u>Title</u>
MP1087	11/736,561	April 17, 2007	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND METHODS
MP1087WO	PCT/US07/09584	April 17, 2007	SHARED MEMORY MULTI VIDEO CHANNEL DISPLAY APPARATUS AND METHODS
MP1084PR	60/793,288	April 18, 2006	FLEXIBLE MODULAR ARCHITECTURE FOR A DIGITAL VIDEO PROCESSOR IC TO BE USED IN MULTIPLE VIDEO AND GRAPHICS APPLICATIONS
MP1085PR	60/793,276	April 18, 2006	AREA-EFFICIENT METHOD TO SAVE DDR BANDWIDTH REQUIREMENT ON A DIGITAL VIDEO PROCESSOR CHIP
MP1087PR	60/793,277	April 18, 2006	ADAPTIVE VIDEO CONVERTERS
MP1088PR	60/793,275	April 18, 2006	DUAL 3D ARCHITECTURE

By its undersigned representative, the Assignor agrees

- a. to execute all papers necessary in connection with the Patent Application(s) and any continuing, divisional, reissue, reexamination or corresponding application thereof and also to execute separate Assignment in connection with such application as the Assignee may deem necessary or expedient;
- b. to execute all papers necessary in connection with any interference which may be declared concerning the Patent Application(s) or any continuation, division, reissue or reexamination thereof and to cooperate with the Assignee in every way possible in obtaining evidence and going forward with such interference; and
- c. to perform all affirmative acts which may be necessary to obtain a grant of a valid United States patent to the Assignee on any of the Patent Application(s) and on any continuation, division, reissue or reexamination of any of the Patent Application.

The Assignor hereby covenants that it has the full right to convey the entire interest herein assigned, and that it has not executed, and will not execute, any agreement in conflict therewith.

Marvell Semiconductor, Inc., a California corporation, certifies that it is the Assignee of the entire right, title and interest in the Patent Application(s) identified above by virtue of Assignment from the inventors of the Patent Application(s) identified above. Assignment to be recorded in the Patent and Trademark Office as identified below.

<u>Marvell Semiconductor, Inc. Case No.</u>	<u>Application No.</u>	<u>Assignment Execution Date</u>
MP1087	11/736,561	<u>04/16/2007, 09/24/2007 and 11/12/2007</u>
MP1087WO	PCT/US07/09584	<u>04/16/2007, 09/24/2007 and 11/12/2007</u>
MP1084PR	60/793,288	<u>04/16/2007, 09/24/2007 and 11/12/2007</u>
MP1085PR	60/793,276	<u>04/16/2007, 09/24/2007 and 11/12/2007</u>
MP1087PR	60/793,277	<u>04/16/2007, 09/24/2007 and 11/12/2007</u>
MP1088PR	60/793,275	<u>04/16/2007, 09/24/2007 and 11/12/2007</u>

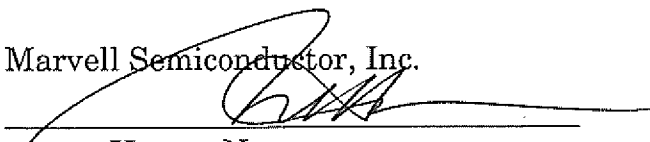
The undersigned has reviewed the documents in the Patent Application(s) identified above and, to the best of undersigned's knowledge and belief, title is in the Assignor identified above.

The undersigned is empowered to sign this certificate on behalf of the Assignor.

I hereby declare that all statements made herein of my own knowledge are true, and that all statements made on information and belief are believed to be true; and

further, that these statements are made with the knowledge that willful false statements, and the like so made, are punishable by fine or imprisonment, or both, under Section 1001, Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

IN WITNESS WHEREOF, executed by the Assignor's undersigned representative on the date following the undersigned's name.

By: Marvell Semiconductor, Inc.  
  
Horace Ng

Title: Managing IP Counsel and Director of Patent Procurement

Date March 3, 2010