PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
RPX Corporation	04/20/2010

RECEIVING PARTY DATA

Name:	Samsung Electronics Co., Ltd.
Street Address:	Maetan3-dong, Youngtonggu
City:	Suwon City, Kyunggido
State/Country:	KOREA, DEMOCRATIC PEOPLE'S REPUBLIC OF

PROPERTY NUMBERS Total: 17

Property Type	Number
Patent Number:	5450085
Patent Number:	5481690
Patent Number:	5489852
Patent Number:	5490257
Patent Number:	5495513
Patent Number:	5495530
Patent Number:	5546039
Patent Number:	5572587
Patent Number:	5615139
Patent Number:	5640393
Patent Number:	5696873
Patent Number:	5737767
Patent Number:	5745648
Patent Number:	5760612
Patent Number:	5771362
Patent Number:	5774836

CH $680.00 5450085

Patent Number:	5778337

CORRESPONDENCE DATA

Fax Number: (201)226-9246

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Correspondent Name: Steve Cha

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NAME OF SUBMITTER:	Steve Cha

Total Attachments: 16

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PATENT ASSIGNMENT AGREEMENT

This PATENT ASSIGNMENT AGREEMENT ("Agreement"), dated as of April 20, 2010 ("Effective Date"), is entered into by and between RPX Corporation, a Delaware corporation with a principal place of business at One Market Plaza, Steuart Tower, Suite 700, San Francisco, CA 94105 ("RPX") and Samsung Electronics Co., Ltd., a corporation duly organized and existing under the laws of Korea, having its principal place of business at Maetan-3dong, Youngtonggu, Suwon City, Kyunggido, Republic of Korea ("Samsung").

1. For good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, and subject to Saxon Innovations, LLC's retained rights to grant and enforce licenses and covenants not to sue with respect to certain entities set forth in Section 2, and subject to RPX's retained rights to grant and enforce licenses, releases and covenants not to sue with respect to Intel Corporation and any RPX licensee or sublicensee existing as of the effective date of that certain Patent Purchase, Assignment and License Agreement between RPX and Samsung (the "PPA"), RPX hereby irrevocably assigns to Samsung:

(1) the entire right, title, and interest, everywhere in the world, to
   (a) the issued patents set forth on Schedule 1,
   (b) any other rights in the inventions described in any of (a), (including rights to future patent applications and all rights of cooperation assigned or granted by a third party;
   (all of the foregoing in (1), collectively, the "Assigned Patents");

(2) the right to sue third parties for infringement (including but not limited to past, present and future infringement, damages and injunctive relief) of any of the Assigned Patents accruing based on activities occurring prior to the Effective Date hereof or hereafter (subject to Section 2 and excluding RPX's right to enforce any licenses it has granted and sublicenses it grants under the Assigned Patents); and

(3) any current or future right to receive royalties based on any of the foregoing in (1) or (2) (excluding (a) RPX's right to receive payments from any licensee or sublicensee (including from their successors and assigns) to which RPX has granted a license or sublicense under the Assigned Patents, (b) RPX's licensee's or sublicensee's rights to receive payments from any sublicensee to which RPX's licensees or sublicensees has granted a sublicense under the Assigned Patents, and (c) Saxon Innovations, LLC's right to receive payments from any licensee or sublicensee to which Saxon Innovations, LLC has granted a license or sublicense under the Assigned Patents);
   (all of the foregoing in (1), (2) and (3), collectively, the "Assigned Rights").

2. Notwithstanding the foregoing, this assignment to the Assigned Rights is granted by RPX subject to Saxon Innovations, LLC's retained exclusive right to grant and enforce licenses and covenants not to sue with respect to the Assigned Rights to one or more of the following entities: Casio Computer Co., Ltd., Sharp Corporation, Nintendo Co., Ltd and their affiliates.

3. This Agreement and the Assigned Rights assigned herein are subject to the terms and conditions of the PPA (as amended). To the extent there is a conflict between this Agreement and the PPA, the PPA shall control.

IN WITNESS WHEREOF, the parties hereto have caused this Agreement to be executed as of the Effective Date. Each individual signing below represents and warrants that he or she has authority to sign for and enter into this Agreement on behalf of his or her respective party.

Agreed to:	Agreed to:
SAMSUNG ELECTRONICS CO., LTD.	**RPX CORPORATION**
	Paul M. Sar
Name:_____	Name: Paul M. Saraceni
Title:_____	Title: Chief Intellectual Property Officer
Date:_____	Date: April 20, 2010

CALIFORNIA ALL-PURPOSE CERTIFICATE OF ACKNOWLEDGMENT

State of California

County of San Francisco

On April 20, 2010 before me, Courtney Gin, Notary Public,
(Here insert name and title of the officer)

personally appeared Paul Saraceni,

who proved to me on the basis of satisfactory evidence to be the person(s) whose name(s) is/are subscribed to the within instrument and acknowledged to me that he/she/they executed the same in his/her/their authorized capacity(ies), and that by his/her/their signature(s) on the instrument the person(s), or the entity upon behalf of which the person(s) acted, executed the instrument.

I certify under PENALTY OF PERJURY under the laws of the State of California that the foregoing paragraph is true and correct.

WITNESS my hand and official seal.

Signature of Notary Public

COURTNEY GIN
Commission # 1818950
Notary Public - California
Santa Clara County
My Comm. Expires Oct 21, 2012

(Notary Seal)

ADDITIONAL OPTIONAL INFORMATION

DESCRIPTION OF THE ATTACHED DOCUMENT

(Title or description of attached document)

(Title or description of attached document continued)

Number of Pages _____ Document Date _____

(Additional information)

CAPACITY CLAIMED BY THE SIGNER

- ☐ Individual (s)
- ☐ Corporate Officer

(Title)

- ☐ Partner(s)
- ☐ Attorney-in-Fact
- ☐ Trustee(s)
- ☐ Other _____

INSTRUCTIONS FOR COMPLETING THIS FORM

Any acknowledgment completed in California must contain verbiage exactly as appears above in the notary section or a separate acknowledgment form must be properly completed and attached to that document. The only exception is if a document is to be recorded outside of California. In such instances, any alternative acknowledgment verbiage as may be printed on such a document so long as the verbiage does not require the notary to do something that is illegal for a notary in California (i.e. certifying the authorized capacity of the signer). Please check the document carefully for proper notarial wording and attach this form if required.

- State and County information must be the State and County where the document signer(s) personally appeared before the notary public for acknowledgment.
- Date of notarization must be the date that the signer(s) personally appeared which must also be the same date the acknowledgment is completed.
- The notary public must print his or her name as it appears within his or her commission followed by a comma and then your title (notary public).
- Print the name(s) of document signer(s) who personally appear at the time of notarization.
- Indicate the correct singular or plural forms by crossing off incorrect forms (i.e. ~~he~~/she/~~they,~~ is ~~are~~) or circling the correct forms. Failure to correctly indicate this information may lead to rejection of document recording.
- The notary seal impression must be clear and photographically reproducible. Impression must not cover text or lines. If seal impression smudges, re-seal if a sufficient area permits, otherwise complete a different acknowledgment form.
- Signature of the notary public must match the signature on file with the office of the county clerk.
  - Additional information is not required but could help to ensure this acknowledgment is not misused or attached to a different document.
  - Indicate title or type of attached document, number of pages and date.
  - Indicate the capacity claimed by the signer. If the claimed capacity is a corporate officer, indicate the title (i.e. CEO, CFO, Secretary).
- Securely attach this document to the signed document

3. This Agreement and the Assigned Rights assigned herein are subject to the terms and conditions of the PPA (as amended). To the extent there is a conflict between this Agreement and the PPA, the PPA shall control.

IN WITNESS WHEREOF, the parties hereto have caused this Agreement to be executed as of the Effective Date. Each individual signing below represents and warrants that he or she has authority to sign for and enter into this Agreement on behalf of his or her respective party.

Agreed to:	Agreed to:
SAMSUNG ELECTRONICS CO., LTD.	**RPX CORPORATION**
Yongtae Lee	
Name: Yong Tae Lee	Name:
Title: Vice President	Title:
Date: April 29, 2010	Date:

SCHEDULE 1

ASSIGNED PATENTS

Part I

Subject to the Agreement, RPX assigns to Samsung all right, title and interest in and to the patents listed in this Part I.

Patent ID	Application	Country	Title	Issue Date
5,199,064	07/589,327	US	Fully-integrated telephone unit	30-Mar-93
5,266,950	07/942,359	US	Programmable keypad monitor	30-Nov-93
5,299,144	07/900,000	US	Architecture for covariance matrix generation	29-Mar-94
5,345,234	08/114,335	US	Method and apparatus for combining a flash analog to digital converter with digital to analog functions	6-Sep-94
5,968,162	08/626,473	US	Microprocessor configured to route instructions of a second instruction set to a second execute unit in response to an escape instruction	19-Oct-99
5,428,654	08/257,543	US	Up/down counter apparatus	27-Jun-95
6,130,936	08/993,789	US	System and method for terminating a telephone call after simulating a telephone connection failure	10-Oct-00
5,450,085	08/139,007	US	Method and apparatus for high speed analog to digital conversion using multiplexed flash sections	12-Sep-95
5,481,690	08/140,820	US	Power-efficient external memory access control using external memory enable time durations independent of external memory accessing rate	2-Jan-96

Patent ID	Application	Country	Title	Issue Date
5,489,852	07/973,089	US	System for interfacing wafer sort prober apparatus and packaged IC handler apparatus to a common test computer	6-Feb-96
5,490,257	07/840,024	US	RAM based FIFO memory half-full detection apparatus and method	6-Feb-96
5,495,513	08/343,990	US	Counter cell and counter circuit	27-Feb-96
5,495,530	08/320,778	US	Low power emergency telephone mode	27-Feb-96
5,546,039	08/423,059	US	Charge dissipation in capacitively loaded ports	13-Aug-96
5,930,820	08/617,412	US	Data cache and method using a stack memory for storing stack data separate from cache line storage	27-Jul-99
5,572,587	08/400,880	US	Telephone system and method for easing wait time in queue	5-Nov-96
5,615,139	08/217,309	US	Apparatus and method for synthesizing a sinusoidal signal	25-Mar-97
5,640,393	08/460,319	US	Multiple address security architecture	17-Jun-97
5,696,873	08/620,758	US	Vocoder system and method for performing pitch estimation using an adaptive correlation sample window	9-Dec-97
5,745,648	08/851,411	US	Apparatus and method for analyzing speech signals to determine parameters expressive of characteristics of the speech signals	28-Apr-98
5,760,612	08/696,087	US	Inertial delay circuit for eliminating glitches on a signal line	2-Jun-98
5,771,362	08/649,810	US	Processor having a bus interconnect which is dynamically reconfigurable in	23-Jun-98

Patent ID	Application	Country	Title	Issue Date
			response to an instruction field	
5,774,836	08/626,728	US	System and method for performing pitch estimation and error checking on low estimated pitch values in a correlation based pitch estimator	30-Jun-98
5,778,337	08/643,522	US	Dispersed impulse generator system and method for efficiently computing an excitation signal in a speech production model	7-Jul-98
5,781,053	08/701,016	US	Positive edge triggered flip flop	14-Jul-98
5,790,880	08/583,157	US	Microprocessor configured to dynamically connect processing elements according to data dependencies	4-Aug-98
5,794,021	08/483,619	US	Variable frequency clock generation circuit using aperiodic patterns	11-Aug-98
5,797,120	08/707,700	US	System and method for generating re-configurable band limited noise using modulation	18-Aug-98
5,812,619	08/608,165	US	Digital phase lock loop and system for digital clock recovery	22-Sep-98
5,829,028	08/643,343	US	Data cache configured to store data in a use-once manner	27-Oct-98
5,845,085	08/316,803	US	System for receiving a data stream of serialized data	1-Dec-98
5,886,539	08/835,935	US	Communication within an integrated circuit by data serialization through a metal plane	23-Mar-99
5,898,342	09/009,389	US	Power amplifier arrangement and method for data signal	27-Apr-99

Patent ID	Application	Country	Title	Issue Date
			interface	
5,930,489	08/599,617	US	Microprocessor configured to detect memory operations having data addresses indicative of a boundary between instructions sets	27-Jul-99
5,930,490	08/582,124	US	Microprocessor configured to switch instruction sets upon detection of a plurality of consecutive instructions	27-Jul-99
5,936,960	08/915,642	US	Apparatus for and method of communicating among devices interconnected on a bus	10-Aug-99
5,956,326	08/967,433	US	System and method for frequency division duplex/time division duplex radio frequency communication	21-Sep-99
6,021,314	08/861,963	US	Free channel selector for selecting an optimal channel	1-Feb-00
6,047,036	08/850,824	US	System and method for implementing a mute voice signal upon reception of a ADPCM zero nibble in wireless communications	4-Apr-00
6,055,300	08/987,538	US	System and method for forwarding a message left on a telephone by a caller to one or more telephone numbers	25-Apr-00
6,073,197	08/915,764	US	Apparatus for and method of communicating data among devices interconnected on a bus by using a signalling channel to set up communications	6-Jun-00

Patent ID	Application	Country	Title	Issue Date
6,075,795	09/286,994	US	Collision detection system for multiple stations in discrete multi-tone data communications network	13-Jun-00
6,092,094	08/633,352	US	Execute unit configured to selectably interpret an operand as multiple operands or as a single operand	18-Jul-00
6,104,217	08/822,975	US	Power on/off control circuit and method	15-Aug-00
6,111,885	08/915,563	US	Apparatus for and method of communicating data among devices interconnected on a bus	29-Aug-00
6,122,278	08/908,415	US	Circuit and method for protocol header decoding and packet routing	19-Sep-00
6,122,347	08/969,652	US	System and method for self-announcing a caller of an incoming telephone call	19-Sep-00
6,134,227	08/841,168	US	Secondary channel for radio frequency communications	17-Oct-00
6,154,820	08/886,190	US	Arrangement for storing program instructions and data in a memory device and method therefor	28-Nov-00
6,160,856	08/992,816	US	System for providing amplitude and phase modulation of line signals using delay lines	12-Dec-00
6,173,041	08/969,781	US	System and method for reducing call interruptions on a telephone	9-Jan-01
6,178,208	08/992,813	US	System for recovery of digital data from amplitude and phase modulated line signals	23-Jan-01

Patent ID	Application	Country	Title	Issue Date
			using delay lines	
6,195,782	09/086,098	US	MLSE implementation using a general purpose DSP and shared hardware for a GSM application	27-Feb-01
6,218,880	08/993,519	US	Analog delay line implemented with a digital delay line technique	17-Apr-01
6,226,368	09/082,644	US	System and method for automatically updating a clock using caller ID information	1-May-01
6,240,131	08/884,932	US	Digitally controlled transmission line equalizer	29-May-01
6,304,756	09/186,319	US	Channel arbitration between handset and base station in a cordless telephone system	16-Oct-01
6,412,055	09/106,880	US	Method and apparatus for product development	25-Jun-02
6,487,418	09/296,969	US	Free channel selection method	26-Nov-02
6,556,965	09/275,173	US	Wired and cordless telephone systems with extended frequency range	29-Apr-03
6,704,395	09/704,453	US	System and method for reducing call interruptions on a telephone	9-Mar-04
6,816,723	08/874,005	US	Telephony device with integrated messaging	9-Nov-04
6,865,375	09/256,543	US	Method and apparatus for locating a synchronization signal	8-Mar-05
6,978,009	08/699,844	US	Microprocessor-control led full-duplex speakerphone using automatic gain control	20-Dec-05
7,130,413	10/985,785	US	Microprocessor-control led full-duplex speakerphone using	31-Oct-06

Patent ID	Application	Country	Title	Issue Date
			automatic gain control	
5,737,767	08/555,081	US	System for reconfiguring the width of an xyram	7-Apr-98
6,728,546	09/384,309	US	Computer peripheral base station for a cordless telephone	27-Apr-04

Part II

Subject to the Agreement, RPX assigns to Samsung the full extent of RPX's rights in and to the patents listed in this Part II.

Patent ID	Application	Country	Title	Issue Date
0471448	91306387.1	EP	Programmable keypad monitor	
0471448	91306387.1	Great Britain	Programmable keypad monitor	
	190122/1991	Japan	Programmable keypad monitor	
69128851.8-08	471448	Germany	Programmable keypad monitor	
0471448	91306387.1	France	Programmable keypad monitor	
0575033	93302223.8	EP	Architecture for covariance matrix generation	
0575033	93302223.8	Great Britain	Architecture for covariance matrix generation	
3740516	143721/1993	Japan	Method and apparatus for covariance matrix generation	
69325618.4	575033	Germany	Architecture for covariance matrix generation	
0575033	93302223.8	France	Architecture for covariance matrix generation	
0691744	95303748.8	EP	Up/down counter apparatus	
0691744	95303748.8	Great Britain	Up/down counter apparatus	

Patent ID	Application	Country	Title	Issue Date
	141942/1995	Japan	Gray Code Up/down counter	
0691744	95303748.8	Spain	Up/down counter apparatus	
0641084	94305472.6	EP	Analog-digital-analog converter circuit	
0641084	94305472.6	Great Britain	Analog-digital-analog converter circuit	
	204857/1994	Japan	Converter circuit	
69428246.4-08	94305472.6	Germany	Analog-digital-analog converter circuit	
0472285	91306492.9	EP	External memory access control for a processing system	
0472285	91306492.9	Great Britain	External memory access control for a processing system	
3250821	201770/1991	Japan	External memory access control for a processing system	
69127873.3-08	0472285	Germany	External memory access control for a processing system	
0472285	91306492.9	France	External memory access control for a processing system	
0580340	93305461.1	EP	Low power emergency telephone mode	
0580340	93305461.1	Great Britain	Low power emergency telephone mode	
	153190/1993	Japan	Low power emergency telephone mode	
	93108777.5	China P.R.	Low power emergency telephone mode	
69317951.1	0580340	Germany	Low power emergency telephone mode	
	13661/1993	South Korea	Low power emergency telephone mode	
0736994	96301592.0	EP	Methodology to ease wait time on telephone through effective communication	
	051858/1996	Japan	Methodology to ease wait time on telephone through effective	

Patent ID	Application	Country	Title	Issue Date
			communication	
	6077/1996	South Korea	Methodology to ease wait time on telephone through effective communication	
	95301583.1	EP	Apparatus and method for synthesizing a sinusoidal signal	
	62929/1995	Japan	Apparatus and method for synthesizing a sinusoidal signal	
0830762	96911584.9	EP	Multiple address security architecture	
	536446/1996	Japan	Multiple address security architecture	
	PCT/US96/04663	PCT	Multiple address security architecture	
442763	708407/1997	South Korea	Multiple address security architecture	
	97903069.9	EP	Vocoder system and method for performing pitch estimation using an adaptive correlation sample window	
	PCT/US97/01049	PCT	Vocoder system and method for performing pitch estimation using an adaptive correlation sample window	
0860010	96936487.6	EP	System for reconfiguring the width of an x-y RAM	
0860010	96936487.6	Great Britain	System for reconfiguring the width of an x-y RAM	
	PCT/US96/16460	PCT	System for reconfiguring the width of an x-y RAM	
69603618.5	0860010	Germany	System for reconfiguring the width of an x-y RAM	
0860010	96936487.6	France	System for reconfiguring the width of an x-y RAM	
	95306470.6	EP	Apparatus and method for locating a plurality	

Patent ID	Application	Country	Title	Issue Date
			of roots of a line spectrum pair expression on unit circle	
	257700/1995	Japan	Apparatus and method for locating a plurality of roots of a line spectrum pair expression on unit circle	
	34100/1995	South Korea	Apparatus and method for locating a plurality of roots of a line spectrum pair expression on unit circle	
	PCT/US97/01044	PCT	Processor having a bus interconnect which is dynamically reconfigurable in response to an instruction field	
0871934	96942944.8	EP	Microprocessor configured to dynamically connect processing elements according to data dependencies	
	525205/1997	Japan	Microprocessor configured to dynamically connect processing elements according to data dependencies	
	PCT/US96/19655	PCT	Microprocessor configured to dynamically connect processing elements according to data dependencies	
0883947	96936629.3	EP	Digital phase lock loop and system for digital clock recovery	
0883947	96936629.3	Great Britain	Digital phase lock loop and system for digital clock recovery	
	530918/1997	Japan	Digital phase lock loop and system for digital	

Patent ID	Application	Country	Title	Issue Date
			clock recovery	
	PCT/US96/16645	PCT	Digital phase lock loop and system for digital clock recovery	
0602806	93309310.6	EP	High-level data link controller (HDLC) receiver	
0602806	93309310.6	Great Britain	High-level data link controller (HDLC) receiver	
	96944281.3	EP	Microprocessor configured to detect memory operations having data addresses indicative of a boundary between instructions sets	
	PCT/US96/19594	PCT	Microprocessor configured to detect memory operations having data addresses indicative of a boundary between instructions sets	
	96943770.6	EP	Microprocessor configured to switch instruction sets upon detection of a plurality of consecutive instructions	
	PCT/US96/20044	PCT	Microprocessor configured to switch instruction sets upon detection of a plurality of consecutive instructions	
	PCT/US97/01184	PCT	Execute unit configured to selectably interpret an operand as multiple operands or as a single operand	
	PCT/US98/10177	PCT	System and method for self-announcing a caller of an incoming telephone call	

Patent ID	Application	Country	Title	Issue Date
	98914335.9	EP	Secondary channel for radio frequency communications	
	546987/1998	Japan	Secondary channel for radio frequency communications	
	PCT/US98/06110	PCT	Secondary channel for radio frequency communications	
	98913230.3	EP	Telephony device with integrated messaging	
	502380/1999	Japan	Telephony device with integrated messaging	
	PCT/US98/06137	PCT	Telephony device with integrated messaging	
	PCT/US96/19587	PCT	Microprocessor configured to detect an escape instruction indicating a switch between instruction sets	
0888587	97904850.1	EP	Data cache and method using a stack memory for storing stack data separate from cache line storage	
	PCT/US97/01091	PCT	Data cache and method using a stack memory for storing stack data separate from cache line storage	