

PATENT ASSIGNMENT

Electronic Version v1.1
 Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT
CONVEYING PARTY DATA	
Name	Execution Date
TSI Telsys, Inc.	07/07/2000
RECEIVING PARTY DATA	
Name:	XILINX, INC.
Street Address:	2100 LOGIC DRIVE
City:	SAN JOSE
State/Country:	CALIFORNIA
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PROPERTY NUMBERS Total: 1	
Property Type	Number
Patent Number:	6625797
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ATTORNEY DOCKET NUMBER:	X-800 US
NAME OF SUBMITTER:	Susan Wiens
Total Attachments: 3 source=X_800_US_Assign_TSI_to_Xilinx#page1.tif source=X_800_US_Assign_TSI_to_Xilinx#page2.tif source=X_800_US_Assign_TSI_to_Xilinx#page3.tif	

CH \$40.00 6625797

U.S. PATENT APPLICATION ASSIGNMENT

This Patent Application Assignment (this "**Assignment**") is made as of July 7, 2000 by TSI Telsys, Inc., a Maryland corporation ("**Assignor**"), having a principal place of business at 7100 Columbia Gateway Drive, Columbia, Maryland 21046, to Xilinx, Inc., a Delaware corporation ("**Assignee**"), having a principal place of business at 2100 Logic Drive, San Jose, California 95124.

RECITALS

A. Assignor and Assignee have entered into an Asset Purchase Agreement of even date herewith (the "**Purchase Agreement**").

B. Pursuant to the Purchase Agreement, Assignor desires to assign to Assignee all of Assignor's right, title and interest in and to the patent applications filed with the United States Patent and Trademark Office and set forth on Exhibit A hereto (the "**Patent Applications**"), together with the goodwill of the business pertaining thereto.

AGREEMENT

NOW, THEREFORE, in consideration of the foregoing premises, the mutual covenants and agreements contained in the Purchase Agreement and the covenants and agreements in this Assignment and to induce Assignee to consummate the transactions contemplated by the Purchase Agreement, Assignor agrees as follows:

1. Assignor does hereby sell, transfer, convey, assign and deliver to Assignee all of Assignor's right, title and interest in and to the Patent Applications and any patents that may issue therefrom, including any foreign counterparts, divisions, continuations, or reissues of such patents, together with the goodwill of the business pertaining thereto, the same to be held by Assignee for Assignee's own use and enjoyment, and for the use and enjoyment of Assignee's successors, assigns and other legal representatives, as fully and entirely as the same would have been held and enjoyed by Assignor if this Assignment and sale had not been made, together with all claims for damages by reason of past infringements of the Patent Applications, along with the right to sue for and collect such damages for the use and benefit of Assignee and its successors, assigns and other legal representatives.

2. Assignor hereby authorizes and requests the Commissioner of Patents and Trademarks of the United States to issue the same to Assignee and its successors, assigns and other legal representatives in accordance with the terms of this instrument.

IN WITNESS WHEREOF, Assignor has executed this Assignment on the date first above written.

TSI TELSYS, INC.

By: 

Name: Toby D. Bennett

Title: Chief Executive Officer

Acknowledgment by Notary Public

State of Maryland

County of Howard

On this 7th day of July, 2000, before me, the undersigned Notary Public, personally appeared Toby D. Bennett, personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument, and acknowledged to me that he executed the same.

Seal:

Signature: Erin E. Eckert

ERIN E. ECKERT

NOTARY PUBLIC STATE OF MARYLAND

Name: My Commission Expires March 10, 2001, Notary Public

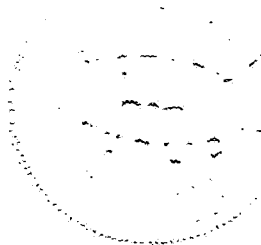


EXHIBIT A1. **SERIAL NUMBER: 09/013,186**

TITLE: System and Method for Programming Reconfigurable Processing Units

SUMMARY: This application describes a computing system that dynamically configures itself during execution based on real time processing requirements including reconfiguration of its hardware implementation. This comprises a system and method for developing and executing processing tasks using dynamically relocatable hardware objects.

BRIEF DESCRIPTION: A method and system for programming the hardware of field programmable gate arrays and related reconfiguration resources as if they were software by creating hardware objects that implement application level functionalities, operating system functionalities, and hardware functionalities. Controlling and executing the hardware objects via high level software constructs and managing the reconfigurable resources are optimized for the tasks currently being executed.

STATUS: Pending, with a request for restriction.

2. **SERIAL NUMBER: 09/501,319**

TITLE: A Means and Method for Compiling High Level Software Languages into Algorithmically Equivalent Hardware Representations

SUMMARY: In the field of electronic design automation and computer aided hardware design this is a computer based technique for compiling functional algorithmic descriptions written in a high level software language into efficient digital hardware implementations.

BRIEF DESCRIPTION: A compilation of a high level software-based description of an algorithm into efficient digital hardware implementation. A definition of new semantics for software constructs with respect to hardware implementations that allows working at a high level of abstraction while being able to infer the hardware implementation. With compilation tool that analyzes the software description the semantics are interpreted by generating control and data flow graphs. The graph is used as intermediate format for optimization which is then transferred into either a register transfer level or a net level description for the hardware implementation.

STATUS: Application, which was filed on February 10, 2000, is pending.