

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	ASSIGNMENT

CONVEYING PARTY DATA

Name	Execution Date
NCR Corporation	10/26/2009

RECEIVING PARTY DATA

Name:	Transpacific Digital Systems, LLC
Street Address:	2711 Centerville Road, Suite 400
City:	Wilmington
State/Country:	DELAWARE
Postal Code:	19808

PROPERTY NUMBERS Total: 4

Property Type	Number
Patent Number:	5109515
Patent Number:	5671384
Application Number:	07761538
Application Number:	07563214

CORRESPONDENCE DATA

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NAME OF SUBMITTER: Rebecca Tu

Total Attachments: 7
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ASSIGNMENT OF PATENT RIGHTS

Pursuant to the Patent Purchase Agreement and the First Amendment to Patent Purchase Agreement, effective July 20, 2009, between the hereinafter mentioned parties and for good and valuable consideration specified therein, NCR Corporation, a Maryland corporation, having an office at 1700 South Patterson Blvd., Dayton, Ohio 45479, U.S.A. (hereinafter referred to as the "Assignor"), does hereby sell, assign, transfer, and convey unto Transpacific Digital Systems, LLC, a Delaware limited liability company, having an office at 2711 Centerville Road, Suite 400, Wilmington, Delaware 19808, U.S.A. (hereinafter referred to as the "Assignee") all right, title, and interest, as of the date set forth below, that exist as of such date and may exist in the future, in and to any and all of the following (hereinafter referred to as the "Patent Rights"):

- (a) the patents and patent applications listed in the table below ("Patents");
- (b) any and all causes of action (whether currently pending, filed, or otherwise) and any and all other enforcement rights under the Patents, including the rights to (i) damages, (ii) injunctive relief, and (iii) any and all other remedies of any kind for past, current, and future infringement of the Patents;
- (c) any and all rights to collect royalties or other payments for infringement of the Patents, not previously settled by NCR Corporation;
- (d) any patent and patent application rights directed to and covering any inventions or the subject matter of any invention disclosures described in any of the Patents that, but only to the extent that any such inventions or the subject matter of any such invention disclosures, (i) are claimed and covered by any claim in the Patents, and/or (ii) are capable of being reduced to a patent claim filed and awarded in any reissue or reexamination proceedings brought with respect to any of the Patents; and
- (e) all rights to apply in any or all countries of the world for patents, certificates of invention, utility models, industrial design protections, design patent protections, or other governmental grants or issuances of any type related to any item in any of the foregoing categories (a) through (d), including, without limitation, under the Paris Convention for the Protection of Industrial Property, the International Patent Cooperation Treaty, or any other convention, treaty, agreement, or understanding.

Patent No. / Appln No.	Country	Title	Inventors	Filing Date
GB8924202.8	GB			10/27/1989
5,003,562	US	Digital Phase Lock Loop Decoder	Van Driest, Hans ; Van Bokhorst, Hendrik ; Kruithof, Richard	3/14/1990
CA2026323	CA	Digital Phase Lock Loop Decoder	Van Driest, Hans ; Van Bokhorst, Hendrik ; Kruithof, Richard	9/27/1990
JP2891535	JP	Digital Phase Lock Loop Decoder	Van Driest, Hans ; Van Bokhorst, Hendrik ; Kruithof, Richard	10/25/1990
EP0425302	EP	Digital Phase Lock Loop Decoder Suitable For Manchester Codes	Van Driest, Hans ; Van Bokhorst, Hendrik ; Kruithof, Richard	10/26/1990
DE69031205.9	DE	Digital Phase Lock Loop Decoder Suitable For Manchester Codes	Van Driest, Hans ; Van Bokhorst, Hendrik ; Kruithof, Richard	10/26/1990

Patent No. / Appin No.	Country	Title	Inventors	Filing Date
FR0425302	FR	Digital Phase Lock Loop Decoder Suitable For Manchester Codes	Van Driest, Hans ; Van Bokhorst, Hendrik ; Kruijthof, Richard	10/25/1990
GB0425302	GB	Digital Phase Lock Loop Decoder Suitable For Manchester Codes	Van Driest, Hans ; Van Bokhorst, Hendrik ; Kruijthof, Richard	10/26/1990
5,109,515	US	User And Application Program Transparent Resource Sharing Multiple Computer Interface Architecture With Kernel Process Level Transfer Of User Requested Services	Laggis, George E. ; Meyer, Paul F.	9/28/1987
PCT/US88/0002 13	WO	Distributed File Server Architecture	Laggis, George E. ; Meyer, Paul F.	1/26/1988
EP0370008	EP	Distributed File Server Architecture	Laggis, George E. ; Meyer, Paul F.	1/26/1988
DE3879947.2	DE	Distributed File Server Architecture	Laggis, George E. ; Meyer, Paul F.	1/26/1988
FR0370008	FR	Distributed File Server Architecture	Laggis, George E. ; Meyer, Paul F.	1/26/1988
GB0370008	GB	Distributed File Server Architecture	Laggis, George E. ; Meyer, Paul F.	1/26/1988
IT0370008	IT	Distributed File Server Architecture	Laggis, George E. ; Meyer, Paul F.	1/26/1988
NL0370008	NL	Distributed File Server Architecture	Laggis, George E. ; Meyer, Paul F.	1/26/1988
JP1785557	JP	Distributed File Server Architecture	Laggis, George E. ; Meyer, Paul F.	1/26/1988
CA1308200	CA	Computer Interface Architecture	Laggis, George E. ; Meyer, Paul F.	8/15/1988
5,360,948	US	Via Programming For Multichip Modules	Thornberg, Gary R.	8/14/1992
JP05-219196	JP	Method For Cross-Linking Programming For Multichip Module	Thornberg, Gary R.	8/12/1993
GB9018992.9	GB			8/31/1990
JP03-242428	JP	Internal Bus For Workstation Interfacing Device	King, Edward C. ; Goepfel, Anton	8/29/1991
5,363,492	US	Internal Bus For Work Station Interfacing Means	King, Edward C. ; Goepfel, Anton	8/30/1991
EP0474442	EP	Internal Bus For Work Station Interfacing Means	King, Edward C. ; Goepfel, Anton	8/30/1991
DE69128823.2	DE	Internal Bus For Work Station Interfacing Means	King, Edward C. ; Goepfel, Anton	8/30/1991
GB0474442	GB	Internal Bus For Work Station Interfacing Means	King, Edward C. ; Goepfel, Anton	8/30/1991

Patent No. / Appln No.	Country	Title	Inventors	Filing Date
07/761,538	US	Method And Apparatus For Transferring Data Within A Computer Using A Burst Sequence Which Includes Modified Bytes And A Minimum Number Of Unmodified Bytes	Young, Randolph G.	9/18/1991
5,386,540	US	Method And Apparatus For Transferring Data Within A Computer Using A Burst Sequence Which Includes Modified Bytes And A Minimum Number Of Unmodified Bytes	Young, Randolph G. ; Bradshaw, James L. ; Batchler, Bobby W. ; Sudduth, Barry C. ; Walrath, Craig A.	7/28/1993
5,392,407	US	Multi-Port Processor With Peripheral Component Interconnect Port And Rambus Port	Heil, Thomas F. ; Walrath, Craig A. ; Hawkey, Jeff A. ; Pike, Jim D.	12/24/1992
JP05-342704	JP	Multiport Processor Provided With Peripheral-Device Interconnection Port And With Rambus Port	Heil, Thomas F. ; Walrath, Craig A. ; Hawkey, Jeff A. ; Pike, Jim D.	12/16/1993
EP93310329.3	EP	A Computer System Interface	Heil, Thomas F. ; Walrath, Craig A. ; Hawkey, Jeff A. ; Pike, Jim D.	12/20/1993
GB9019001.8	GB			8/31/1990
5,428,751	US	Work Station Including A Direct Memory Access Controller And Interfacing Means To A Data Channel	Dollinger, Georg ; King, Edward C.	8/30/1991
EP0478148	EP	Work Station Including A Direct Memory Access Controller	Dollinger, Georg ; King, Edward C.	8/30/1991
DE69125876.4	DE	Work Station Including A Direct Memory Access Controller	Dollinger, Georg ; King, Edward C.	8/30/1991
GB0478148	GB	Work Station Including A Direct Memory Access Controller	Dollinger, Georg ; King, Edward C.	8/30/1991
JP3698324	JP	Work Station Having Direct Memory Access Controller And Interface Device For Data Channel	Dollinger, Georg ; King, Edward C.	8/29/1991
GB9019023.2	GB			8/31/1990
5,440,754	US	Work Station And Method For Transferring Data Between An External Bus And A Memory Unit	Goeppel, Anton ; King, Edward C.	8/30/1991
EP0473455	EP	Work Station Having Burst Mode Data Transfer	Goeppel, Anton ; King, Edward C.	8/30/1991
DE69128319.2	DE	Work Station Having Burst Mode Data Transfer	Goeppel, Anton ; King, Edward C.	8/30/1991
GB0473455	GB	Work Station Having Burst Mode Data Transfer	Goeppel, Anton ; King, Edward C.	8/30/1991
JP03-242430	JP	Workstation Provided With Multiplexed Mode And Burst Mode Capability	Goeppel, Anton ; King, Edward C.	8/29/1991

Patent No. / Appln No.	Country	Title	Inventors	Filing Date
5,502,824	US	Peripheral Component Interconnect "Always On" Protocol	Heil, Thomas F.	12/28/1992
5,528,764	US	Bus System With Cache Snooping Signals Having A Turnaround Time Between Agents Driving The Bus For Keeping The Bus From Floating For An Extended Period	Heil, Thomas F.	12/24/1992
5,712,986	US	Asynchronous PCI-To-PCI Bridge	Vo, Tri Tinh	12/19/1996
EP0780773	EP	Asynchronous Bus Bridge	Vo, Tri Tinh	12/12/1996
DE59622830.0	DE	Asynchronous Bus Bridge	Vo, Tri Tinh	12/12/1996
FR0780773	FR	Asynchronous Bus Bridge	Vo, Tri Tinh	12/12/1996
GB0780773	GB	Asynchronous Bus Bridge	Vo, Tri Tinh	12/12/1996
JP3705863	JP	Device Bridging Method And Device Therefor	Vo, Tri Tinh	12/17/1996
5,835,945	US	Memory System With Write Buffer, Prefetch And Internal Caches	King, Edward C. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L.	8/6/1990
EP0470736	EP	Cache Memory System	King, Edward C. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L.	7/26/1991
DE69131338.5	DE	Cache Memory System	King, Edward C. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L.	7/26/1991
GB0470736	GB	Cache Memory System	King, Edward C. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L.	7/26/1991
JP03-216622	JP	Computer Memory System	King, Edward C. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L.	8/2/1991
5,918,249	US	Promoting Local Memory Accessing And Data Migration In Non-Uniform Memory Access System Architecture	Cox, John S. ; Waters, John H.	12/19/1996
GB9019026.5	GB	Work station including a direct memory access controller	Goeppel, Anton	8/31/1990
JP03-242432	JP	Workstation having direct memory access controller	Goeppel, Anton	8/29/1991
EP91307982.8	EP	Work station including a direct memory access controller	Goeppel, Anton	8/30/1991

Patent No. / Appln No.	Country	Title	Inventors	Filing Date
5,671,384	US	Work station with a DMA controller having extended addressing capability	Goeppel, Anton	8/30/1991
GB9018991.1	GB	Work station architecture with selectable CPU	King, Edward C. ; Goeppel, Anton	8/31/1990
JP3550155	JP	Architecture of workstation having selectable central processing unit	King, Edward C. ; Goeppel, Anton	8/29/1991
EP0473453	EP	Work station having a selectable CPU	King, Edward C. ; Goeppel, Anton	8/30/1991
DE69125071.5	DE	Work station having a selectable CPU	King, Edward C. ; Goeppel, Anton	8/30/1991
GB0473453	GB	Work station having a selectable CPU	King, Edward C. ; Goeppel, Anton	8/30/1991
5,640,536	US	Work station architecture with selectable CPU	King, Edward C. ; Goeppel, Anton	8/30/1991
07/583,214	US	A computer memory system having programmable operational characteristics based on characteristics of a central processor	King, Edward C. ; Arnold, Forrest O. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L. ; Vermeer, Fulps V.	8/6/1990
EP0470735	EP	Computer memory system	King, Edward C. ; Arnold, Forrest O. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L. ; Vermeer, Fulps V.	7/26/1991
DE89130987.1	DE	Computer memory system having programmable operational characteristics based on characteristics of a central processor	King, Edward C. ; Arnold, Forrest O. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L. ; Vermeer, Fulps V.	7/26/1991
GB0470735	GB	Computer memory system	King, Edward C. ; Arnold, Forrest O. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L. ; Vermeer, Fulps V.	7/26/1991
JP03-216620	JP	Computer memory system	King, Edward C. ; Arnold, Forrest O. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Pirmin L. ; Vermeer, Fulps V.	8/2/1991
5,953,740	US	Computer memory system having programmable operational characteristics based on characteristics	King, Edward C. ;	10/5/1993

Patent No. / Appln No.	Country	Title	Inventors	Filing Date
		of a central processor	Arnold, Forrest O. ; Ellis, Jackson L. ; Moussavi, Robert B. ; Weisser, Firmin L. ; Vermeer, Fulps V.	

Assignor hereby authorizes the respective patent office or governmental agency in each jurisdiction to issue any and all patents, certificates of invention, utility models or other governmental grants or issuances that may be granted upon any of the Patent Rights in the name of Assignee, as the assignee to the entire interest therein.

The terms and conditions of this Assignment of Patent Rights will inure to the benefit of Assignee, its successors, and its assigns, and will be binding upon Assignor, its successors, and its assigns.

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Dayton, Ohio on October 26,
 , 2009.

ASSIGNOR

NCR Corporation

By: *Douglas S. Foote*

Name: Douglas S. Foote

Title: Law Vice President

(Signature MUST be notarized)

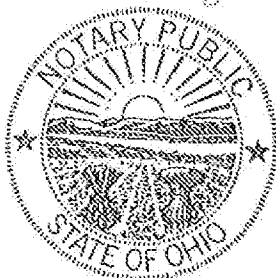
STATE OF OHIO)

COUNTY OF MONTGOMERY)

On October 26, 2009, before me, Shirley A. Byrd, Notary Public in and for said State, Douglas S. Foote personally known to me (or proved to me on the basis of satisfactory evidence) to be the person whose name is subscribed to the within instrument and acknowledged to me that he/she executed the same in his/her authorized capacity, and that by his/her signature on the instrument the person, or the entity upon behalf of which the person acted, executed the instrument.

WITNESS my hand and official seal.

Signature *Shirley A. Byrd* (Seal)



SHIRLEY A. BYRD, Notary Public
In and for the State of Ohio
My Commission Expires Oct. 6, 2012