

PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	Purchase Option Agreement
CONVEYING PARTY DATA	
Name	Execution Date
T-RAM Semiconductor, Inc.	05/07/2010
RECEIVING PARTY DATA	
Name:	Micron Technology, Inc.
Street Address:	8000 S. Federal Way
Internal Address:	MS 1-525
City:	Boise
State/Country:	IDAHO
Postal Code:	83716-9632
PROPERTY NUMBERS Total: 123	
Property Type	Number
Patent Number:	6462359
Patent Number:	6653175
Patent Number:	7075122
Patent Number:	7592642
Patent Number:	7491586
Patent Number:	7488626
Patent Number:	6891205
Application Number:	12367891
Patent Number:	6583452
Patent Number:	6611452
Patent Number:	6845037
Patent Number:	6653174
Patent Number:	6690038
Patent Number:	6666481

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PATENT
REEL: 024474 FRAME: 0979

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Patent Number:	7049182
Patent Number:	6683330
Patent Number:	6979602
Patent Number:	6686612
Patent Number:	6690039
Patent Number:	6703646
Patent Number:	6828176
Patent Number:	7053423
Patent Number:	6721220
Patent Number:	6958931
Patent Number:	6727528
Patent Number:	6777271
Patent Number:	7456439
Patent Number:	6835997
Patent Number:	7374974
Application Number:	12277290
Patent Number:	6734815
Patent Number:	6819278
Patent Number:	6975260
Patent Number:	6735113
Patent Number:	6756612
Patent Number:	6872602
Patent Number:	6756838
Patent Number:	6767770
Patent Number:	6828202
Patent Number:	6911680
Patent Number:	7125753
Patent Number:	6778435
Patent Number:	6940772
Patent Number:	6781888
Patent Number:	6901021
Patent Number:	7064977
Patent Number:	7123508
Patent Number:	6785169
Patent Number:	6790713

Patent Number:	6804162
Patent Number:	7236421
Patent Number:	6815734
Patent Number:	7015077
Patent Number:	6998652
Patent Number:	6818482
Patent Number:	7183591
Patent Number:	6885581
Patent Number:	7042759
Patent Number:	7405963
Patent Number:	6888176
Patent Number:	7279367
Application Number:	12182128
Application Number:	11906619
Patent Number:	6888177
Patent Number:	6891774
Patent Number:	6903987
Patent Number:	7006398
Patent Number:	7324394
Patent Number:	6913955
Patent Number:	6937085
Patent Number:	6944051
Patent Number:	7245525
Patent Number:	6947349
Patent Number:	6953953
Patent Number:	7351614
Patent Number:	6965129
Patent Number:	7320895
Patent Number:	6980457
Patent Number:	6998298
Patent Number:	7256430
Patent Number:	7030425
Patent Number:	7037763
Patent Number:	7054191
Patent Number:	7304327

Patent Number:	7078739
Patent Number:	7268373
Patent Number:	7488627
Application Number:	12368226
Patent Number:	7089439
Patent Number:	7096144
Patent Number:	7109532
Patent Number:	7489008
Application Number:	12368171
Patent Number:	7135745
Application Number:	11581316
Patent Number:	7157342
Patent Number:	7187530
Patent Number:	7195959
Application Number:	11728170
Patent Number:	7262443
Patent Number:	7316941
Patent Number:	7319622
Patent Number:	7326969
Patent Number:	7379381
Patent Number:	7381999
Patent Number:	7460395
Application Number:	12326027
Application Number:	11881049
Application Number:	11881159
Patent Number:	7464282
Patent Number:	7554130
Patent Number:	7573077
Application Number:	12538805
Patent Number:	7587643
Application Number:	11084343
Application Number:	12079548
Application Number:	11362285
Application Number:	12271758
Application Number:	11303237

Application Number:	12421399
Application Number:	12442152
Application Number:	12501995
Application Number:	12555665

CORRESPONDENCE DATA

Fax Number: (208)368-5606

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: (208) 368-4591

Email: tpaulin@micron.com

Correspondent Name: Tanra Paulin

Address Line 1: 8000 S. Federal Way

Address Line 2: MS 1-525

Address Line 4: Boise, IDAHO 83716-9632

ATTORNEY DOCKET NUMBER:	TRAM-0000
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NAME OF SUBMITTER:	Russell D. Slifer
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Total Attachments: 6

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TECHNOLOGY LICENSE AND TRANSFER AGREEMENT

This **TECHNOLOGY LICENSE AND TRANSFER AGREEMENT** ("Agreement") is effective as of and entered into on May 7, 2010 ("Effective Date") by and between **Micron Technology, Inc.**, a company duly incorporated under the laws of Delaware, and its Subsidiaries (as defined below) (collectively "Micron"), and **T-RAM Semiconductor, Inc.**, a company duly incorporated under the laws of the California, and its Subsidiaries (collectively "T-RAM") (Micron and T-RAM, each, a "Party" and collectively, the "Parties").

WHEREAS, T-RAM owns certain intellectual property and technology related to Thyristor-RAM semiconductor products; and

[REDACTED]

[REDACTED]

WHEREAS, Micron wishes to be granted an option to purchase such intellectual property and technology from T-RAM; and

WHEREAS, T-RAM is willing to grant such an option to Micron pursuant to the terms and conditions of this Agreement;

NOW, THEREFORE, in consideration of the premises contained herein, and of the obligations herein made and undertaken, the Parties hereto do hereby covenant and agree as follows:

1. DEFINITIONS; CERTAIN INTERPRETIVE MATTERS.

1.1 Definitions. For purposes of this Agreement, the definitions set forth in this Section 1 shall apply to the respective capitalized terms used herein.

[REDACTED]

(b) "Asset Purchase Agreement" shall mean a written asset purchase agreement entered into between Micron and T-RAM, containing representations, warranties and covenants typical for transactions of the type, which provides for the assignment and transfer by T-RAM to Micron of all right, title and interest in and to all T-RAM Intellectual Property (including T-RAM Patents) and Licensed Technology owned by T-RAM.

[REDACTED]

(k) "Licensed Technology" shall mean any and all Technology owned by T-RAM (and any and all Technology to which T-RAM has the right to grant licenses or sublicenses without the requirement of payment of any fees or other consideration to any Third Party(ies)), including without limitation the Technology listed in Exhibit A attached to this Agreement.

[REDACTED]

(o) "Patents" shall mean any and all United States and foreign patents and utility models, invention registrations, and applications therefor and all reissues, divisions, renewals, extensions, provisionals, continuations and continuations-in-part thereof.

[REDACTED]

(q) "Technology" shall mean any and all documentation, works of authorship, know-how, data and databases, formulae, algorithms, processes, inventions and discoveries (whether or not patentable), ideas, concepts, techniques, methods, content, technical information, engineering, designs, drawings, schematics, mask works, tooling requirements, and other information and technology.

[REDACTED]

[REDACTED]

(t) "T-RAM Intellectual Property" shall mean any and all Intellectual Property owned by T-RAM (and any and all Intellectual Property to which T-RAM has the right to grant licenses or sublicenses without the requirement of payment of any fees or other consideration to any Third Party(ies)) at any time during the term of this Agreement.

(u) "T-RAM Patents" shall mean any and all Patents owned by T-RAM (and any and all Patents which T-RAM has the right to grant licenses or sublicenses to without the requirement of payment of any fees or other consideration to any Third Party(ies)) at any time during the term of this Agreement.

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

8. TERM AND TERMINATION.

8.1 Term of Agreement. This Agreement shall become effective as of the Effective Date of this Agreement and shall remain in effect for two (2) years thereafter ("Term"), unless this Agreement is extended or earlier terminated in accordance with the terms of this Agreement.

[REDACTED]

[REDACTED]

[REDACTED]

IN WITNESS WHEREOF, this Agreement has been duly executed by and on behalf of each Party hereto through its duly authorized representative as of the Effective Date hereof.

MICRON TECHNOLOGY, INC.

T-RAM SEMICONDUCTOR, INC.

By: Dm a

By: Sam NAKIB

REVIEWED
MTI LEGAL

Name: Dermot M. Durcan

Name: SAM NAKIB

C.S.

Title: President and COO

Title: President & CEO