

PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT																														
NATURE OF CONVEYANCE:	Corrective Assignment to correct the Assignor on the Notice of Recordation previously recorded on Reel 024474 Frame 0569. Assignor(s) hereby confirms the correct Assignor is Teridian Semiconductor Holdings Corporation.																														
CONVEYING PARTY DATA																															
<table border="1"><tr><td>Name</td><td>Execution Date</td></tr><tr><td>Teridian Semiconductor Holdings Corporation</td><td>04/09/2010</td></tr></table>		Name	Execution Date	Teridian Semiconductor Holdings Corporation	04/09/2010																										
Name	Execution Date																														
Teridian Semiconductor Holdings Corporation	04/09/2010																														
RECEIVING PARTY DATA																															
<table border="1"><tr><td>Name:</td><td>Maxim Integrated Products, Inc.</td></tr><tr><td>Street Address:</td><td>120 San Gabriel Drive</td></tr><tr><td>City:</td><td>Sunnyvale</td></tr><tr><td>State/Country:</td><td>CALIFORNIA</td></tr><tr><td>Postal Code:</td><td>94086</td></tr></table>		Name:	Maxim Integrated Products, Inc.	Street Address:	120 San Gabriel Drive	City:	Sunnyvale	State/Country:	CALIFORNIA	Postal Code:	94086																				
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Street Address:	120 San Gabriel Drive																														
City:	Sunnyvale																														
State/Country:	CALIFORNIA																														
Postal Code:	94086																														
PROPERTY NUMBERS Total: 28																															
<table border="1"><thead><tr><th>Property Type</th><th>Number</th></tr></thead><tbody><tr><td>Application Number:</td><td>11480269</td></tr><tr><td>Patent Number:</td><td>7733965</td></tr><tr><td>Application Number:</td><td>12178547</td></tr><tr><td>Application Number:</td><td>11477136</td></tr><tr><td>Patent Number:</td><td>7724831</td></tr><tr><td>Patent Number:</td><td>5726650</td></tr><tr><td>Patent Number:</td><td>5093847</td></tr><tr><td>Patent Number:</td><td>7327754</td></tr><tr><td>Patent Number:</td><td>5168179</td></tr><tr><td>Patent Number:</td><td>5189664</td></tr><tr><td>Patent Number:</td><td>5533053</td></tr><tr><td>Patent Number:</td><td>7009603</td></tr><tr><td>Patent Number:</td><td>7158573</td></tr><tr><td>Patent Number:</td><td>7317732</td></tr></tbody></table>		Property Type	Number	Application Number:	11480269	Patent Number:	7733965	Application Number:	12178547	Application Number:	11477136	Patent Number:	7724831	Patent Number:	5726650	Patent Number:	5093847	Patent Number:	7327754	Patent Number:	5168179	Patent Number:	5189664	Patent Number:	5533053	Patent Number:	7009603	Patent Number:	7158573	Patent Number:	7317732
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PATENT
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Patent Number:	7102556
Patent Number:	6795450
Patent Number:	7292597
Patent Number:	6943714
Patent Number:	7317691
Patent Number:	5214641
Patent Number:	5561660
Patent Number:	5345118
Patent Number:	5654984
Patent Number:	5602912
Patent Number:	5500894
Patent Number:	7352216
Patent Number:	7408996
PCT Number:	US0949279

CORRESPONDENCE DATA

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NAME OF SUBMITTER:	Michael D. Wiggins
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TO: NORTH WEBER & BAUGH LLP COMPANY: 2479 E. BAYSHORE RD.

PATENT ASSIGNMENT

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SUBMISSION TYPE:	NEW ASSIGNMENT
NATURE OF CONVEYANCE:	MERGER
EFFECTIVE DATE:	04/09/2010

CONVEYING PARTY DATA

Name	Execution Date
Teridian Semiconductor Corporation	04/09/2010

RECEIVING PARTY DATA

Name:	Maxim Integrated Products, Inc.
Street Address:	120 San Gabriel Drive
City:	Sunnyvale
State/Country:	CALIFORNIA
Postal Code:	94086

PROPERTY NUMBERS Total: 28

Property Type	Number
Patent Number:	5728650
Patent Number:	5083847
Patent Number:	7327754
Patent Number:	5168179
Patent Number:	5189684
Patent Number:	5533053
Patent Number:	7009603
Patent Number:	7158573
Patent Number:	7317732
Patent Number:	7102558
Patent Number:	6785450
Patent Number:	7292597
Patent Number:	6943714
Patent Number:	7317691

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Patent Number:	5214841
Patent Number:	5581880
Patent Number:	5345118
Patent Number:	5654884
Patent Number:	5802912
Patent Number:	5500884
Application Number:	11480289
Application Number:	11477130
Patent Number:	7352218
Application Number:	12178547
Application Number:	11477138
Application Number:	11477129
Patent Number:	7408996
PCT Number:	US0949279

CORRESPONDENCE DATA

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Address Line 4: Palo Alto, CALIFORNIA 94303

ATTORNEY DOCKET NUMBER: TERRIDAN/MAXIM MERGER

NAME OF SUBMITTER: Aimee Marth

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AGREEMENT AND PLAN OF MERGER

THIS AGREEMENT AND PLAN OF MERGER (the "Agreement") is made as of April 9, 2010, by and among Maxim Integrated Products, Inc., a Delaware corporation (the "Purchaser"), MIP Tropic, Inc., a Delaware corporation and a wholly-owned subsidiary of the Purchaser (the "Merger Sub"), Teridian Semiconductor Holdings Corporation, a Delaware corporation (the "Company"), GGC Services Holdeo, LLC, not individually, but solely in its capacity as the representative of the Securityholders (the "Stockholder Representative") and Teridian Semiconductor Corporation, a California corporation, solely for purposes of Article 5 hereof (the "OpCo"). Except as otherwise set forth herein, capitalized terms used herein have the meanings set forth in Exhibit A.

WHEREAS, the respective boards of directors of the Purchaser, the Merger Sub and the Company have deemed it advisable and in the best interests of their respective corporations and stockholders that the Purchaser and the Company consummate the business combination and other transactions contemplated by this Agreement; and

WHEREAS, the respective boards of directors of the Merger Sub and the Company have authorized and approved the merger of the Merger Sub with and into the Company in accordance with the provisions of this Agreement.

NOW, THEREFORE, intending to be legally bound and in consideration of the mutual provisions set forth in this Agreement and for other good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, the parties agree as follows:

ARTICLE 1 THE MERGER

1.1 The Merger. In accordance with the provisions of this Agreement, at the Effective Time, the Merger Sub will be merged (the "Merger") with and into the Company in accordance with the provisions of the Delaware General Corporation Law ("DGCL"). Following the Merger, the Company will continue as the surviving corporation and a wholly-owned subsidiary of the Purchaser (the "Surviving Corporation"), and the separate corporate existence of the Merger Sub will cease.

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

3.13 Intellectual Property.

(a) Company Intellectual Property. The Company owns or otherwise has valid and legally enforceable rights to use the Intellectual Property owned, created, acquired, licensed, used or held for use by the Company at any time prior to and through the Closing Date (the "Company Intellectual Property").

(i) Owned Intellectual Property. Section 3.13(a)(i) of the Company Disclosure Schedule sets forth an accurate list of the following items of the Owned Intellectual

Property: (A) patents and patent applications [REDACTED]

[REDACTED] Except as specifically set forth on Section 3.13(a)(i) of the Company Disclosure Schedule, Company has valid title to the Owned Intellectual Property and is the sole and exclusive owner of the Owned Intellectual Property. The Company has not permitted the Company's rights in such Owned Intellectual Property to enter into the public domain.

[REDACTED]

[REDACTED]

[REDACTED]

(c) Effect of Closing. Immediately after the Closing, the Company will be the sole owner of, and will have valid title to, the Owned Intellectual Property, and will have the full right to use, license and transfer the Company Intellectual Property in the same manner and on the same terms that the Company had immediately prior to the Closing. Except as set forth in any Material Contract, the Company is not legally bound by any such Material Contract or other obligation under which the occurrence of the Closing (in and of itself) would (i) obligate the Company or Purchaser to license, or otherwise grant rights to any other Person in, any Intellectual Property (whether owned or used by the Company or the Purchaser), (ii) entitle any Person to a release of any source code escrow, (iii) result in an Encumbrance or new or incremental restriction on the Company Intellectual Property, (iv) result in the Company being bound by or subject to any non-compete or other material restriction on the operation of

the business of the Company or its Subsidiaries, (v) obligate Company or any of its Subsidiaries to pay any royalties or other material amounts, or offer any discounts, to any third Person in excess of those payable by, or required to be offered by, any of them, respectively, in the absence of this Agreement or the transactions contemplated hereby, or (vi) otherwise increase any burdens or decrease any rights relating to the Company Intellectual Property.

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

[REDACTED]

IN WITNESS WHEREOF, the parties hereto have executed this Agreement as of the date first set forth above.

"Purchaser"

MAXIM INTEGRATED PRODUCTS, INC.,
a Delaware corporation

By: 

Name: Edwin Medlin

Title: Vice President, Senior Counsel

"Merger Sub"

MIP TROPIC, INC., a Delaware corporation

By: 

Name: Mark Casper

Title: President

[Signature pages continued on next page]

[Continuation of signature pages]

"Company"

TERIDIAN SEMICONDUCTOR HOLDINGS
CORPORATION, a Delaware corporation

By: [Signature]
Name: GERALD FITCH
Title: VICE PRESIDENT

"OpCo "

Solely for purposes of Article 5 herein

TERIDIAN SEMICONDUCTOR
CORPORATION, a California corporation

By: [Signature]
Name: GERALD FITCH
Title: PRESIDENT, CHIEF EXECUTIVE OFFICER

"Stockholder Representative"

*Solely for the purposes related to the Stockholder
Representative as set forth herein*

GGC SERVICES HOLDCO, LLC, a limited
liability company organized under the laws of
Delaware

By: _____
Name: _____
Title: _____

(solely in its capacity as Stockholder
Representative)

[End of signature pages]

Section 3.13(a)(i) - Owned Intellectual Property

Settlement Agreement by and between TDK Semiconductor Corp. and Silicon Laboratories, Inc., dated 4/1/2003

A. PATENTS

Record Owner	Title	Jurisdiction	Applic. No/ Filing Date	Pub. No/ Pub. Date	Patent No. Issue Date	Status
Teridian Semiconductor Corp.	Adaptive Manchester decoding with adjustable delay and power saving mode	U.S.	08/479613 6/7/1995		5726650 3/10/1998	Patented
Teridian Semiconductor Corp.	Adaptive phase lock loop	U.S.	07/633477 12/21/1990		5093847 3/3/1992	Patented
Teridian Semiconductor Corp.	Apparatus and method for freezing the states of a receiver during silent line state operation of a network device	U.S.	10/263461 10/2/2002		7327754 2/5/2008	Patented
Teridian Semiconductor Corp.	Balanced modulator for auto zero networks	U.S.	07/769338 10/1/1991		5168179 12/1/1992	Patented
Teridian Semiconductor Corp.	Listener echo cancellation	U.S.	07/657535 2/19/1991		5189664 2/23/1993	Patented
Teridian Semiconductor Corp.	Low frequency common mode rejection in a clock circuit	U.S.	08/243563 5/16/1994		5533053 7/2/1996	Patented
Teridian Semiconductor Corp.	Method and apparatus for driving light emitting polymer displays	U.S.	10/259234 9/27/2002		7009603 3/7/2006	Patented
Teridian Semiconductor, Corp.	Method and apparatus for full duplex signaling across a transformer	U.S.	10/857469 5/28/2004		7158573 1/2/2007	Patented

Record Owner	Title	Jurisdiction	Applic. No./ Filing Date	Pub. No./ Pub. Date	Patent No. Issue Date	Status
Teridian Semiconductor, Corp.	Method and apparatus for handling link suspend pulse and silent line state transitions of a network device	U.S.	10/263465 10/2/2002		7317732 1/8/2008	Patented
Teridian Semiconductor, Corp.	Method and apparatus for obtaining power computation parameters	U.S.	11/084713 3/18/2005		7102556 9/5/2006	Patented
Teridian Semiconductor, Corp.	Method and apparatus for supporting physical layer link-suspend operation between network nodes	U.S.	09/676040 9/28/2000		6795450 9/21/2004	Patented
Teridian Semiconductor, Corp.	Method and apparatus for transparent implementation of link-suspend capabilities in network devices	U.S.	10/263365 10/2/2002		7292597 11/6/2007	Patented
Teridian Semiconductor, Corp.	Method and apparatus of obtaining power computation parameters	U.S.	10/637969 9/13/2005		6943714 8/7/2003	Patented
Teridian Semiconductor, Corp.	Method for initializing a link suspend device for optimum receive recovery	U.S.	10/263464 10/2/2002		7317691 1/8/2008	Patented
Teridian Semiconductor, Corp.	Mixed analog digital secondary channel FSK modem	U.S.	07/758347 9/9/1991		5214641 5/25/1993	Patented

Record Owner	Title	Jurisdiction	Applic. No./ Filing Date	Pub. No./ Pub. Date	Patent No. Issue Date	Status
Teridian Semiconductor, Corp.	Offset and phase correction for delta- sigma modulators	U.S.	08/416614 4/5/1995		5561660 10/1/1996	Patented
Teridian Semiconductor, Corp.	Precision MOS resistor	U.S.	07/873948 4/23/1992		5345118 9/6/1994	Patented
Teridian Semiconductor, Corp.	Signal modulation across capacitors	U.S.	08/161741 12/3/1993		5654984 8/5/1997	Patented
Teridian Semiconductor, Corp.	Telephone hybrid circuit	U.S.	08/591209 1/17/1996		5602912 2/11/1997	Patented
Teridian Semiconductor, Corp.	Telephone line interface with AC and DC transconductance loops	U.S.	08/161740 12/3/1993		5500894 3/19/1996	Patented
Teridian Semiconductor Corp.	Isolated current sensor	EPO	09790013.8 6/30/2009			Pending
Teridian Semiconductor Corp.	Active rectifier with load impedance switching	U.S.	11/480269 6/29/2006	2007001437 4 1/18/2007		Pending
Teridian Semiconductor Corp.	Encoding method and apparatus for a dual mode super source follower circuit	U.S.	11/477130 6/27/2006	2007001437 2 1/18/2007		Pending Claims 1-20 have been allowed, but continuation has been filed in an attempt to recover the other claims.
Teridian Semiconductor Corp.	High speed ramp generator	U.S.	11/476922 6/27/2006		7352216 4/1/2008	Patented
Teridian Semiconductor Corp.	Isolated current sensor	U.S.	12/178547 7/23/2008	2010002328 3 1/28/2010		Pending

Record Owner	Title	Jurisdiction	Applic. No./ Filing Date	Pub. No./ Pub. Date	Patent No. Issue Date	Status
Teridian Semiconductor Corp.	Method and apparatus for cancellation of magnetizing inductance current in a transformer circuit	U.S.	11/477136 6/27/2006	2007001437 3 1/18/2007		Pending
Teridian Semiconductor Corp.	Method and apparatus for detection of load impedance modulation in a transformer circuit	U.S.	11/477129 6/27/2006	2007001437 1 1/18/2007		Pending Examiner has allowed all claims except 3,8,13, and 19, but a continuation has been filed to recover these claims
Teridian Semiconductor Corp.	Method and apparatus for synchronization of data in a transformer circuit	U.S.	11/476923 6/27/2006		7408996 8/5/2008	Patented
Teridian Semiconductor Corp.	Isolated current sensor	WIPO (PCT)	PCT/US200 9/049279 6/30/2009	WO 2010/11475 1/28/2010		Pending