PATENT ASSIGNMENT

Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE: NEW ASSIGNMENT

NATURE OF CONVEYANCE: ASSIGNMENT

CONVEYING PARTY DATA

	Name	Execution Date
T	oshiba America Electronic Components, Inc.	06/05/2010

RECEIVING PARTY DATA

Name:	Kabushiki Kaisha Toshiba
Street Address:	1-1 Shibaura 1-chome
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PROPERTY NUMBERS Total: 1

Property Type	Number
Application Number:	11409090

CORRESPONDENCE DATA

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NAME OF SUBMITTER: Christopher R. Glembocki

Total Attachments: 1

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PATENT REEL: 024625 FRAME: 0077 CH \$40.00 11409

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<u>ASSIGNMENT</u>

We, Toshiba America Electronic Components, Inc., a corporation organized under the laws of California, having a place of business at 19900 MacArthur Boulevard, Suite 400, Irvine, CA 92612, U.S.A., the assignee of the following U.S. Patent Application:

Serial No.:

11/409,090

Entitled:

Stress Relaxation for Top of Transistor Gate

hereby assign the entirety of our right, title and interest in said U.S. Patent Application, including all divisions, reissues, continuations, substitutions and extensions thereof and all rights arising under or pursuant to any and all international agreements, treaties or laws relating to the protection of industrial property, including rights of priority, resulting from the filing of any said application, to KABUSHIKI KAISHA TOSHIBA, a Japanese corporation, located at 1-1 Shibaura 1-chome, Minato-ku, Tokyo, Japan, their successors, assigns, or other legal representatives.

The undersigned is authorized to sign this document on behalf of the corporation.

Toshiba America Electronic Components, Inc. 615/13 By: Date: Name: Hideya Sakaida

Title: President

PATENT RECORDED: 07/01/2010 REEL: 024625 FRAME: 0078