Electronic Version v1.1 Stylesheet Version v1.1

SUBMISSION TYPE: NEW ASSIGNMENT

NATURE OF CONVEYANCE: ASSIGNMENT

CONVEYING PARTY DATA

	Name	Execution Date
PHOENIX TECHNOLOGIES LTD.		06/15/2010

RECEIVING PARTY DATA

Name:	HEWLETT-PACKARD COMPANY	
Street Address:	3000 Hanover Street	
City:	Palo Alto	
State/Country:	CALIFORNIA	
Postal Code:	94304	

PROPERTY NUMBERS Total: 13

Property Type	Number
Application Number:	12217240
Application Number:	12322670
Application Number:	12315435
Application Number:	12317639
Application Number:	12229705
Application Number:	12317646
Application Number:	12317648
Application Number:	12544900
Application Number:	12542553
Application Number:	12605290
Application Number:	12549294
Application Number:	12459953
Application Number:	12459963

CORRESPONDENCE DATA

PATENT REEL: 024721 FRAME: 0319 *H \$520 00 1

501239540

Fax Number: (970)778-4063

Correspondence will be sent via US Mail when the fax attempt is unsuccessful.

Phone: 970 898-4728

Email: jerry.shorma@hp.com

Correspondent Name: Hewlett-Packard Company

Address Line 1: 3404 East Harmony Rd

Address Line 2: Intellectual Property Administration
Address Line 4: Ft. Collins, COLORADO 80528

NAME OF SUBMITTER:

Lynette DeBrey-Cota

Total Attachments: 4

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PATENT ASSIGNMENT

THIS PATENT ASSIGNMENT, effective as of June 15, 2010, between Phoenix Technologies Ltd., a Delaware corporation (hereinafter referred to as ASSIGNOR), with offices at 915 Murphy Ranch Rd., Milpitas, California 95035, and Hewlett Packard Company, a Delaware corporation (hereinafter referred to as ASSIGNEE), with offices at 3000 Hanover Street, Palo Alto, California 94304;

WHEREAS, Assignor and Assignee have entered into that certain Asset Purchase Agreement dated as of June 4, 2010, (the "Agreement"). Subject to the terms and conditions of the Agreement, Assignor wishes to assign the Patents (as defined below) to Assignee.

WHEREAS, ASSIGNOR has acquired all right, title and interest in and to certain inventions, improvements and patent rights relating to the patents listed in Exhibit A attached hereto; and

WHEREAS, ASSIGNEE is desirous of acquiring ASSIGNOR's entire right, title and interest in and to the inventions, improvements and patent rights;

NOW THEREFORE, ASSIGNOR, for good and valuable consideration, the receipt and sufficiency of which is hereby acknowledged, does hereby sell, assign, convey, and transfer unto ASSIGNEE all right, title and interest in and to the patents listed in Exhibit A attached hereto, together with all corresponding foreign applications and patents which claim priority therefrom (collectively, the "Patents"); and hereby agrees that ASSIGNOR will, at ASSIGNEE'S reasonable request, sign all lawful papers, including, without limitation, all divisional, continuation, renewal, extension and reissue applications, and make all rightful oaths in execution thereof, and will take such further action, at ASSIGNEE'S expense, that ASSIGNEE may reasonably request to aid ASSIGNEE, its successors, assigns, and nominees to obtain and enforce proper protection for the Patents in all countries, this obligation to be binding upon ASSIGNOR and its legal successor.

[Signature Page Follows]

IN WITNESS WHEREOF, ASSIGNOR has caused this agreement to be executed this $15^{\rm th}$ day of June, 2010.

PHOENIX TECHNOLOGIES LTD.

By:

Name: Timothy C. Chu

Title: Vice President, General Counsel and Secretary

[Signature Page to Patent Assignment]

EXHIBIT A

Title of Invention	Patent Number/	Country
	Application Number	
HyperSpace Patent Applications		
Memory Management for	12/217,240	US
Hypervisor Loading		
Memory Management for	PCT/IB2009/052750	PCT
Hypervisor Loading		
Memory Management for	098122469	Taiwan
Hypervisor Loading		
Network Connection Manager	61/204,598	US
Network Connection Manager	12/322,670	US
Network Connection Manager	PCT/IB2010/050050	PCT
Input-Output Virtualization	12/315,435	US
Technique		
Input-Output Virtualization	98141186	Taiwan
Technique		
Content Based Virtualization	12/317,639	US
Hypervisor Security Using SMM	12/229,705	US
Interrupt Request and Message	12/317,646	US
Signaled Interrupt Logic for		
Passthru Processing		
DMA Compliance by Remapping in	12/317,648	US
Virtualization		
Hierarchical Power Management	61/204,613	US
Hierarchical Power Management	12/544,900	US
Virtual Hotplug Techniques	61/215,733	US
Virtual Hotplug Techniques	12/542,553	US
Hierarchical Power Management	12/605,290	US
with Hot Mode		
File System for Dual Operating	12/549,294	US
Systems		
Flip Patent Applications		
Loading Operating Systems Using	61/210,578	US
Memory Segmentation and ACPI		
Based Context Switch		
Loading Operating Systems Using	12/459,953	US
Memory Segmentation and ACPI		
Based Context Switch		
Loading Operating Systems Using	PCT/US2010/027430	PCT
Memory Segmentation and ACPI		
Based Context Switch		

Title of Invention	Patent Number/ Application Number	Country
Inter Operating System Memory Hotswap to Support Memory Growth in a Non-Virtualized System	12/459,963	US
Inter Operating System Memory Hotswap to Support Memory Growth in a Non-Virtualized System	PCT/US2010/027428	PCT

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RECORDED: 07/21/2010