PATENT ASSIGNMENT

Electronic Version v1.1
Stylesheet Version v1.1

SUBMISSION TYPE:	CORRECTIVE ASSIGNMENT
NATURE OF CONVEYANCE:	Corrective Assignment to correct the ASSIGNOR NAME previously recorded on Reel 018022 Frame 0941. Assignor(s) hereby confirms the ASSIGNOR NAME IS FRANCE TELECOM S.A., NOT FRANCE TELECOM INC.

CONVEYING PARTY DATA

Name	Execution Date
France Telecom S.A.	12/03/2004

RECEIVING PARTY DATA

Name:	Fahrenheit Thermoscope LLC
Street Address:	2215-B RENAISSANCE DRIVE
Internal Address:	SUITE 5
City:	LAS VEGAS
State/Country:	NEVADA
Postal Code:	89119

PROPERTY NUMBERS Total: 10

Property Type	Number
Patent Number:	5555272
Patent Number:	5473610
Patent Number:	5473713
Patent Number:	5539760
Patent Number:	5502591
Patent Number:	5526833
Patent Number:	5749085
Patent Number:	5640366
Patent Number:	5885711
Patent Number:	5729487

CH $400.00 5555272

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Address Line 2:	Suite 800
Address Line 4:	Austin, TEXAS 78701

ATTORNEY DOCKET NUMBER:	5957-05500
NAME OF SUBMITTER:	Dean M. Munyon

Total Attachments: 10
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06-16-2006

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FORM PTO-1595
1-31-92

U.S. DEPARTMENT OF COMMERCE
Patent and Trademark Office

To the Honorable Commissioner of Patents and Trademarks. Please record the attached original documents or copy thereof.

1. Name of conveying party(ies) and execution date(s):

~~France Telecom Inc.~~ December 3, 2004
correct: France Telecom S.A.
Additional name(s) of conveying party(ies) attached? ☐ Yes ☒ No

2. Name and address of receiving party(ies):

Name: Fahrenheit Thermoscope, LLC
Street Address 2215-B Renaissance Dr, Ste 5
City Las Vegas State Nevada ZIP 89119
Additional name(s) & address(es) attached? ☐ Yes ☒ No

3. Nature of Conveyance:

☒ Assignment
☐ Merger
☐ Security Agreement
☐ Change of Name
☐ Other _____

4. Application number(s) or patent number(s):

A. Patent Application No.(s)

SEE ATTACHED

B. Patent No.(s)

Additional numbers attached? ☒ Yes ☐ No

595. Name and address of party to whom correspondence concerning document should be mailed:

Name: B. Noël Kivlin
Internal Address: Meyertons, Hood, Kivlin, Kowert & Goetzel, P.C.
Street Address: P.O. Box 398
City Austin State TX ZIP 78767-0398

6. Total number of applications & patents involved: 46

7. Total fee (37 CFR 3.41): .....$ 1840
☐ Fee authorization form enclosed
☒ Authorized to be charged to deposit account

8. Deposit account number: 501505/5957-05500
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To the best of my knowledge and belief, the foregoing information is true and correct and any attached copy is a true copy of the original document.

B. Noël Kivlin — Name of Person Signing
Reg. No. 33,929

Signature

6-9-06 — Date

Total number of pages comprising cover sheet: 2

OMB No. 0651-011 (exp.4/94)

PTO/SB/35 (11-00)
Approved for use through 10/31/2002. OMB 0651-003

(continued). Application number(s) or patent number(s):

Patent Application No.(s)

07/699,600
08/264,199
07/792,262
07/764,211
07/764,196
07/836,248
07/832,078
07/866,342
07/910,618
07/911,013
08/231,024
08/228,479
08/279,546
08/299,663
~~08/385,953~~
08/377,472
08/492,591
08/564,120
08/527,137
08/569,819
08/639,083
08/603,620
08/674,675
09/462,716
09/102,849
~~09/094,632~~
09/384,362
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09/936,487
09/979,283
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09/540,188
09/553,916
10/019,170
09/518,944
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09/980,027
10/018,680
09/260,896
10/168,041
09/912,057
10/380,756
10/332,532
10/725,978

Additional numbers attached? ☐ Yes ☒ No

ASSIGNMENT OF PATENT RIGHTS

For good and valuable consideration, the receipt of which is hereby acknowledged, France Telecom S.A. having offices at France Telecom R&D, 38-40 rue du Général Leclerc, 92794 Issy les Moulineaux Cedex 9, France (which France Telecom S.A. is the transformation of *France Telecom, Etablissement autonome de droit Public*, 6, Place d'Alleray, F-75015 Paris, France, by virtue of French law 96-660 of July 26, 1996), ("***Assignor***"), does hereby sell, assign, transfer and convey unto Fahrenheit Thermoscope LLC a Delaware limited liability company, having an office at 2215-B Renaissance Drive, Suite 5, Las Vegas, NV 89119 ("***Assignee***") or its designees, all of Assignor's right, title and interest in and to the patent applications and patents listed below, any patents, registrations, or certificates of invention issuing on any patent applications listed below, the inventions disclosed in any of the foregoing, any and all counterpart United States, international and foreign patents, applications and certificates of invention based upon or covering any portion of the foregoing, and all reissues, re-examinations, divisionals, renewals, extensions, provisionals, continuations and continuations-in-part of any of the foregoing (collectively "***Patent Rights***"):

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Filing Number	Country Code	Filing Date	Page 1 of 7 Title / Inventors (US Patent)
699,600	US	14/05/1991	Method for manufacturing an active matrix display screen with storage capacitors / Morin, Le Contellec
264,199	US	22/06/1994	Active matrix display screen with storage capacitors formed of conductive blocks, semiconductive material, nonconductive material, and capacitive lines / Morin, Le Contellec
792,262	US	15/11/1991	Method for etching integrated-circuit layers to a fixed depth and corresponding integrated circuit / Haond
91403055.6	DE	14/11/1991	
90 14306	FR	16/11/1990	
91403055.6	FR/EP	14/11/1991	
91403055.6	GB	14/11/1991	
764,211	US	23/09/1991	Protective circuit for a control circuit, in particular of liquid crystal display screen / Senn, Lelah, Martel
764,196	US	23/09/1991	Sample and hold circuit for a liquid crystal display screen/ Senn, Lelah, Martel
836,248	US	18/02/1992	Collector of a bipolar transistor compatible with MOS technology / Nouailhat, Bois
92420047.0	EP	12/02/1992	
91 01984	FR	13/02/1991	
832,078	US	06/02/1992	Process for buried localized oxidation of a silicon substrate and corresponding integrated circuit / Straboni, Barla, Wuillermoz
91 01381	FR	07/02/1991	
866,342	US	10/04/1992	Process for the production of thin film transistors / Chouan
910,618	US	08/07/1992	Method of manufacturing a vertical field effect transistor / Chantre, Bois, Nouailhat
92401909.4	DE	03/07/1992	
92401909.4	GB	03/07/1992	
91 08677	FR	10/07/1991	
911,013	US	09/07/1992	Active matrix, high definition, liquid crystal display structure / Morin, Chouan, Vinouze
92401987.0	GB	09/07/1992	
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231,024	US	20/04/1994	Method of clock signal recovery and of synchronization for the reception of information elements transmitted by an ATM network and device for the implementationofthe method / Rainard
94400853.1	DE	19/04/1994	
93 04775	FR	22/04/1993	
94400853.1	GB	19/04/1994	
228,479	US	15/04/1994	Signal processing device using several different filterings, especially for audio-frequency coding of voice signals / Balestro, Senn
94400780.6	DE	11/04/1994	
93 04532	FR	16/04/1993	
94400780.6	GB	11/04/1994	
279,546	US	25/07/1994	Optical amplifier having a doped fluoride glass optical fibre and process for producing this amplifier / RonarcH, Guibert, Houmed
94401706.0	GB	25/07/1994	
93 09165	FR	26/07/1993	
94401706.0	DE	25/07/1994	
94401706.0	IT	25/07/1994	
299,663	US	02/09/1994	Process for the transposition of an optical modulation of one wavelength to another adjustable wavelength / Dupont, Auffret, Tromeur
94401984.3	GB	07/09/1994	
94401984.3	DE	07/09/1994	
93 10737	FR	09/09/1993	
385,953	US	09/02/1995	Degreasing device particularly for optical fibers /Crespel, Cailleaux, Caudrelier
94 01673	FR	11/02/1994	
95400232.5	DE	03/02/1995	
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377,472	US	24/01/1995	Optical amplifier with a doped fluoride glass of optical fibre and process for producing said amplifier / Semenkoff, RonarcH, Guibert
492,591	US	20/06/1995	Electronic memory addressing device especially for a memory organized into banks / Quillevere, Dufal
95401444.5	DE	20/06/1995	
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Filing Number	Country Code	Filing Date	Title / Inventors (US Patent)
564,120	US	30/03/1995	Electrically conductive polymer compositions, production process and coated substrates / Clarisse, Delabouglise, Ciprelli
95915230.7	GB	30/03/1995	
95915230.7	DE	30/03/1995	
94 03812	FR	31/03/1994	
527,137	US	12/09/1995	Sequential-access asynchronous memory device and corresponding process for storage and reading / Majos, Weil
95402213.3	DE	04/10/1995	
94 12170	FR	12/10/1994	
95402213.3	GB	04/10/1995	
569,819	US	08/12/1995	Electronic component capable, in particular, of performing a division of two numbers to the base 4 / Dufal, Robert
94 15504	FR	22/12/1994	
95402809.8	DE	13/12/1995	
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639,083	US	24/04/1996	Device for programmable delay of an analog signal and corresponding acoustic antenna / Le Tourneur, Balestro
96400851.0	FR/EP	22/04/1996	
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95 04918	FR	25/04/1995	
08/603,620	US	21/02/1996	Minimizing program code storage for performing regular and repetitive operations in a programmable processor / Dufal, Privat
96400333.9	DE	19/02/1996	
95 02131	FR	23/02/1995	
96400333.9	GB	19/02/1996	
674,675	US	02/07/1996	Apparatus and a method for identifying and splicing multicore fibers / Le Noane, Perrin, Le Marer
9614093.4	GB	04/07/1996	
95 08042	FR	04/07/1995	
09/462,716	US	08/07/1998	Method of minimising the corner effect by densifying the insulative layer / Schiavonne, Gaillard
2000-502,539	JP	08/07/1998	
98936480.7	GB	08/07/1998	
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09/102,849	US	23/06/1998	Process for obtaining a transistor having a silicon-germanium gate / Sagnes
98401432.4	EP	12/06/1998	
97 07938	FR	25/06/1997	
178,749	JP	25/06/1998	
094,632	US	15/06/1998	High-density and high-capacity distribution frame for optical fibers / Lanjepoe, Blanchard, Gueguen
98401410.0	EP	11/06/1998	
97 07892	FR	20/06/1997	
09/384,362	US	26/08/1999	Instrument for measuring the near-end crosstalk per unit length of multicore fibers / Boscher, Bizeul, Leve
99402120.2	EP	25/08/1999	
98 10768	FR	27/08/1998	
09/289,667	US	12/04/1999	Electrooptic method of signal processing, device for implementing the latter, and use / Ferrieu
99400852.2	EP	08/04/1999	
104,365/99	JP	12/04/1999	
98 04557	FR	10/04/1998	
09/936,487	US	07/03/2000	Process for testing integrated circuits with acces to the memory points of the circuit / Barthel
99 02823	FR	08/03/1999	
89104000	TW	07/03/2000	
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09/979,283	US	18/05/2000	Complex number multiplier / Montalvo, Arndt
99 06425	FR	20/05/1999	
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00929626.0	IT	18/05/2000	
09/511,330	US	23/02/2000	Dual mode radio frequency reception device and corresponding multimedia receiver / Andre, Senn
99 03769	FR	23/03/1999	
00460016.9	EP	24/02/2000	
09/540,188	US	31/03/2000	Process for fabricating a planar heterostructure / Hernandez, Campidelli, Rivoire, Bensahel
99 04052	FR	31/03/1999	
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Filing Number	Country Code	Filing Date	Page 5 of 7 Title / Inventors (US Patent)
09/553,916	US	20/04/2000	Integrated circuit device comprising an inductor with high quality coefficient / Merckel, Pons, Senn, Fournier
99 04986	FR	20/04/1999	
00401086.4	EP	19/04/2000	
118,658	JP	19/04/2000	
10/019,170	US	23/06/2000	Method for compensating non-linearity of a sigma-delta analog-to-digital converter / Morche
99 08323	FR	29/06/1999	
09/518,944	US	06/03/2000	Radiofrequency transmitter with a high degree of integration and possibly with self-calibrating image deletion / Andre
00460019.3	EP	02/03/2000	
99 03768	FR	23/03/1999	
09/559,524	US	28/04/2000	Frequency control system that stabilizes an output through both a counter and voltage-controlled oscillator via sampling a generated clock into four states / Majos
00401100.3	EP	19/04/2000	
99 05627	FR	30/04/1999	
129,903	JP	28/04/2000	
09/505,448	US	16/02/2000	Process for the anisotropic etching of an organic dielectric polymer material by a plasma gas and application in microelectronics / Joubert, Fuard
99 01925	FR	17/02/1999	
030,132	JP	08/02/2000	
00400363.8	EP	09/02/2000	
10/018,179	US	05/06/2000	Semiconductor device with compensated threshold voltage and method for making the same / Skotnicki, Gwoziecki
00938886.9	EP	05/06/2000	
99 07391	FR	11/06/1999	
09/980,027	US	26/05/2000	Band-pass filter with carrier frequency reduction/ Morche
00936952.1	GB	26/05/2000	
00936952.1	IT	26/05/2000	
99 06710	FR	27/05/1999	
10/018,680	US	08/06/2000	Method for making a silicon substrate comprising a buried thin silicon oxide film / Jurczak, Skotnicki
99 07496	FR	14/06/1999	
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F5

Filing Number	Country Code	Filing Date	Page 6 of 7 Title / Inventors (US Patent)
09/620,896	US	21/07/2000	Method of correcting topographical effects on a micro-electronic substrate / Schiltz, Paoli, Schiavone, Prola
219,525	JP	19/07/2000	
2000-0042258	KR	22/07/2000	
99 09521	FR	22/07/1999	
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10/168,041	US	08/06/2001	Low-noise spectroscopic ellipsometer / Ferrieu
00 07425	FR	09/06/2000	
01945381.0	EP	08/06/2001	
2002-502,401	JP	08/06/2001	
09/912,057	US	25/07/2001	Resin, a double resin layer for extreme ultraviolet (EUV) photolithography, and an extreme ultraviolet light (EUV) photolithography process / Schiltz
01401972.3	EP	23/07/2001	
00 09759	FR	26/07/2000	
10/380,756	US	18/09/2001	Device for punctual measurement of a radiofrequency magnetic field with constant amplitude and frequency / Bouvier, Geoffroy
00 11923	FR	19/09/2000	
01969920.6	DE	18/09/2001	
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528842/2002	JP	18/09/2001	
01969920.6	GB	18/09/2001	
10/332,532	US	10/07/2001	Saturable absorbent structure, in particular for regenerating optical component and method for making same / Marceaux, Loualiche, Le Corre, Dehaese
00 09030	FR	11/07/2000	
01954065.7	EP	10/07/2001	
2002-509824	JP	10/07/2001	
2,416,127	CA	10/07/2001	
01 03829	FR	21/03/2001	Method and device for transmitting a video sequence comprising a face, especially in a mobile videophone system / Roux, Petit
02716894.7	EP	14/03/2002	

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Filing Number	Country Code	Filing Date	Page 7 of 7 Title / Inventors (US Patent)
10/725,978	US	24/05/2002	Large capacity automatic distributor, particularly for optic fibres; device and method for automatic connection/ disconnection of the binding fibres/ Morellec, Le Traon, Bouchet
02 06477	FR	29/05/2001	
01 07032	FR	29/05/2001	
02747487.3	EP	24/05/2002	
2003-500628	JP	24/05/2002	

IN WITNESS WHEREOF this Assignment of Patent Rights is executed at Issy les Moulineaux on December 3rd, 2004

ASSIGNOR

By: France Telecom

Name: François JAMET

Title: Directeur de la Propriété Industrielle et de la Valorisation

(Signature MUST be notarized)

Je soussigné Eric de La HAYE SAINT HILAIRE
Notaire à Paris, Certifie véritable
la signature de M. François JAMET
apposée ci-contre
Paris, le 3 décembre 2004